

Vishay Semiconductors

RoHS COMPLIANT

HALOGEN

FREE

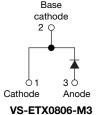
Hyperfast Rectifier, 8 A FRED Pt®



2L TO-220AC



2L TO-220 FULL-PAK





VS-ETX0806FP-M3

PRODUCT SUMMARY					
Package	2L TO-220AC, 2L TO-220FP				
I _{F(AV)}	8 A				
V_{R}	600 V				
V _F at I _F	1.5 V				
t _{rr} (typ.)	14 ns				
T _J max.	175 °C				
Diode variation	Single die				

FEATURES

- Hyperfast recovery time, extremely low Q_{rr}
- Low forward voltage drop
- 175 °C operating junction temperature
- · Low leakage current
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- True 2 pin package
- · Designed and qualified according to JEDEC®-JESD 47
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	V_{RRM}		600	V			
A construction of the control of the	I _{F(AV)}	T _C = 142 °C	- 8	А			
Average rectified forward current in DC FULL-PAK		T _C = 105 °C	0				
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	80				
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-		
Forward voltage		I _F = 8 A	-	2.5	3.4	V	
	V _F	I _F = 8 A, T _J = 150 °C	-	1.5	2.0		
Reverse leakage current I _R		V _R = V _R rated	-	0.02	30		
		T _J = 150 °C, V _R = V _R rated	-	21	150	μA	
Junction capacitance	C _T	V _R = 600 V	-	6	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH	



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 A, dI_F/dt = 100$	$V_R = 30 V$	-	14	18	
Reverse recovery time		$I_F = 8 \text{ A}, dI_F/dt = 100$	$V_R = 30 V$	-	15	24	ns
neverse recovery time	t _{rr}	T _J = 25 °C		-	17	-	
		T _J = 125 °C	$I_F = 8 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_B = 390 \text{ V}$	-	33	-	
Peak recovery current		T _J = 25 °C		=	2.6	-	А
reak recovery current	I _{RRM}	T _J = 125 °C		-	4.3	-	
Davaraa raaayan aharra	_	T _J = 25 °C	1,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	-	22	-	nC
Reverse recovery charge	Q _{rr}			-	77	-	nc
Reverse recovery time	t _{rr}		I _F = 8 A	-	26	-	ns
Peak recovery current	I _{RRM}	T _J = 125 °C	$dI_F/dt = 600 A/\mu s$	=	11	=	Α
Reverse recovery charge	Q _{rr}		V _R = 390 V	=	150	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance,	В		-	2.0	2.6	
junction to case FULL-PAK	R _{thJC}		-	4.6	5.5	
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	70	°C/W
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
\A/-:			-	2	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Maybing daying		Case style 2L TO-220AC ETX0806 Case style 2L TO-220 FULL-PAK ETX0806FF		0806	-	
Marking device				ETX0806FP		

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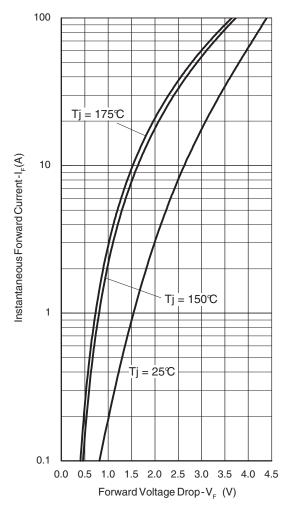


Fig. 1 - Typical Forward Voltage Drop Characteristics

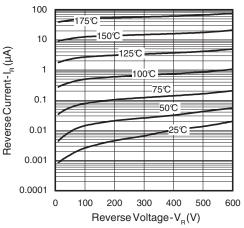


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

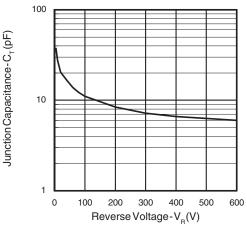


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

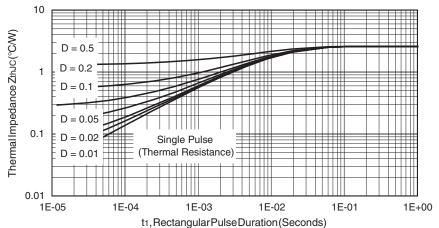


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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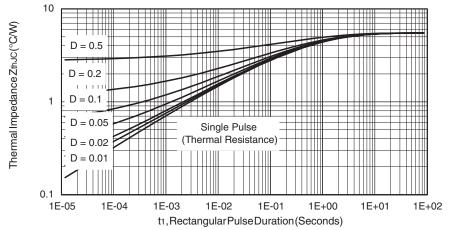


Fig. 5 - Maximum Thermal Impedance Z_{thJC} Characteristics (FULL-PAK)

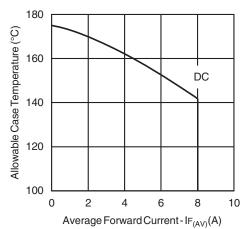


Fig. 6 - Maximum Allowable Case Temperature vs. Average Forward Current

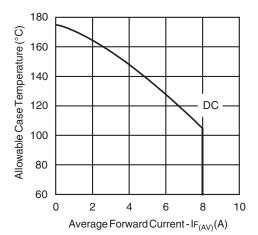


Fig. 7 - Maximum Allowable Case Temperature vs. Average Forward Current (FULL-PAK)

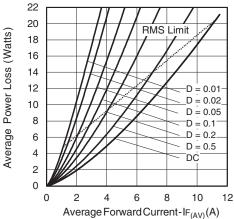


Fig. 8 - Forward Power Loss Characteristics

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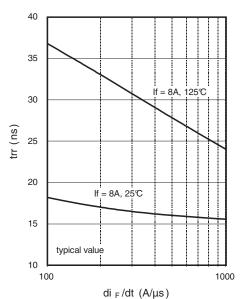


Fig. 9 - Typical Reverse Recovery vs. dl_F/dt

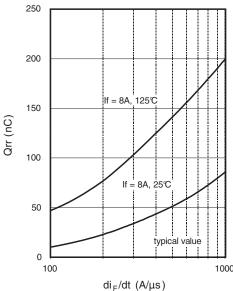


Fig. 10 - Typical Stored Charge vs. dl_F/dt

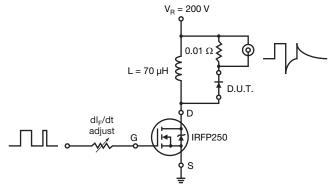
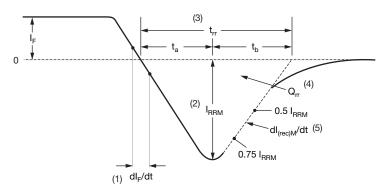


Fig. 11 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dI_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

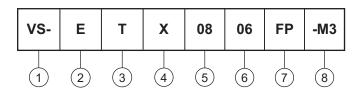
Fig. 12 - Reverse Recovery Waveform and Definitions



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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Circuit configuration:

E = single diode

3 - T = TO-220

X = hyperfast recovery time

5 - Current code: 08 = 8 A

6 - Voltage code: 06 = 600 V

7 - • None = TO-220

• FP = FULL-PAK

8 - Environmental digit:

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

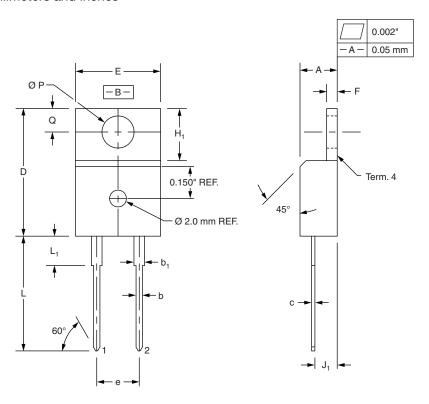
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-ETX0806-M3	50	1000	Antistatic plastic tube			
VS-ETX0806FP-M3	50	1000	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
2L TO-220AC www.vishay.com/doc?95259					
Dimensions	2L TO-220 FULL-PAK	www.vishay.com/doc?95260			
Part marking information	2L TO-220AC	www.vishay.com/doc?95391			
Fait marking information	2L TO-220 FULL-PAK	www.vishay.com/doc?95392			

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True 2 Pin TO-220

DIMENSIONS in millimeters and inches



SYMBOL -	MILLIM	METERS	INC	HES
STMBOL	MIN.	MAX.	MIN.	MAX.
А	4.32	4.57	0.170	0.180
b	0.71	0.91	0.028	0.036
b ₁	1.15	1.39	0.045	0.055
С	0.36	0.53	0.014	0.021
D	14.99	15.49	0.590	0.610
E	10.04	10.41	0.395	0.410
е	5.08	BSC	0.200	BSC
F	1.22	1.37	0.048	0.054
H ₁	5.97	6.47	0.235	0.255
J ₁	2.54	2.79	0.100	0.110
L	13.47	13.97	0.530	0.550
L ₁ (1)	3.31	3.81	0.130	0.150
Ø P	3.79	3.88	0.149	0.153
Q	2.60	2.84	0.102	0.112

Notes

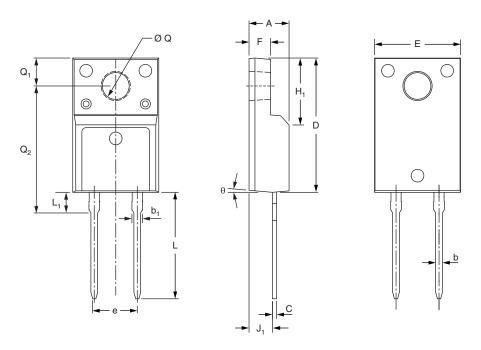
- $^{(1)}$ Lead dimension and finish uncontrolled in L_1
- These dimensions are within allowable dimensions of JEDEC TO-220AB rev. J outline dated 3-24-87
- Controling dimension: Inch



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True 2 Pin TO-220 FULL-PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLI	METERS	INC	HES
STMBOL	MIN.	MAX.	MIN.	MAX.
A	4.53	4.93	0.178	0.194
b	0.71	0.91	0.028	0.036
b ₁	1.15	1.39	0.045	0.055
С	0.36	0.53	0.014	0.021
D	15.67	16.07	0.617	0.633
E	9.96	10.36	0.392	0.408
е	5.08	typical	0.200	typical
F	2.34	2.74	0.092	0.107
H ₁	6.50	6.90	0.256	0.272
J ₁	2.56	2.96	0.101	0.117
L	12.78	13.18	0.503	0.519
L ₁	2.23	2.63	0.088	0.104
ØQ	2.98	3.38	0.117	0.133
Q ₁	3.10	3.50	0.122	0.138
Q_2	14.80	15.20	0.583	0.598
θ	0°	5°	0°	5°





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