

Vishay Siliconix

P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^d	Q _g (Typ.)		
- 30	0.0033 at V _{GS} = - 10 V	- 36	90 nC		
- 30	0.0046 at V _{GS} = - 4.5 V	- 29	90110		

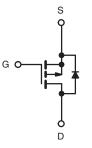
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

- · Adaptor Switch
- · High Current Load Switch
- Notebook



P-Channel MOSFET

	SO-8	
S 1 S 2 S 3 G 4		8 D 7 D 6 D 5 D
•	Top View	_

Ordering Information: Si4497DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 30	V	
Gate-Source Voltage	V _{GS}	± 20	v	
	T _C = 25 °C		- 36	
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C		- 29	
Continuous Diain Current (1) = 150 °C)	T _A = 25 °C	I _D	- 24.8 ^{a, b}	
	T _A = 70 °C		- 19.2 ^{a, b}	
Pulsed Drain Current	I _{DM}	- 70	Α	
Ocation of Ocata	T _C = 25 °C		- 6.5	
Continuous Source-Drain Diode Current	T _A = 25 °C	ls =	- 2.9 ^{a, b}	
Avalanche Current		I _{AS}	- 30	
Single-Pulse Avalanche Energy L = 0.1 mH		E _{AS}	45	mJ
	T _C = 25 °C		7.8	
Mariana Persan Dissination	T _C = 70 °C		5.0	١٨/
Maximum Power Dissipation	T _A = 25 °C	P _D	3.5 ^{a, b}	W
	T _A = 70 °C		2.2 ^{a, b}	
Operating Junction and Storage Temperature Rang	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{a, c}	t ≤ 10 s	R_{thJA}	29	35	°C/W	
Maximum Junction-to-Foot	Steady State	R _{thJF}	13	16	C/VV	

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under Steady State conditions is 80 °C/W.
- d. Based on $T_C = 25$ °C.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	-						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	In = - 250 μA		- 26		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μA		5.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zava Cata Valtana Busin Courset		V _{DS} = - 30 V, V _{GS} = 0 V			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C			- 5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	- 30			Α	
	_	V _{GS} = - 10 V, I _D = - 20 A		0.0027	0.0033	1 .	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 15 A		0.0038	0.0046	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 20 A		75		S	
Dynamic ^b				•			
Input Capacitance	C _{iss}			9685		pF	
Output Capacitance	C _{oss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		995			
Reverse Transfer Capacitance	C _{rss}			995			
Total Cata Charge	Q _g	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 20 A		190	285	nC	
Total Gate Charge				90	135		
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -20 \text{ A}$		27.5			
Gate-Drain Charge	Q _{gd}			26.5			
Gate Resistance	R _g	f = 1 MHz	0.5	2.3	4.6	Ω	
Turn-On Delay Time	t _{d(on)}			19	35		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 1.5 Ω		13	25]	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -10 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		115	200		
Fall Time	t _f			25	50	ne	
Turn-On Delay Time	t _{d(on)}			100	180	- ns - -	
Rise Time	t _r	V_{DD} = - 15 V, R_L = 1.5 Ω		75	150		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ - 10 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		100	180		
Fall Time	t _f			42	80		
Drain-Source Body Diode Characteris	stics						
Continous Source-Drain Diode Current	I _S	T _C = 25 °C			- 36		
Pulse Diode Forward Current	I _{SM}				- 70	A	
Body Diode Voltage	V_{SD}	I _S = - 3 A, V _{GS} = 0 V		- 0.70	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			31	60	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	10 A dl/dt = 100 A/v2 T = 05 °C		23	45	nC	
Reverse Recovery Fall Time	$I_F = -10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$			13		ns	
Reverse Recovery Rise Time	t _b	7		18			

Notes:

- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

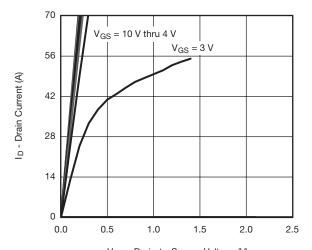
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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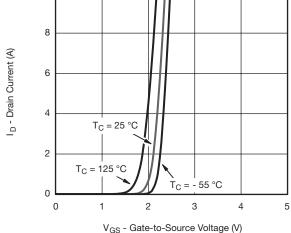


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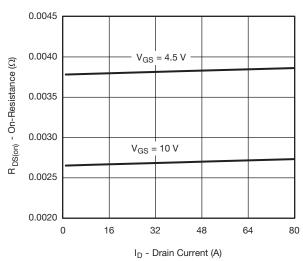
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



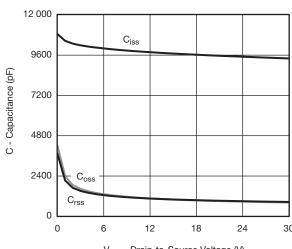
V_{DS} - Drain-to-Source Voltage (V) **Output Characteristics**



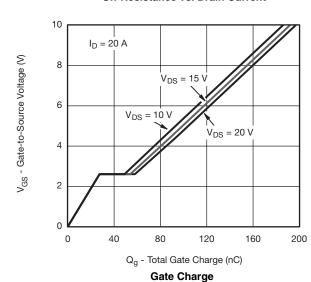
Transfer Characteristics

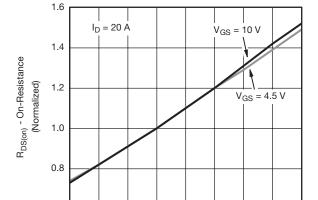


On-Resistance vs. Drain Current



 V_{DS} - Drain-to-Source Voltage (V) $\label{eq:capacitance}$





25

T_J - Junction Temperature (°C)

On-Resistance vs. Junction Temperature

50

75

100

125

150

0.6

- 50

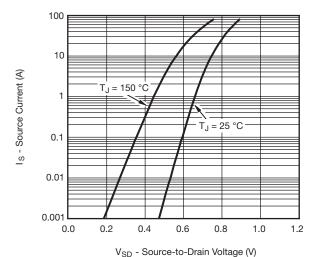
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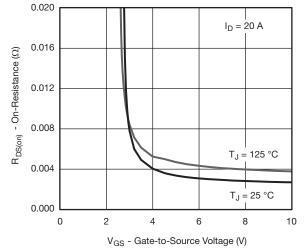
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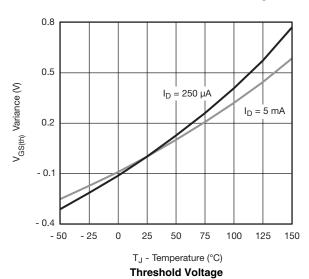
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

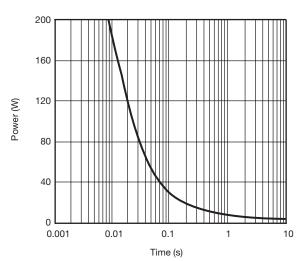




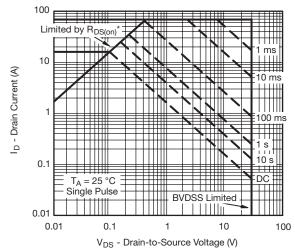




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



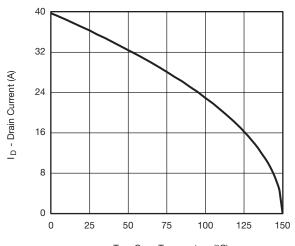
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area



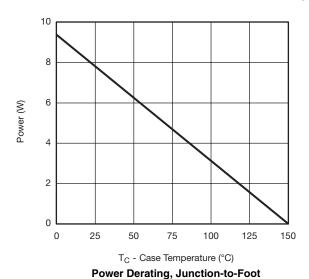
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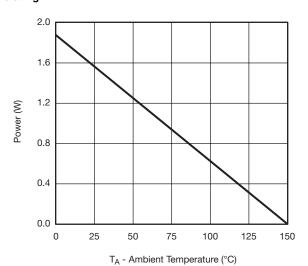
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



 $T_{\mbox{\scriptsize C}}$ - Case Temperature (°C)

Current Derating*





Power Derating, Junction-to-Ambient

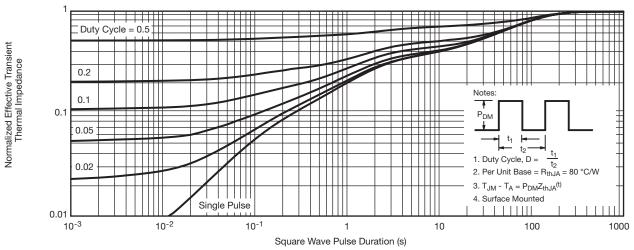
^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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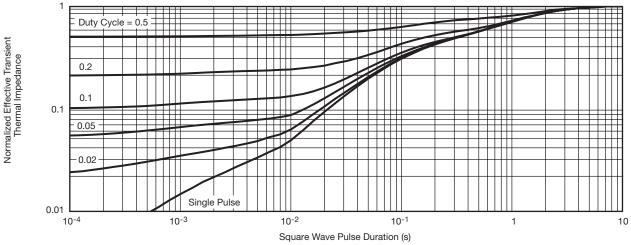
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

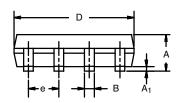
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www.vishay.com



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIMETERS INCHES			HES	
DIM	Min	Max	Min	Max	
Α	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
Е	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
FCN: C-06527-Rev 11-Sen-06					

ECN: C-06527-Rev. I, 11-Sep-06

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06 www.vishay.com



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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