

Surface Mount PAR® Transient Voltage Suppressors

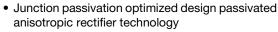
High Temperature Stability and High Reliability Conditions



DO-218 Compatible

PRIMARY CHARACTERISTICS					
V_{BR}	11.1 V to 52.8 V				
P _{PPM} (10 x 1000 μs)	6600 W				
P _{PPM} (10 x 10 000 μs)	5200 W				
P_{D}	8 W				
V_{WM}	10 V to 43 V				
I _{FSM}	700 A				
T _J max.	175 °C				
Polarity	Uni-directional				
Package	DO-218AC				

FEATURES





 T_J = 175 °C capability suitable for high reliability and automotive requirement

RoHS

- Available in uni-directional polarity only
- Low leakage current
- · Low forward voltage drop
- High surge capability
- Meets ISO7637-2 surge specification (varied by test condition)
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- AEC-Q101 qualified
 - Automotive ordering code: base P/NHE3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

Use in sensitive electronics protection against voltage transients induced by inductive load switching and lighting, especially for automotive load dump protection application.

MECHANICAL DATA

Case: DO-218AC

Molding compound meets UL 94 V-0 flammability rating Base P/NHE3 - RoHS-compliant, AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

HE3 suffix meets JESD 201 class 2 whisker test

Polarity: Heatsink is anode

MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	VALUE	UNIT			
Peak pulse power dissipation	with 10/1000 μs waveform	В	6600	W		
	with 10/10 000 µs waveform	- P _{PPM}	5200			
Power dissipation on infinite heats	P _D	8.0	W			
Peak pulse current with 10/1000 µ	I _{PPM} ⁽¹⁾	See next table	Α			
Peak forward surge current 8.3 m	I _{FSM}	700	Α			
Operating junction and storage te	T _J , T _{STG}	-55 to +175	°C			

Note

(1) Non-repetitive current pulse derated above T_A = 25 °C



ELECTRICAL CHARACTERISTICS (T _C = 25 °C unless otherwise noted)								
DEVICE TYPE	BREAKDOWN VOLTAGE V _{BR} (V)		TEST CURRENT I _T	STAND-OFF VOLTAGE V _{WM}	MAXIMUM REVERSE LEAKAGE	MAXIMUM REVERSE LEAKAGE AT V _{WM} T _J = 175 °C	MAX. PEAK PULSE CURRENT AT 10/1000 µs WAVEFORM	MAXIMUM CLAMPING VOLTAGE
	MIN.	MAX.	(mA)	(V)	ΑΤ V _{WM} I _D (μΑ)	I _D (μA)	(A)	AT I _{PPM} V _C (V)
SM8S10AT	11.1	12.3	5.0	10.0	15	250	388	17.0
SM8S11AT	12.2	13.5	5.0	11.0	10	150	363	18.2
SM8S12AT	13.3	14.7	5.0	12.0	10	150	332	19.9
SM8S13AT	14.4	15.9	5.0	13.0	10	150	307	21.5
SM8S14AT	15.6	17.2	5.0	14.0	10	150	284	23.2
SM8S15AT	16.7	18.5	5.0	15.0	10	150	270	24.4
SM8S16AT	17.8	19.7	5.0	16.0	10	150	254	26.0
SM8S17AT	18.9	20.9	5.0	17.0	10	150	239	27.6
SM8S18AT	20.0	22.1	5.0	18.0	10	150	226	29.2
SM8S20AT	22.2	24.5	5.0	20.0	10	150	204	32.4
SM8S22AT	24.4	26.9	5.0	22.0	10	150	186	35.5
SM8S24AT	26.7	29.5	5.0	24.0	10	150	170	38.9
SM8S26AT	28.9	31.9	5.0	26.0	10	150	157	42.1
SM8S28AT	31.1	34.4	5.0	28.0	10	150	145	45.4
SM8S30AT	33.3	36.8	5.0	30.0	10	150	136	48.4
SM8S33AT	36.7	40.6	5.0	33.0	10	150	124	53.3
SM8S36AT	40.0	44.2	5.0	36.0	10	150	114	58.1
SM8S40AT	44.4	49.1	5.0	40	10	150	102	64.5
SM8S43AT	47.8	52.8	5.0	43	10	150	95.1	69.4

Note

For all types maximum $V_F = 1.8 \text{ V}$ at $I_F = 100 \text{ A}$ measured on 8.3 ms single half sine-wave or equivalent square wave, duty cycle = 4 pulses per minute maximum

THERMAL CHARACTERISTICS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	SYMBOL VALUE			
Typical thermal resistance, junction to case	$R_{ heta JC}$	0.90	°C/W		

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
SM8S10ATHE3/I ⁽¹⁾	2.605	I	750	13" diameter plastic tape and reel, anode towards the sprocket hole		

Note

(1) AEC-Q101 qualified



RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

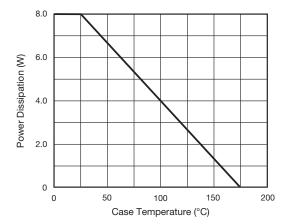


Fig. 1 - Power Derating Curve

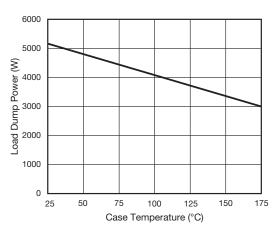


Fig. 2 - Load Dump Power Characteristics (10 ms Exponential Waveform)

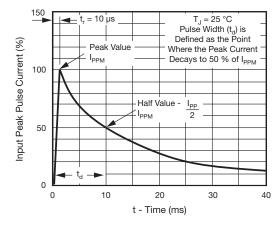


Fig. 3 - Pulse Waveform

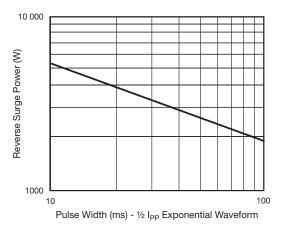


Fig. 4 - Reverse Power Capability

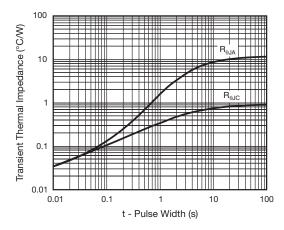


Fig. 5 - Typical Transient Thermal Impedance

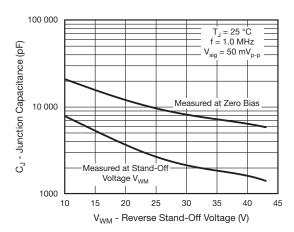
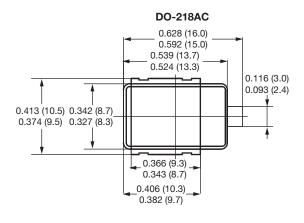
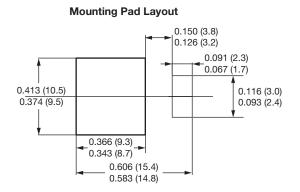


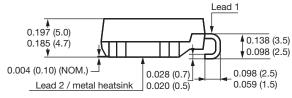
Fig. 6 - Typical Junction Capacitance



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)









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