muRata

Reference Specification

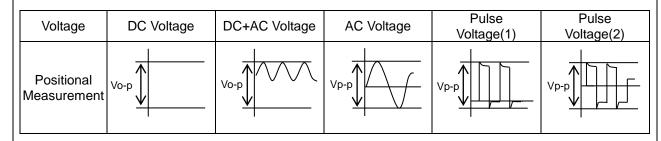
Type SA Safety Standard Certified Lead Type Disc Ceramic Capacitors for General Purpose

Product specifications in this catalog are as of Oct. 2018, and are subject to change or obsolescence without notice.

Please consult the approval sheet before ordering. Please read rating and Cautions first.

1. OPERATING VOLTAGE

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range. When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.



2. OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

When the capacitor is used in a high-frequency current, pulse current or the like, it may have the selfgenerated heat due to dielectric-loss. Applied voltage should be the load such as self-generated heat is within 20 °C on the condition of atmosphere temperature 25 °C. When measuring, use a thermocouple of small thermal capacity-K of ϕ 0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability.(Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

3. TEST CONDITION FOR WITHSTANDING VOLTAGE

(1) TEST EQUIPMENT

Test equipment for AC withstanding voltage should be used with the performance of the wave similar to 50/60 Hz sine wave.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.

(2) VOLTAGE APPLIED METHOD

When the withstanding voltage is applied, capacitor's lead or terminal should be firmly connected to the out-put of the withstanding voltage test equipment, and then the voltage should be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the *zero cross. At the end of the test time, the test voltage should be reduced to near zero, and then capacitor's lead or terminal should be taken off the out-put of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.

*ZERO CROSS is the point where voltage sine wave pass 0V. - See the right figure -

0V voltage sine wave

4. FAIL-SAFE

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

5. VIBRATION AND IMPACT

Do not expose a capacitor or its leads to excessive shock or vibration during use.

6. SOLDERING

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip : 400 °C max.

Soldering iron wattage : 50W max.

Soldering time : 3.5s max.

7. BONDING, RESIN MOLDING AND COATING

In case of bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.

In case of the amount of applications, dryness / hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.

The variation in thickness of adhesive, molding resin or coating may cause a outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

8. TREATMENT AFTER BONDING, RESIN MOLDING AND COATING

When the outer coating is hot (over 100 $^{\circ}$ C) after soldering, it becomes soft and fragile. So please be careful not to give it mechanical stress.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

9. OPERATING AND STORAGE ENVIRONMENT

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -10 to 40 °C and 15 to 85%.

Use capacitors within 6 months after delivered. Check the solderability after 6 months or more.

10. LIMITATION OF APPLICATIONS

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. Power plant control equipment
- 5. Medical equipment
- 6. Transportation equipment (vehicles, trains, ships, etc.)
- 7. Traffic signal equipment
- 8. Disaster prevention / crime prevention equipment
- 9. Data-processing equipment exerting influence on public
- 10. Application of similar complexity and/or reliability requirements to the applications listed in the above.

NOTICE

1. CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity : Output of 20 watts per liter or less.

Rinsing time : 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

2. CAPACITANCE CHANGE OF CAPACITORS

· Class 1 capacitors

Capacitance might change a little depending on a surrounding temperature or an applied voltage. Please contact us if you use for the strict time constant circuit.

· Class 2 and 3 capacitors

Class 2 and 3 capacitors like temperature characteristic B, E and F have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor leaves for a long time. Moreover, capacitance might change greatly depending on a surrounding temperature or an applied voltage. So, it is not likely to be able to use for the time constant circuit. Please contact us if you need a detail information.

3. PERFORMANCE CHECK BY EQUIPMENT

Before using a capacitor, check that there is no problem in the equipment's performance and the specifications.

Generally speaking, CLASS 2 ceramic capacitors have voltage dependence characteristics and temperature dependence characteristics in capacitance. So, the capacitance value may change depending on the operating condition in a equipment. Therefore, be sure to confirm the apparatus performance of receiving influence in a capacitance value change of a capacitor, such as leakage current and noise suppression characteristic.

Moreover, check the surge-proof ability of a capacitor in the equipment, if needed, because the surge voltage may exceed specific value by the inductance of the circuit.

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1.Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.

2. You are requested not to use our product deviating from this specification.

1. Application

This specification is applied to Safety Standard Certified Lead Type Disc Ceramic Capacitors Type SA used for General Electric equipment.

Type SA is Safety Standard Certified disc ceramic capacitor of Class X1,Y2.

Do not use these products in any automotive power train or safety equipment including battery chargers for electric vehicles and plug-in hybrids.

	Standard number	*Certified number	AC Rated volt. V(r.m.s.)		
UL	UL60384-14	E37921			
ENEC (VDE)	EN60384-14	40042990	X1:300 Y2:250		
CQC	IEC60384-14	CQC15001137840	12.200		
ктс	KC60384-14	HU03008-17009			
*Above Certified number may be changed on account of the revision of standards and the renewal of certification.					

2. Rating

2-1. Operating temperature range	-40 ~ +125°C
2-2. Rated Voltage	X1:AC300V(r.m.s.) Y2:AC250V(r.m.s.)

2-3. Part number configuration

ex.) <u>DE2</u>	B3	SA	471	K	A3	В	T02F
Product	Temperature	Туре	Capacitance	Capacitance	Lead	Packing	Individual
code	characteristic	name		tolerance	code	style code	specification

• Product code DE2 denotes class X1,Y2.

•Temperature characteristic

Code	Temperature characteristic
1X	SL
B3	В
E3	E

Please confirm detailed specification on [Specification and test methods].

• Type name

This denotes safety certified type name Type SA.

Capacitance

The first two digits denote significant figures ; the last digit denotes the multiplier of 10 in pF. ex.) In case of 471.

$$47 \times 10^1 = 470 \text{pF}$$

• Capacitance tolerance Please refer to [Part number list].

• Lead code

Code	Lead style				
A*	Vertical crimp long type				
J*	Vertical crimp short type				
N*					
* Please refe	* Please refer to [Part number list].				

• Packing style code

ig otyle coue			
Code	Packing type		
В	Bulk type		
A	Ammo pack taping type		

• Individual specification

In case part number cannot be identified without 'individual specification', it is added at the end of part number.

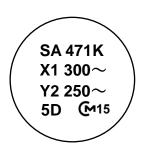
Code	Specification						
T01F	Dielectric strength between lead wires: AC2000V(r.m.s.)	 Rated voltage : X1:AC300V(r.m.s.) Y2:AC250V(r.m.s.) Halogen Free 					
T02F	Dielectric strength between lead wires: AC2600V(r.m.s.)	Br ≤ 900ppm, Cl ≤ 900ppm Br + Cl ≤ 1500ppm → CP wire					

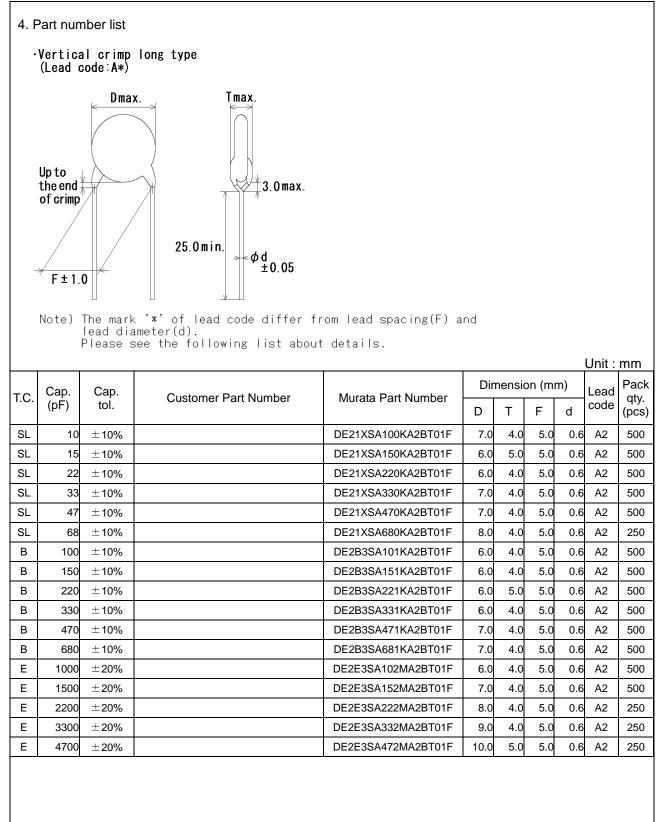
Note) Murata part numbers might be changed depending on lead code or any other changes. Therefore, please specify only the type name(SA) and capacitance of products in the parts list when it is required for applying safety standard of electric equipment.

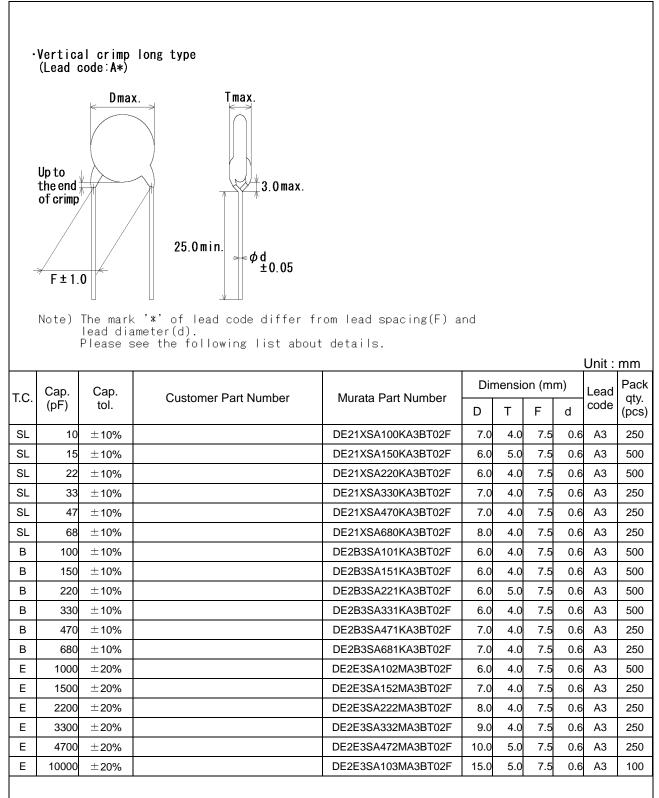
3. Marking

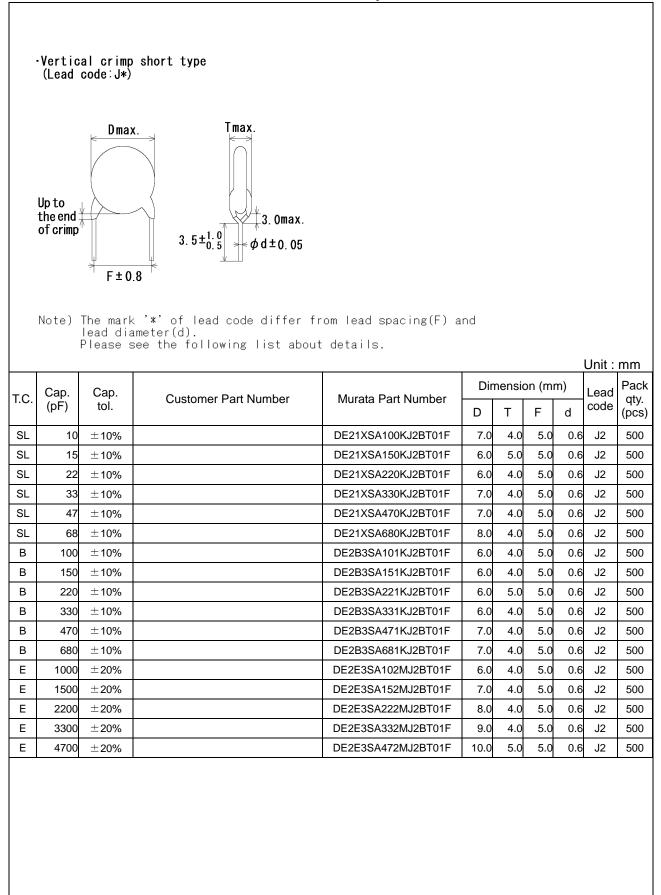
Type name	: SA				
Nominal capacitance	: Actual value(under 100pF)				
	3 digit system(100pF and over)				
Capacitance tolerance	: Code				
Class code and Rated voltage mark	: X1 300~				
	Y2 250~				
Manufacturing year	: Letter code(The last digit of A.D. year.)				
Manufacturing month	: Çode				
	$($ Feb./Mar. $\rightarrow 2$ Aug./Sep. $\rightarrow 8$ $)$				
	$ \left(\begin{array}{ccc} \text{Feb./Mar.} \rightarrow 2 & \text{Aug./Sep.} \rightarrow 8 \\ \text{Apr./May.} \rightarrow 4 & \text{Oct./Nov.} \rightarrow 0 \\ \text{Jun./Jul.} \rightarrow 6 & \text{Dec./Jan.} \rightarrow D \end{array} \right) $				
	\bigcup Jun./Jul. \rightarrow 6 Dec./Jan. \rightarrow D				
Company name code	: CM15 (Made in Thailand)				

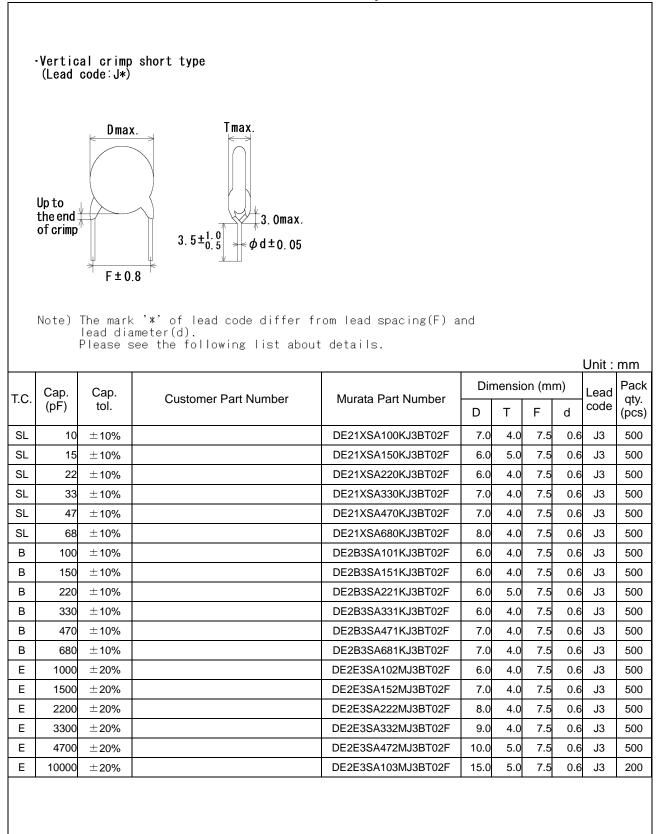
(Example)

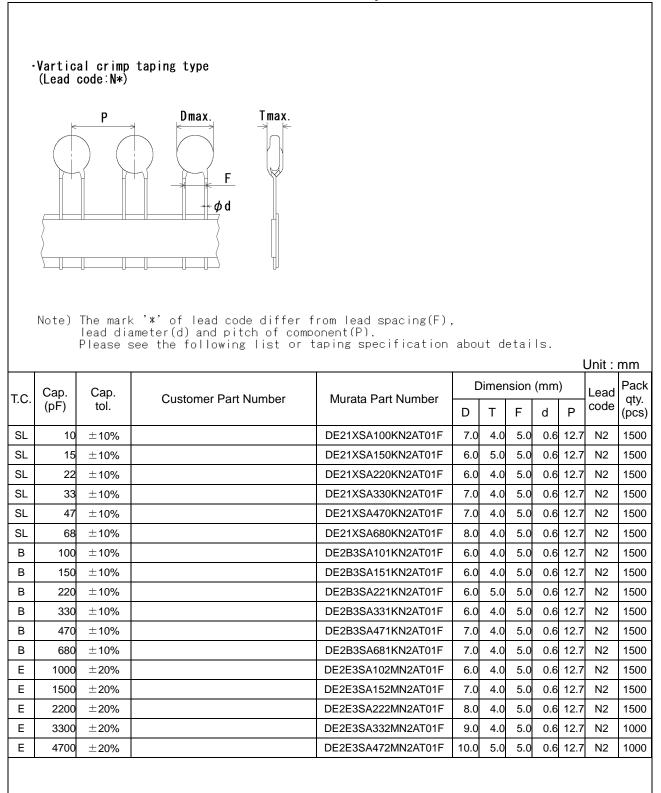


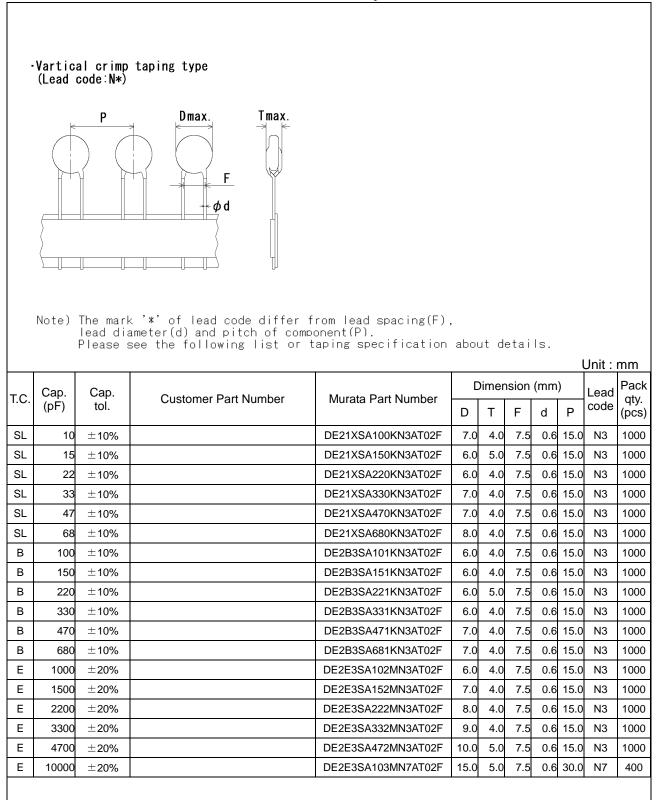










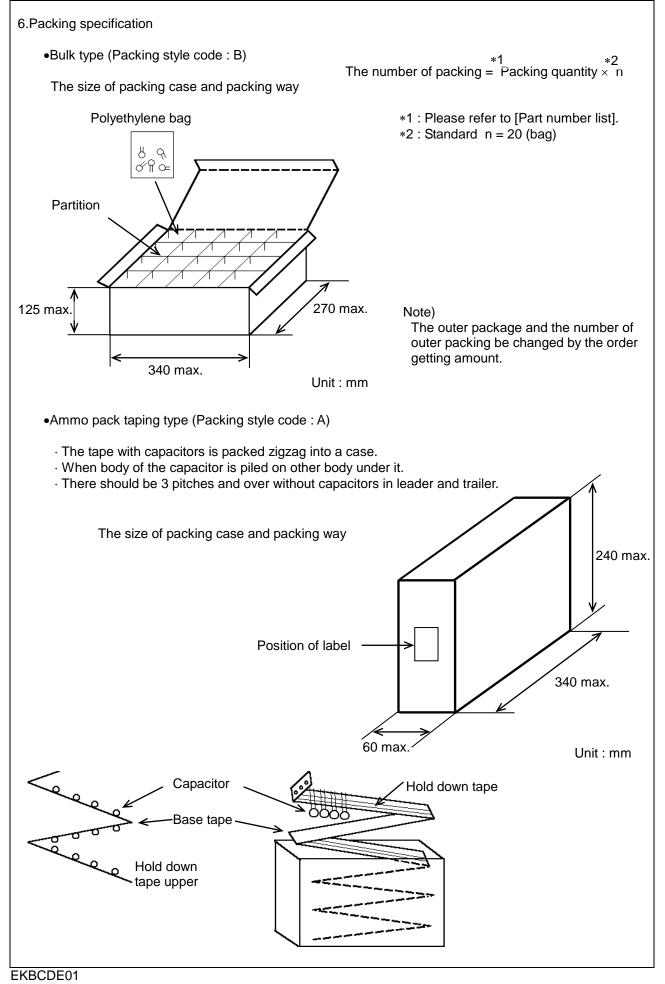


	pecification and te		-				T	(
<u>No.</u> 1	Item Appearance and dimensions		Item Specification earance and dimensions No marked defect on appearance		nce	The capacitor		t method	d by nake	deves
			form and dime			for visible evic				2 0 9 0 0
			Please refer to [Part number list].		ist].	Dimensions s				
2	Marking	1	To be easily le	egible.		The capacitor should be inspected by naked eyes.				
3	Dielectric					The capacitor				
	strength wires					AC2000V(r.m. :T01F] or AC2				
						specification:1				
						the lead wires	for 60 s.			
		Body	No failure.			First, the term		e capacito	or should b	be
		insulation				connected tog		4	V	
			Then, a metal foil should be closely wrapped around			X				
						the body of the			tal 🖉 📜	
						to the distance		o		
						about 3 to 4m		ိ၀ိ၀	š <u>1988 860</u> č	o Metal
						from each terr Then, the cap		uld he ins	erted into	
						container filled				
						diameter. Fina				
						applied for 60		n the capa	citor lead	wires
4	Insulation Posic		1000010	<u>, </u>		and metal ball The insulation		o chould b	00 0000	ed
4	Insulation Resis	Starille (I.N.)	10000MΩ min.			with DC500±5				eu
						The voltage sl				tor
						through a resi	stor of 1M	Ω.	-	
5	Capacitance		Within specifie	ed tolerance.	ſ	The capacitan				°C with
6	Dissinction For	tor (DE)	2.5% max.		\rightarrow	1±0.1kHz and AC1±0.2V(r.m.s.) max				
U	Dissipation Fac	uui (D.F.)				The dissipation factor should be measured at 20° C with 1 ± 0.1 kHz and AC1 ±0.2 V(r.m.s.) max				
7	Temperature ch	aracteristic	Char SI · ±25	$50 \text{ to } -1000 \text{ pm}^{/3}$	°C	The capacitan				,
'	remperature of		Char. SL : +350 to -1000 pm/°C (Temp. range : +20 to +85°C) Char. B : Within ±10 %			each step spe				lade at
			Char. E : Within +20/-55% (Temp. range : -25 to +85°C)							
					1	1 2	3	4	Б	1
				Temp.(°C)	20		3 20±2	4 85+2	5 20±2	
									-	
8	Active flammab	oility		loth should not b	e on	The capacitor				
		fire.			least one but more than two complete layers of cheese-cloth. The capacitor should be subjected to					
						20 discharges. The interval between successive				
						discharges should be 5 s. The UAc should be				
						maintained for	r 2min afte	er the last	discharge	
						S1		L2		
								2 <u> </u>	⊥ <u>_</u> ⊥	<u> </u>
									ΤΙ ΫΤ	个"
						11		Ţ.		
										ope
						C1,2 : 1µF	+10% C	3 · 0 033'''	F±5% 10k	•
						L1 to L4 : 1.5r				
								: 3µF±5%		
						UAc : UR	±5% U	R : Rated	working v	oltage
							acitor und			
							e, Rated 1 age applie			
							~ac abbiic			
							Ux			
							5KV			
								もへ	5	
								1 - 1	-	
									time	
² "C'	' expresses nomi	nal capacitance valu	l ie(pF)						unio	
	-									
20	A02C									

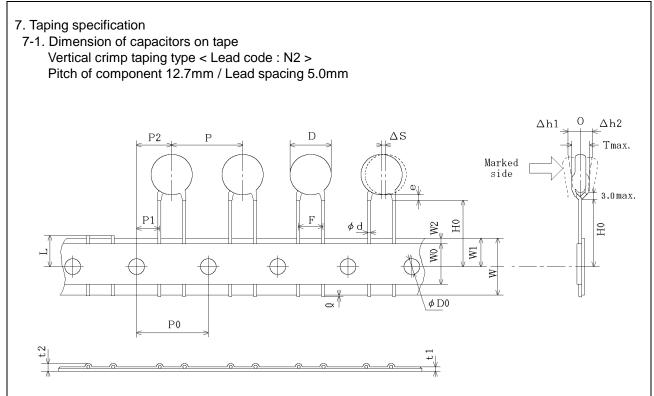
			Reference only	
No.	Item		Specification	Test method
9	Robustness of terminations	Tensile	Lead wire should not cut off. Capacitor should not be broken.	Fix the body of capacitor, apply a tensile weight gradually to each lead wire in the radial direction of capacitor up to 10N and keep it for 10 ± 1 s.
		Bending		With the termination in its normal position, the capacitor is held by its body in such a manner that the axis of the termination is vertical; a mass applying a force of 5N is then suspended from the end of the termination. The body of the capacitor is then inclined, within a period of 2 to 3 s, through an angle of
				about 90° in the vertical plane and then returned to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.
10	Vibration resistance	Appearance Capacitance D.F.	No marked defect. Within the specified tolerance. 2.5% max.	The capacitor should be firmly soldered to the supporting lead wire and vibration which is 10 to 55Hz in the vibration frequency range,1.5mm in total amplitude, and about 1min in the rate of vibration change from 10Hz to 55Hz and back to 10Hz is applied for a total of 6 h; 2 h each in 3 mutually perpendicular directions.
11	Solderability of lead	ls	Lead wire should be soldered with uniformly coated on the axial direction over 3/4 of the circumferential direction.	The lead wire of a capacitor should be dipped into a ethanol solution of 25wt% rosin and then into molten solder for 2±0.5 s. In both cases the depth of dipping is up to about 1.5 to 2.0mm from the root of lead wires. Temp. of solder : 245±5°C Lead Free Solder (Sn-3Ag-0.5Cu)
12	Soldering effect	Appearance	No marked defect.	Solder temperature: 350±10°C or 260±5°C
	(Non-preheat)	Capacitance change	Within ±10%	Immersion time : 3.5±0.5 s (In case of 260±5°C : 10±1 s)
		I.R.	1000MΩ min.	The depth of immersion is up to about
		Dielectric	Per item 3	1.5 to 2.0mm from the root of lead wires.
		strength		Thermal Capacitor
				4 1.5 to 2.0mm
				Pre-treatment : Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL)
				Post-treatment : Capacitor should be stored for 1
13	Soldering offsat	Appearance	No marked defect	to 2 h at *1room condition.
ıЗ	Soldering effect (On-preheat)	Appearance Capacitance	No marked defect. Within ±10%	First the capacitor should be stored at 120+0/-5°C for 60+0/-5 s. Then, as in figure, the lead wires should be
		change I.R.	1000MΩ min.	immersed solder of 260+0/-5°C up to 1.5 to 2.0mm
		Dielectric	Per item 3	from the root of terminal for 7.5+0/-1 s.
		strength		Thermal Capacitor
				1.5 1.5 1.5 1.5 1.5 1.5 1.5 1.5
				Pre-treatment : Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h before initial measurements.
				(Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 1 to
*2 "C	expresses nominal	L capacitance valu	l e(pF)	2 h at *1room condition.
U.	expresses nominal	capacitarice valu	ыс(h))	
-ss				

			Reference only	
No.	Item	1	Specification	Test method
14	Flame test		The capacitor flame discontinue as follows.	The capacitor should be subjected to applied flame for 15 s. and then removed for 15 s until 5 cycle.
			Cycle Time	Capacitor
			1 to 4 30 s max.	18 Flame
			5 60 s max.	
			0 000 max	is A start
				Gas Burner
15	Passive flammabilit	У	The burning time should not be	The capacitor under test should be held in the flame
			exceeded the time 30 s. The tissue paper should not	in the position which best promotes burning. Time of exposure to flame is for 30 s.
			ignite.	Length of flame : 12±1mm
				Gas burner : Length 35mm min. Inside Dia. 0.5±0.1mm
				Outside Dia. 0.9mm max.
				Gas : Butane gas Purity 95% min.
				↓ Capacitor
				About 8mm
				Gas burner → Flame
				45° 200±5mm
				V ← Tissue
				About 10mm thick board
16	Humidity	Appearance	No marked defect.	Set the capacitor for 500±12 h at 40±2°C in 90 to
	(Under steady	Capacitance	Char. SL : Within ±5%	95% relative humidity.
	state)	change	Char. B : Within $\pm 10\%$	Pre-treatment : Capacitor should be stored at
		D.F.	Char. E : Within ±15% Char. SL : 2.5% max.	125±2°C for 1 h, and apply the
		2	Char. B, E : 5.0% max.	AC2000V(r.m.s.) 60s then placed
		I.R.	3000MΩ min.	at $*1$ room condition for 24±2 h
		Dielectric	Per item 3	 before initial measurements. (Do not apply to Char. SL)
		strength		(Do not apply to Char. SL) Post-treatment :Capacitor should be stored for 1
				to 2 h at *1 room condition.
17	Humidity loading	Appearance	No marked defect.	Apply AC300V(r.m.s.) for 500±12 h at 40±2°C in
		Capacitance change	Char. SL : Within ±5%	90 to 95% relative humidity.
		Shange	Char. B : Within ±10% Char. E : Within ±15%	Pre-treatment : Capacitor should be stored at
		D.F.	Char. SL : 2.5% max.	125±2°C for 1 h, and apply the
			Char. B, E : 5.0% max.	AC2000V(r.m.s.) 60s then placed
		I.R.	3000MΩ min.	at * ¹ room condition for 24±2 h
		Dielectric	Per item 3	before initial measurements. (Do not apply to Char. SL)
		strength		Post-treatment :Capacitor should be stored for 1
				to 2 h at *1room condition.
* ¹ "ro	om condition" Tempe	rature: 15 to 35°	C, Relative humidity: 45 to 75%, Atmo	ospheric pressure: 86 to 106kPa
:SS/	402C		1= 1 - 1	
			15 / 21	

18 Life Appearance change i.R. No marked defect. Within ±20%. Impulse voltage Each individual capacitor should be subjected to a 5KV impulses for three times. Then the capacitor are applied to life test. Defection strength Per item 3 Per item 3 The capacitors are placed in a circulating air oven for a period of 1000 h. The air in the own is maintained at a temperature of 12542/0 °C, and relative humidity of 50%, max. Throughout test, the capacitor as subjected to a AC425V(rm. s)<500H2-alternating voltage of mains frequency, except that none cap. hour the voltage is increased to AC1000V(rm.s.) for 0.1 s. Pre-treatment: Capacitor should be stored at 1251/2° Cr 1 h, and apply the AC2000V(rm.s). Store to 1260/ at *'noor condition for 24:2 h before initial measurements. (Do not apply to Char. SL). 19 Temperature and immersion cycle change Appearance Char. E: :Within ±10% Char. E: :E.50% max. Defection: strength The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. <temersion cycles<="" texter<br="">intermersion cyclescycles the consecutively to 2 immersion cycles. 10 LiR. 3000MQ min. Defection: strength Per item 3 The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. Char. B: Char. B: Within ±10% Char. E: :2.5% max. (Do not themp. 3 min vater 2 0±3 15 min vater Settime temp. 3 min vater 2 0±3 15 min vater</temersion>		-		Reference only	
19 Temperature and Lapacitance Char. SL: 2:Within ±20%, Char. B. :: 5:0% max. Char. B. :: 2:0% max. Ch	No.				
19 Temperature and immersion cycle Appearance No marked defect. 19 Temperature and immersion cycle Appearance No marked defect. 19 Temperature and immersion cycle Appearance No marked defect. 10 Char, B. L. 2.5.0% max. The capacitor should be stored of a sinual store cycles. 10 Temperature and immersion cycle Appearance 11 Temperature and immersion cycle Appearance 12 Char, B. L. 2.5.0% max. The capacitor should be stored at "icon condition for 424.2 h to capacitor should be stored at "icon condition for 424.2 h to capacitor should be stored at "icon condition for 424.2 h to capacitor should be stored at a "icon condition for 244.2 h to capacitor should be stored at a "icon condition for 244.2 h to capacitor should be stored in the store initial measurements. (Do not apply to Char, SL : 2.5.0% max. 12 Temperature and immersion cycles Appearance 13 Temperature cycles 14 Appearance 15 Char, SL : 2.5.0% max. 16 Char, SL : 2.5.0% max. 17 Temperature (C) Time 18 3000000 min. 19 Temperature (C) Time 10 Char, B. E: 5.0% max. 11 Temperature (C) Time 12 Char, B. E: 5.0% max. 13 Temperature (C) Time	18	LIIE			
1R. 3000MΩ min. Dielectric strength Per item 3 18 Per item 3 19 Temperature and immersion cycle Appearance Char. B. : 0V/k max. Char. S. : 25% max. Char. B. : 0V/k max. 19 Temperature and I.R. Appearance Char. B. : 0V/k max. Char. B. : 0V/k max. The capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(rm. s) 60s then placed at 'room condition for 24-2 h before initial measurements. (Do not apply to Char. SL) 19 Temperature and I.R. Appearance Char. B. : 0V/k max. The capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(rm. s) 60s then placed at 'room condition for 24-2 h before initial measurements. (Do not apply to Char. SL) 19 Temperature and I.R. Appearance Char. SL : 2.6% max. Char. SL : 2.6% max. 11.R. 3000MQ min 12.R. D.F. Char. SL : 2.6% max. Char. SL : 2.6% max. 11.R. 3000MQ min 12.R. Temperature cycle> 13.R. Temperature (*C) Time (*Troom condition for 24-2 h) before initial measurements. (Do not apply to Char. SL) 2.8 D.F. Char. SL : 2.6% max. Char. SL : 2.6% max. Temperature (*C) 14.8 300MQ min. Temperature(*C) Time 13 12.8 14 Room temp 3				vviuiiii ±20%	
Dielectric strength Per item 3 Dielectric strength Per item 3 Per item 3 Per item 3 Per item 4 Per item 4 Per item 5 Per item 5 Per item 5 Per item 5 Per item 3 Per item 3 Per item 3 Per item 3				3000MQ min	
strength Forther (1) = 1.2 μ = 1.67 Time bit-value (12) = 50 μ s 10 Temperature and Appearance No marked defect. 11 Temperature and Capacitance Appearance No marked defect. 12 Temperature and Capacitance Char: SL: Within ±5% Char: SL: Within ±5% Char: SL: Within ±5% Char: SL: Within ±2% Char: SL: Z: W max 18 Temperature (n) Char: SL: Z: W max 19 Temperature (n) Char: SL: Z: W max 118 Appearance 118 Temperature (n) Char: SL: Z: W max 119<					
19 Temperature and immersion cycle Appearance Char. B. I: S0% max. Char. B. I: S0% max. I.R. No marked defect. Char. S. I: Within ±5% Char. B. I: S0% max. I.R. The appearance Char. S. I: Within ±5% Char. S. I: Within ±10% Char. B. I: S0% max. I.R. The capacitor should be subjected to a AC425V(r.m.).5(00 Hz- alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s) for 0.1 s. Pre-treatment : Capacitor should be subjected to a AC425V(r.m.).5(00 Hz- alternating voltage of mains frequency, except that once each hour the voltage is increased to AC1000V(r.m.s) for 0.1 s. Pre-treatment : Capacitor should be subjected to 5 temperature cycles. then consecutively to 2 immersion cycles 19 Temperature and immersion cycle Appearance Char. B. I: S0% max. I.R. No marked defect. Char. B. I: S0% max. I.R. The capacitor should be subjected to 5 temperature cycles. then consecutively to 2 immersion cycles. 19 Temperature cycle> The capacitor should be subjected to 5 temperature cycles. then consecutively to 2 immersion cycles. 19 Temperature cycle> Time capacitor should be subjected to 5 temperature cycles. then consecutively to 2 immersion cycles. 19 Temperature cycle> Time capacitor should be stored at 128/25/0 d0 min. 19 Temperature(*C) Time water 10 Per item 3 Cycle time-2 cycles 11 +65+5/-0 15 min Water 11 +65+5/-0 15 min </td <td></td> <td></td> <td></td> <td></td> <td>$100 \frac{(76)}{90}$ Front time (T1) = 1.2 μ s=1.67T</td>					$100 \frac{(76)}{90}$ Front time (T1) = 1.2 μ s=1.67T
19 Temperature and inmersion cycle Appearance No marked defect. The capacitor should be stored at "foor condition." 19 Temperature and inmersion cycle Appearance No marked defect. The capacitor should be stored for apply to Phar. SL) 19 Temperature and inmersion cycle Appearance No marked defect. The capacitor should be stored for apply to Phar. SL) 19 Temperature and inmersion cycle Appearance No marked defect. The capacitor should be stored for apply to Phar. SL) 19 Temperature and inmersion cycle Appearance No marked defect. The capacitor should be stored for at "foor condition. 19 Temperature and inmersion cycle Appearance No marked defect. The capacitor should be stored for at "foor condition. 19 Temperature (C) Time The apply to Phar. SL) The capacitor should be stored for at "foor condition. 10 D.F. Char. SL : Within ±5% Char. SL : SW max. The apperature("C) Time 10 D.F. Char. SL : SW max. Char. SL : Colve max. Colve mersion cycles Cycle time.5 cycles 10 The capacitor should be stored at "100 con apply to Phar. SL (Paperature("C) Time water of thor apply the AC2000V(rm.s.) 60 s then placed at "100 co			0		11112 = 50.012
19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored at 125±2°C of 1, h, and apply to 2 immersion cycles 19 Temperature and immersion cycle Appearance No marked defect. The capacitor are subjected to 5 temperature of 24±2 h at "room condition. 19 Temperature and immersion cycle D.F. Char. SL : Within ±5% Char. SL : Within ±5% Char. SL : Within ±20% Char. E : Within ±20% Char. SL : Within ±20% Char. E : Within ±20% Char. E : Within ±20% Char. E : Within ±20% Char. SL : Within ±20% Char. E : Within ±20% Char. SL : Within ±20% Char. E : Within ±20%					
19 Temperature and immersion cycle Appearance No marked defect. The capacitor set placed in a circulating air oven for a period of 1000 h. 19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for a 42±2 h at "room condition. 19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for a 42±2 h at "room condition. 19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for 24±2 h at "room condition. 19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for 24±2 h at "room condition. 19 Temperature cycles D.F. Char. B: : Within ±0% Char. E: : Within ±0% Char. B: : Within ±0% Char. B: : Within ±0% Char. B: : Store max. The capacitor should be stored for 24±2 h at "room condition. 10 D.F. Char. B: : Store max. Char. B: : Capacitor should be stored for 24±2 h at *icon condition. 10 Delectric strength Per item 3 The capacitor should be stored for 24±2 h at *icon condition. 11 +40±0/-3 30 min 1/2 Room tempe. 3 min 1/2 12 0:13					
19 Temperature and immersion cycle Appearance No marked defect. Throughout the subject of to subject of the subject of to subject of to subject of the subject of the subject of to subject of the sub					
19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored at 125±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC2000V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and apply the AC200V(rm.s.) 600 sthen placed at 126±2/C for 1 h, and ap					The capacitors are placed in a circulating air oven
19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for 264-20 (s. capet that once each hour the AC2000V(rm.s.) for 0.1 s. Pre-treatment : Capacitor should be stored at 125:2°C for 1 h, and apply the AC2000V(rm.s.) for 0.1 s. (Do not apply to Char. SL) 19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for 24:2 h before initial measurements. (Do not apply to Char. SL) 19 Temperature and immersion cycle Char. SL : Within ±10% Char. SL : Within ±20% The capacitor should be stored for 24:2 h before initial measurements. (Co not apply to Char. SL : Within ±20%) 10.F. Char. SL : Within ±10% Char. SL : 5.0% max. The capacitor should be stored to 5 temperature cycles, then consecutively to 2 immersion cycle. 11.R. 3000MQ: min. Dielectric strength Per item 3 12.R. 3000MQ: min. Cycle time:5 cycles 13. History Cycle time:5 cycles Cycle time:2 cycles 12.R. 3000MQ: min. Dielectric strength Per item 3 14. Agoen temp. 3 min Cycle time:2 cycles 14. Hoor temp. 3 min Cycle time:2 cycles 14. 400 or 300 min Cycle time:2 cycles 15. Per item 3 Cycle time:2 cycles					
19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored at 125±2°C for 1 h, and apply to Char. SL) 19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for 24±2 h at "room condition. 19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for 24±2 h at "room condition. 19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for 24±2 h at "room condition. 19 Temperature and immersion cycle Char. SL : Within ±5% Char. B : Utihin ±10% Char. E : Within ±20% Char. E : Within ±20% Char. B : 5.0% max. The capacitor should be stored for 24±2 h at "room condition. 10 D.F. Char. SL : 2.5% max. Char. B : 5.0% max. Cycle time:5 cycles. 1 1.R. 3000MΩ min. 2 Room temp. 3 min 1 4 Room temp. 3 min 2 0±3 15 min water 2 0±3 15					The air in the oven is maintained at a temperature
19 Temperature and immersion cycle Appearance No marked defect. 19 Temperature and immersion cycle Appearance No marked defect. 19 Temperature and immersion cycle Appearance No marked defect. 19 Temperature and immersion cycle Appearance No marked defect. 19 Temperature and immersion cycle Char. SL : Within ±10% 19 Temperature and immersion cycle Char. SL : Within ±20% 19 Temperature and immersion cycle Char. SL : Within ±20% 19 Temperature and immersion cycle Char. SL : Within ±20% 10 D.F. Char. SL : 2.5% max. 10 Dielectric strength Per item 3 11 4040/-3 30 min 12 Room temp. 3 min 12 Quest itim strength Cycle time:5 cycles 11 +65+5/-0 15 min Water 12 0±3 15 min Water					of 125+2/-0 °C, and relative humidity of 50% max
19 Temperature and immersion cycle Appearance No marked defect. 19 Temperature and immersion cycle Appearance No marked defect. 19 Temperature and immersion cycle Capacitance Char. SL : Within ±5% Char. B : Within ±20% The capacitor should be subjected to 5 temperature change 19 Temperature and immersion cycle Capacitance Char. SL : Within ±20% The capacitor should be subjected to 5 temperature change 19 Temperature and immersion cycle Capacitance Char. SL : Within ±20% The capacitor should be subjected to 5 temperature change 19 Temperature and immersion cycle Char. B : Utithin ±20% The capacitor should be subjected to 5 temperature change 19 Temperature and immersion cycle Char. B : 2.5% max. The capacitor should be subjected to 5 temperature change 10 D.F. Char. B : 2.5% max. The capacitor should be subjected to 5 temperature change 11 40+0/-3 30 min 12 Room temp. 3 min 13 Hile Ber item 3 14 Room temp. 3 min 2 Room temp. 3 min 3 +125+3/-0 10 min 1 +65+5/-0 15 min 2 0±3 15 min 2 0±3 15 min <td< td=""><td></td><td></td><td></td><td></td><td></td></td<>					
19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at "1room condition for 24±2 h 19 Temperature and immersion cycle Appearance No marked defect. 10 The capacitor should be stored for Char. SL : Within ±10% Char. B: Within ±10% Char. B: Within ±10% Char. B: Within ±20% The capacitor should be stored for 24±2 h 1.R. 3000MD min. The capacitor should be stored for 300 min 1/R. Step 1.R. 3000MD min. Step Time cycles. 1.R. 3000MD min. Cycle time: 5 cycles 1.R. 3000MD min. Step Temperature(°C) Time water 1 1 +45+5/-0 15 min water Cycle time: 5 cycles 2 0±3 15 min water Cycle time: 2 cycles 2 0±3 15 min water Cycle time: 2 cycles 1 +65+5/-0 15 min water Cycle time: 2 cycles 1 +65+5/-0 15 min water Cycle time: 2 cycles 1 +65+5/-0 15 min water Cycle time: cycles 1 +65+5/-0 15 min water Cycle time: cycles					
19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for 24±2 h 19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. 19 Temperature and immersion cycle Char. SL : Within ±20% Char. B : Within ±20% Char. B : Within ±20% Char. SL : Z-5% max. The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. 1.R. 3000MΩ min. Dielectric strength Per item 3 1.R. 3000MΩ min. The capacitor should be stored at 125±3/-0 30 min 3 min 2 Room temp. 3 min 2 Room temp. 3 min 2 Room temp. 3 min 2 Cycle time:5 cycles 2 0±3 15 min water Cycle time:2 cycles 1 +65+5/-0 15 min water Cycle time:2 cycles 2 0±3 15 min water Cycle time:2 cycles 1 +65+5/-0 15 min water Cycle time:2 cycles					
19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for 24±2 h at "froom condition for 24±2 h 19 Temperature and immersion cycle Appearance Char. SL : Within ±5% Char. B : Within ±5% Char. B : Within ±20% The capacitor should be stored for 24±2 h at "froom condition. 19 Temperature and immersion cycle Appearance Char. SL : 2.5% max. Char. SL : 2.5% max. Char. B : Within ±20% The capacitor should be stored for 24±2 h at "froom condition. 10 T.R. 3000MC min. The capacitor should be stored for 24±2 h at "froom condition. 11 R.R. 3000MC min. The capacitor should be stored for 30 min 2 11 R.R. 3000MC min. The capacitor should be stored for 30 min 2 12 Room temp. 3 min 3 13 Temperature(°C) Time 14 Room temp. 3 min 3 15 Step Temperature(°C) Time 14 +65±5/-0 15 min Salt 12 0±3 15 min Salt 12 0±3 15 min Salt 12 0±3 15 min Salt 12 0±3 <td< td=""><td></td><td></td><td></td><td></td><td></td></td<>					
19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for 24±2 h at *1room condition. 19 Temperature and immersion cycle Appearance Char. SL : Within ±5% Char. B : Within ±10% Char. E : Within ±20% The capacitor should be subjected to 5 temperature cycles. 19 Temperature and immersion cycle Capacitance Char. SL : Within ±20% The capacitor should be subjected to 5 temperature cycles. 18 D.F. Char. SL : 2.5% max. The capacitor should be subjected to 5 temperature cycles. 18. D.F. Char. SL : 2.5% max. Temperature cycle> 19. Temperature cycle Char. SL : 2.5% max. Temperature cycle> 18. No 00000 min. Dielectric Step Temperature(°C) Time 19. Temperature cycle> Step Temperature(°C) Time Water 19. D.F. Char. SL : 2.5% max. Temperature(°C) Time Step 18. I.R. 30000MQ min. Dielectric Step Temperature(°C) Time Water 2 0±3 15 min water 2 <t< td=""><td></td><td></td><td></td><td></td><td></td></t<>					
19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be stored for 24±2 h at *1room condition. 19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycle. D.F. Char. SL : Within ±20% Char. SL : 2.5% max. - 1.R. 3000MQC min. - - Dielectric strength Per item 3 - - 1 +00+0/-3 30 min - 2 0±3 15 min 3 min 3 +125+3/-0 30 min - 4 Room temp. 3 min - 2 0±3 15 min water 1 +65+5/-0 15 min water 2 0±3 15 min Salt 3					
19 Temperature and immersion cycle Appearance No marked defect. Char. SL: Within ±5% Char. SL: Within ±5% Char. B: Within ±10% Char. E: Within ±20% The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. D.F. Char. SL: 2:5% max. Char. B, E: 5:0% max. The capacitance change Time char. B, E: 5:0% max. 1.R. 3000MΩ min. 2 Room temp. 3 min 1.R. 3000MΩ min. 2 Room temp. 3 min 0ielectric strength Per item 3 3 +125+3/-0 30 min 1 +65+5/-0 15 min water 1 +65+5/-0 15 min water 2 0±3 15 min solit water 1 +65+5/-0 15 min water 2 0±3 15 min water 2 0±3 15 min solit water 2 0±3 15 min water 1					
19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. 19 Temperature and immersion cycle D.F. Char. B. : Within ±10% Char. B. E : 5.0% max.					before initial measurements.
19 Temperature and immersion cycle No marked defect. Capacitance char. SL : Within ±5% Char. B : Within ±10% Char. E : Within ±20% The capacitor should be subjected to 5 temperature cycles. D.F. Char. SL : 2.5% max. Char. B, E : 5.0% max. Temperature cycle> I.R. 3000MΩ min. 2 Room temp. 3 min Dielectric strength Per item 3 3 +125+3/-0 30 min 2 0±3 15 min Salt water Cycle time: 2 cycles Pre-treatment : Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(r.m.s.) 60s then placed at *1room condition for 24±2 h *1*room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa					
19 Temperature and immersion cycle Appearance No marked defect. The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. 19 Temperature and immersion cycle Char. B: Within ±10% The capacitor should be subjected to 5 temperature cycles, then consecutively to 2 immersion cycles. 0 D.F. Char. SL: 2.5% max. Char. SL: 2.5% max. 1.R. 3000MΩ min. 2 Room temp. 3 min 0 Dielectric strength Per item 3 9 Per item 3 3 +125+3/-0 30 min 2 0:0:0:0:0:0:0:0:0:0:0:0:0:0:0:0:0:0:0:					
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change Char. B. : Within ±10% Char. E. : Within ±20% D.F. Char. S. E. : 2.5% max. Char. B. E : 5.0% max. 1.R. 3000MΩ min. Dielectric strength Per item 3 Vertex Per item 3 Char. B. E. 5.0% max. 3 min 2 Room temp. 3 min 2 Room temp. 3 min Cycle time:5 cycles Step Temperature(°C) Immersion cycle> Step Temperature(°C) Time 1 +65+5/-0 15 min Water 2 0±3 15 min Water 2 0±3 15 min Water 2 0±3 15 min Step Pre-treatment : Capacitor should be stored at 125±2°C for 1 h, and apply the AC2000V(rm.s) 60s then placed at *1room condition or 24±2 h at *1room condition. *1"room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa	19				I he capacitor should be subjected to 5 temperature
Char. E: Within ±20%		immersion cycle			cycles, then consecutively to 2 infinersion cycles.
D.F. Char. SL : 2.5% max. Char. B, E : 5.0% max. Step Temperature(°C) Time 1 -40+0/-3 30 min 2 Room temp. 3 min 0 Dielectric strength Per item 3 3 +125+3/-0 30 min 2 Room temp. 3 min 3 +125+3/-0 30 min 3 +125+3/-0 1 mmersion 3 mmersion 3 +125+3/-0 1 mmersion Step Temperature(°C) Time Immersion Cycle time:5 cycles Step Temperature(°C) Time Immersion water 1 +65+5/-0 15 min Salt Vater 2 0±3 15 min Salt 2 0±3 15 min Salt Vater Cycle time:2 cycles Pre-treatment : Capacitor should be stored at 125±2°C for 1 h, and apply the AC200V(r.m.s.) 60s then placed at *1room condition for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 24±2 h at *1room condition. Step 106kPa			change		<temperature cycle=""></temperature>
I.R. 3000MΩ min. Dielectric strength Per item 3 I.R. 3000MΩ min. Dielectric strength Per item 3 I.R. 3000MΩ min. Q Room temp. 3 +125+3/-0 3 +125+3/-0 3 +125+3/-0 You the strength Cycle time:5 cycles Step Temperature(°C) Time Immersion water 2 0±3 1 +65+5/-0 Step Temperature(°C) You the stored at 125±2°C for 1 h, and apply the AC2000V(rm.s.) 60s then placed at *1room condition roution for 24±2 h before initial measurements. (Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 24±2 h at *1room condition.			DE		
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(Do not apply to Char. SL) Post-treatment : Capacitor should be stored for 24±2 h at *1room condition. 1 "room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa					
Post-treatment : Capacitor should be stored for 24±2 h at *1room condition. ¹ "room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa					
¹ "room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa					Post-treatment : Capacitor should be stored for
¹ "room condition" Temperature: 15 to 35°C, Relative humidity: 45 to 75%, Atmospheric pressure: 86 to 106kPa					
² "C" expresses nominal capacitance value(pF)	¹ "ro	om condition" Tempe	erature: 15 to 35°	C, Relative humidity: 45 to 75%, Atm	ospheric pressure: 86 to 106kPa
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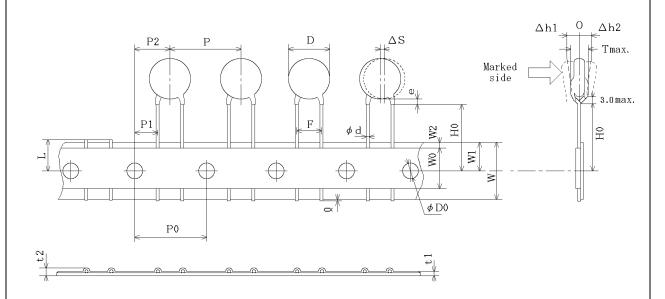


Unit : mm

Item	Code	Dimensions	Remarks
Pitch of component	Р	12.7±1.0	
Pitch of sprocket hole	P0	12.7±0.3	
Lead spacing	F	$5.0\pm_{0.2}^{0.8}$	
Length from hole center to component center	P2	6.35±1.3	
Length from hole center to lead	P1	3.85±0.7	Deviation of progress direction
Body diameter	D	Please refer to [P	art number list].
Deviation along tape, left or right	ΔS	0±1.0	They include deviation by lead bend .
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and bottom planes	H0	$18.0\pm_{0}^{2.0}$	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness
Deviation across tape, front	∆h1	1.0 max.	
Deviation across tape, rear	Δ h2		
Portion to cut in case of defect	L	11.0± ⁰ _{1.0}	
Hold down tape width	W0	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead		Up to the end of crimp	
Body thickness		Please refer to [Part number list].	

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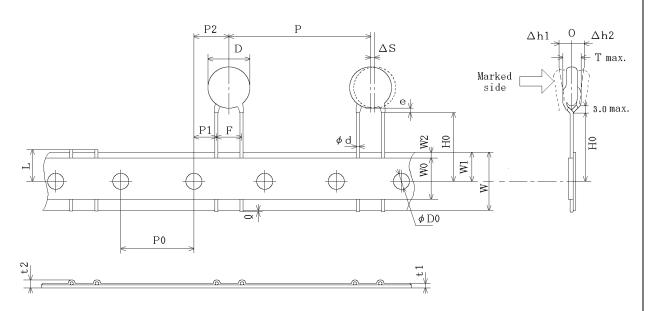
Vertical crimp taping type < Lead code : N3 > Pitch of component 15.0mm / Lead spacing 7.5mm



Unit : mm

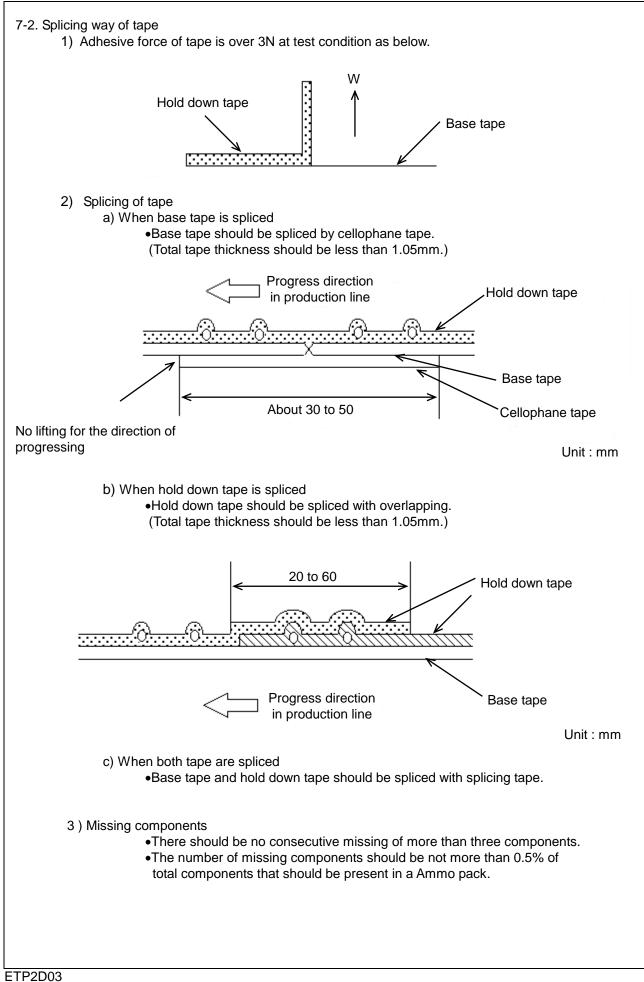
1		
Code	Dimensions	Remarks
Р	15.0±2.0	
P0	15.0±0.3	
F	7.5±1.0	
P2	7.5±1.5	
P1	3.75±1.0	Deviation of progress direction
D	Please refer to [Part number list].
ΔS	0±2.0	They include deviation by lead bend .
W	18.0±0.5	
W1	9.0±0.5	Deviation of tape width direction
HO	18.0± ^{2.0} ₀	
Q	+0.5~-1.0	
φD0	4.0±0.1	
φd	0.60±0.05	
t1	0.6±0.3	
t2	1.5 max.	They include hold down tape thickness.
∆h1	0.0	
∆h2		
L	11.0± ⁰ _{1.0}	
WO	11.5 min.	
W2	1.5±1.5	
е	Up to the end of crimp	
Т	Please refer to [Part number list].
	P P0 F P2 P1 D ΔS W W1 H0 Q φD0 φd t1 t2 Δh1 Δh2 L W0 W2 e	P 15.0±2.0 P0 15.0±0.3 F 7.5±1.0 P2 7.5±1.5 P1 3.75±1.0 D Please refer to [ΔS 0±2.0 W 18.0±0.5 W1 9.0±0.5 H0 18.0± $_0^{2.0}$ Q +0.5~-1.0 φD0 4.0±0.1 φd 0.60±0.05 t1 0.6±0.3 t2 1.5 max. Δh1 2.0 max. L 11.0± $_{1.0}^{0}$ W0 11.5 min. W2 1.5±1.5 e Up to the end of





	-		Unit : mm
Item		Dimensions	Remarks
Pitch of component	Р	30.0±2.0	
Pitch of sprocket hole	P0	15.0±0.3	
Lead spacing	F	7.5±1.0	
Length from hole center to component center	P2	7.5±1.5	Deviation of anomalous direction
Length from hole center to lead	P1	3.75±1.0	Deviation of progress direction
Body diameter	D	Please refer to [Part number list].
Deviation along tape, left or right	ΔS	0±2.0	They include deviation by lead bend.
Carrier tape width	W	18.0±0.5	
Position of sprocket hole	W1	9.0±0.5	Deviation of tape width direction
Lead distance between reference and bottom planes	HO	18.0± ^{2.0} ₀	
Protrusion length	Q	+0.5~-1.0	
Diameter of sprocket hole	φD0	4.0±0.1	
Lead diameter	φd	0.60±0.05	
Total tape thickness	t1	0.6±0.3	
Total thickness, tape and lead wire	t2	1.5 max.	They include hold down tape thickness.
Deviation across tape, front	∆h1	2.0 may	
Deviation across tape, rear	Δ h2	2.0 max.	
Portion to cut in case of defect	L	11.0± ⁰ _{1.0}	
Hold down tape width	W0	11.5 min.	
Hold down tape position	W2	1.5±1.5	
Coating extension on lead	е	Up to the end of	crimp
Body thickness	Т	Please refer to [Part number list].

ETP1N701A



EU RoHS and Halogen Free

This products of the following crresponds to EU RoHS and Halogen Free

(1) RoHS

EU RoHs 2011/65/EC compliance

maximum concentration values tolerated by weight in homogeneous materials •1000 ppm maximum Lead

- •1000 ppm maximum Mercury
- •100 ppm maximum Cadmium
- •1000 ppm maximum Hexavalent chromium
- •1000 ppm maximum Polybrominated biphenyls (PBB)
- •1000 ppm maximum Polybrominated diphenyl ethers (PBDE)

(2) Halogen-Free

The International Electrochemical Commission's (IEC) Definition of Halogen-Free (IEC 61249-2-21) compliance

- •900 ppm maximum chlorine
- •900 ppm maximum bromine
- •1500 ppm maximum total chlorine and bromine



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>>Murata(村田)

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