

#### **Features**

- Voltage Offset:
  - ±50  $\mu$ V (Max) at V<sub>CM</sub> = 2.75 V
  - ±180 µV (Max) at  $V_{CM} = 12 \text{ V}$
- Wide Common-Mode Voltage: -0.3 V to +36 V
- Supply Voltage: 2.7 V to 36 V
- · Accuracy and Zero-Drift Performance
  - ±1% Gain Error (Max over temperature)
  - 0.2 μV/°C Offset Drift (Typ)
  - 10 ppm/°C Gain Drift (Max)
- Three Gain Options for Voltage Output
  - TPA191A1: 20 V/V
  - TPA191A2: 50 V/V
  - TPA191A3: 75 V/V
  - TPA191A4: 100 V/V
  - TPA191A5: 200 V/V
  - TPA191A6: 500 V/V
  - TPA191A7: 1000 V/V
- Low Supply Current: 80 μA (Typ)
- Rail-to-Rail Output
- Package: SOT363
- Industrial -40°C to 125°C Operating Range

### **Applications**

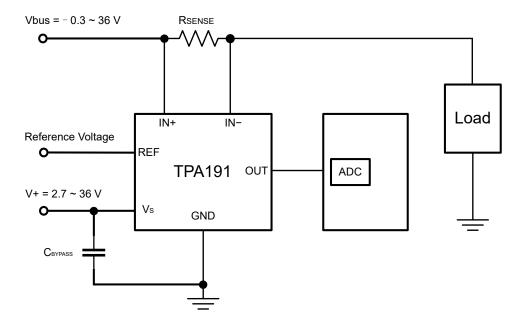
- Current Sensing (High-Side/Low-Side)
- Battery Charger
- Power Management
- · Cell Phone Charger
- · Electrical Cigarette
- · Wireless Charger
- · Telecom Equipment

### **Description**

The TPA191 series of zero-drift, bi-directional current sense amplifier can sense voltage drops across shunts at common-mode voltages from -0.3 V to 36 V, independent of the supply voltage. Five fixed gains are available: 20 V/V, 50 V/V, 75 V/V, 100 V/V, and 200 V/V. The integration matched gain resistor network minimizes gain errors and reduces the temperature drift. The low offset of the zero-drift architecture enables current sensing with the maximum drops across the shunt as low as 10-mV full-scale.

The TPA191 devices operate from a single 2.7-V to 36-V power supply, with drawing a typical of 80- $\mu$ A supply current. All versions are specified from -40°C 125°C, and offered in the SOT363 package.

## **Typical Application Circuit**





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## **Revision History**

Date	Revision	Notes
2023-02-10	Rev.Pre.0	Preliminary version.
2023-03-15	Rev.A.0	Inital release.
2023-03-31	Rev.A.1	Modified the mark information from "9Ax" to "1Ax", "x" represents 1, 2, 3, 4, 5.
2023-04-19	Rev A 2	Added new part: TPA191A6(G=500), TPA191A7(G=1000).
2023-04-19	Rev.A.Z	Updated HBM: 2kV -> 3kV based on new test result.



# **Pin Configuration and Functions**

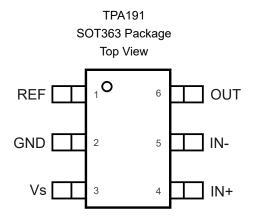


Table 1. Pin Functions: TPA191

Pin No.	Pin Name	I/O	Description			
1	REF	I	Reference voltage, 0 V to V <sub>S</sub>			
2	GND	_	round			
3	Vs	I	Power supply, 2.7 V to 36 V			
4	IN+	I	Noninverting input			
5	IN-	I	Inverting input			
6	OUT	0	Output			



## **Specifications**

### Absolute Maximum Ratings (1)

	Parameter	Min	Max	Unit
Supply Volt	age		42	V
Analog	Differential (IN+) - (IN-)	-42	42	V
Input, IN+, IN-	Common-mode	GND - 0.3	42	V
REF Input	REF Input		V <sub>S</sub> + 0.3	V
Output	Output		V <sub>S</sub> + 0.3	V
Input Curre	nt into All Pins (2)	-10	10	mA
Operating 7	- Temperature	-40	125	°C
Junction Te	mperature		150	°C
Storage Ter	mperature, T <sub>stg</sub>	-65	150	°C

<sup>(1)</sup> Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

#### **ESD, Electrostatic Discharge Protection**

Parameter		Condition	Minimum Level	Unit
НВМ	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001 (1)	3	kV
CDM	Charged Device Model ESD	ANSI/ESDA/JEDEC JS-002 (2)	1.5	kV

<sup>(1)</sup> JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

#### **Recommended Operating Conditions**

	Parameter	Min	Тур	Max	Unit
Vs	Operating Supply Voltage	2.7		36	V
V <sub>CM</sub>	Common-Mode Input Voltage	-0.3		36	V
T <sub>A</sub>	Operating Free-Air Temperature	-40		125	°C

#### **Thermal Information**

Package Type	θυΑ	θυς	Unit
SOT363	227	80	°C/W

<sup>(2)</sup> Input voltage at any pin can exceed the voltage shown if the current at that pin is limited to 10 mA.

<sup>(2)</sup> JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.



#### **Electrical Characteristics**

All test conditions:  $T_A$  = 27°C,  $V_S$  = 5.5 V,  $V_{IN+}$  = 12 V, and  $V_{SENSE}$  =  $V_{IN+}$  -  $V_{IN-}$ , and  $V_{REF}$  =  $V_S$  / 2, unless otherwise noted.

	Parame	eter	Conditions	Min	Тур	Max	Unit
Supply	Voltage and Curren	t					
Vs	Operating Voltage I	Range	T <sub>A</sub> = -40°C to 125°C	2.7		36	V
ΙQ	Quiescent Current		V <sub>SENSE</sub> = 0 mV, T <sub>A</sub> = −40°C to 125°C		80	115	μA
Input							
\/	Inner Office t Voltage		V <sub>SENSE</sub> = 0 mV	-180		180	/
Vos	Input Offset Voltage		V <sub>IN+</sub> = 2.75 V, V <sub>SENSE</sub> = 0 mV	-50		50	μV
Vos TC	Input Offset Voltage	e Drift	T <sub>A</sub> = −40°C to 125°C		0.2		μV/°C
V <sub>CM</sub>	Common-Mode Inp	ut Range	T <sub>A</sub> = −40°C to 125°C	-0.3		36	V
CMRR	Common Mode Rej	ection Ratio	$V_{IN+} = 0 \text{ V to } 26 \text{ V, } V_{SENSE} = 0 \text{ mV, } T_A$ = -40°C to 125°C	95	120		dB
I <sub>B</sub>	Input Bias Current		V <sub>SENSE</sub> = 0 mV		22		μA
Ios	Input Offset Curren	t	V <sub>SENSE</sub> = 0 mV		±0.05		μA
PSRR	Power Supply Rejection Ratio		$V_S = 2.7 \text{ V to } 18 \text{ V, } V_{IN+} = 18 \text{ V,}$ $V_{SENSE} = 0 \text{ mV, } T_A = -40^{\circ}\text{C to } 125^{\circ}\text{C}$	100	128		dB
Output				<b>'</b>			
		TPA191A1			20		
		TPA191A2			50		
		TPA191A3			75		
G	Gain	TPA191A4			100		V/V
		TPA191A5			200		
		TPA191A6			500		
		TPA191A7			1000		
GE	Gain Error		$V_{SENSE} = -5 \text{ mV to } 5 \text{ mV},$ $T_A = -40^{\circ}\text{C to } 125^{\circ}\text{C}$		±0.05	±1	%
GE TC	Gain Error vs Temp	erature	T <sub>A</sub> = -40°C to 125°C		3	10	ppm/°
NE	Nonlinearity Error		V <sub>SENSE</sub> = -5 mV to 5 mV		±0.05		%
CLOAD	Maximum Capacitive Load		No sustained oscillation		1		nF
V <sub>OH</sub>	Output Swing from	Supply Rail	$R_L = 10 \text{ k}\Omega \text{ to GND},$ $T_A = -40^{\circ}\text{C to } 125^{\circ}\text{C}$		0.05	0.11	V
V <sub>OL</sub>	Output Swing from	Supply Rail	$R_L = 10 \text{ k}\Omega \text{ to GND},$ $T_A = -40^{\circ}\text{C to } 125^{\circ}\text{C}$		0.01	0.05	V



### **Electrical Characteristics (continued)**

All test conditions:  $T_A = 27^{\circ}C$ ,  $V_S = 5.5$  V,  $V_{IN+} = 12$  V, and  $V_{SENSE} = V_{IN+} - V_{IN-}$ , and  $V_{REF} = V_S / 2$ , unless otherwise noted.

	Par	ameter	Conditions	Min	Тур	Max	Unit
Freque	ency Response						
		TPA191A1			150		
		TPA191A2			80		
		TPA191A3			50		kHz
GBW	Bandwidth	TPA191A4	C <sub>LOAD</sub> = 10 pF		30		
		TPA191A5			15		
		TPA191A6					
		TPA191A7					
SR	Slew Rate		T <sub>A</sub> = -40°C to 125°C	1		2.5	V/µs
Noise,	RTI			·			
en	Input Voltage Noise Density				35		nV/ √Hz
Tempe	rature Range						
	Specified Range			-40		125	°C
	Operating Rang	je		-40		125	°C



### **Typical Performance Characteristics**

All test conditions:  $T_A = 25$ °C,  $V_S = 5$  V,  $V_{IN+} = 12$  V, and  $V_{REF} = V_S / 2$ , unless otherwise noted.

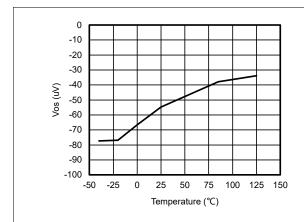


Figure 1. Offset Voltage vs Temperature

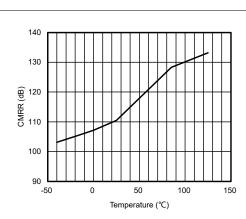


Figure 2. Common-Mode Rejection Ratio vs Temperature

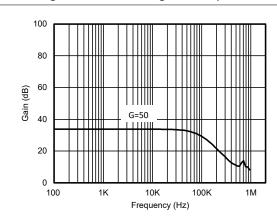


Figure 3. Gain vs Frequency

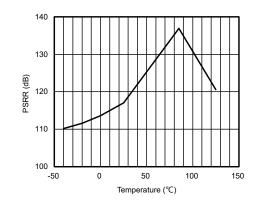


Figure 4. Power-Supply Rejection Ratio vs Temperature

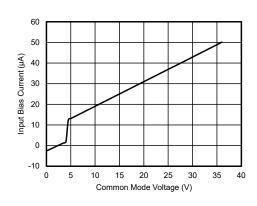


Figure 5. Input Bias Current vs Common-Mode Voltage

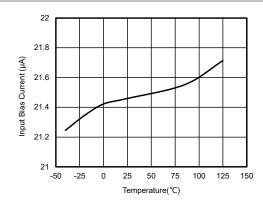


Figure 6. Input Bias Current vs Temperature



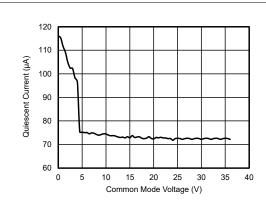


Figure 7. Quiescent Current vs Common-Mode Voltage

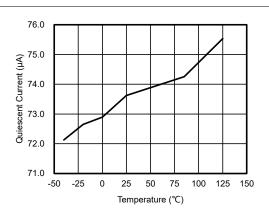


Figure 8. Quiescent Current vs Temperature

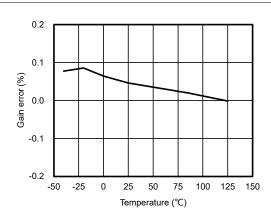


Figure 9. Gain error vs Temperature

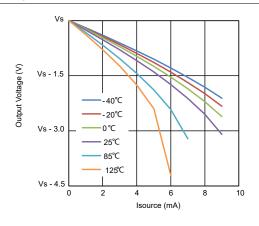


Figure 10. Output Voltage Swing vs Isource

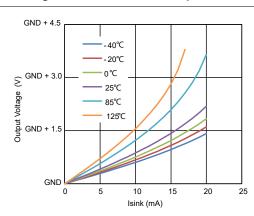


Figure 11. Output Voltage Swing vs Isink

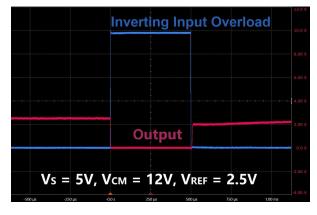
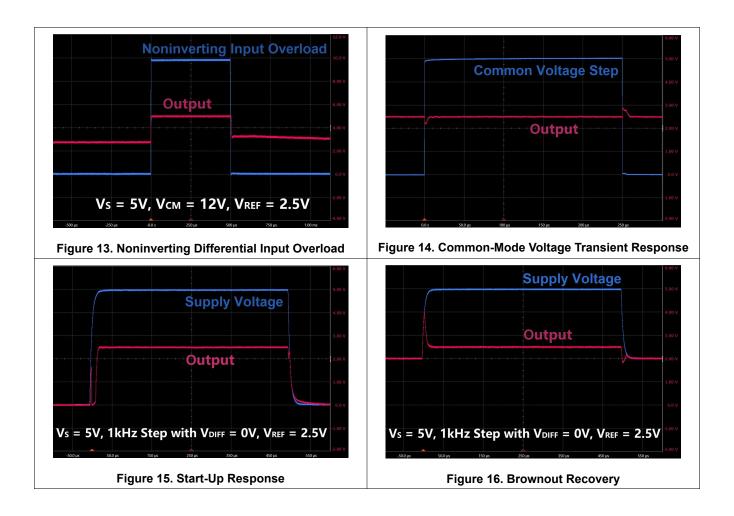


Figure 12. Inverting Differential Input Overload







### **Detailed Description**

#### Overview

The TPA191 family features a high-accuracy unidirectional, current-sense amplifier in various gain options, and a -0.3-V to 36-V input common-mode range that is independent of supply voltage ( $V_s$ ). The low input offset voltage, tight gain error, and low-temperature drift characteristics allow the use of small-sense resistors for current measurements to improve power-supply conversion efficiency and accuracy of measurements. This feature allows monitoring power-supply load current even if the rail is shorted to ground. High-side current monitoring does not interfere with the ground path of the load measured, making the IC particularly useful in a wide range of high-reliability systems. Because of its extended common-mode range below ground, the TPA191 can also be used as a low-side current sensing element.

#### **Functional Block Diagram**

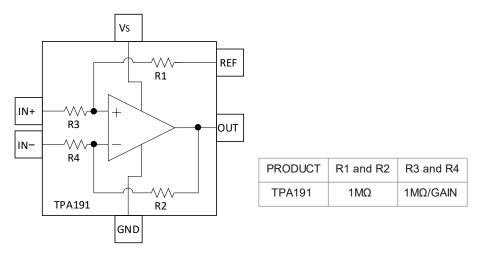


Figure 17. Functional Block Diagram

#### **Feature Description**

#### Wide Input Common-Mode Voltage Range

Because of the internal topology, the TPA191 supports -0.3 V to 36 V input common-mode voltage that is independent of the supply voltage (Vs). The ability to operate with common-mode voltages greater or less than Vs allows the TPA191 to be used in high-side, as well as low-side current-sensing applications.

#### Reference Input, REF

The TPA191 supports both unidirectional and bidirectional current-sensing operations. Connecting the reference input (REF) to ground configures the TPA191 for unidirectional current sensing. For unidirectional current sensing, the output is referenced to ground and the output voltage  $V_{OUT}$  is proportional to the positive voltage drop ( $V_{SENSE}$ ) from IN+ to IN-. The TPA191 operates as a bidirectional Current-Sense-Amplifier (CSA) by the application of a low source impedance reference voltage to REF above ground, typically  $V_S/2$ . In the bidirectional current-sensing mode of operation, the output voltage  $V_{OUT}$  is referenced to  $V_{REF}$ .



## **Application and Implementation**

#### Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

#### **Application Information**

The TPA191 monitors the current through a current-sense resistor and amplifes the voltage across the resistor. The 36-V input common-mode voltage range of the TPA191 is independent of the supply voltage. It is a bidirectional, current-sense amplifier capable of measuring currents through a resistive shunt in two directions.

#### **Typical Application**

Figure 18 and Figure 19 show the typical application schematics of Unidirectional and Bidirectional applications.

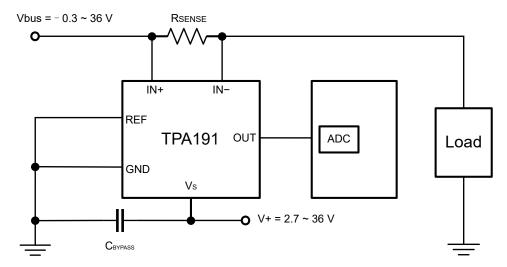


Figure 18. Unidirectional Application Schematic

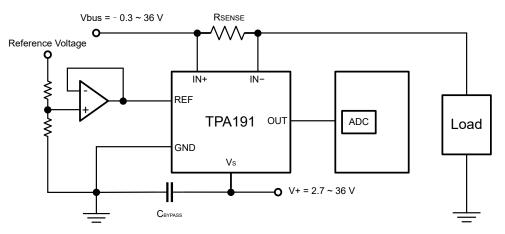


Figure 19. Bidirectional Application Schematic



#### **Bidirectional and Unidirectional Operation**

The TPA191 series of products are capable of both unidirectional and bidirectional operations. For unidirectional current-sense applications, connect the REF input to GND. For bidirectional, connect REF to a reference. This sets bidirectional current sense with  $V_{OUT} = V_{REF}$  for  $V_{SENSE} = 0$  mV. Positive  $V_{SENSE}$  causes OUT to swing toward the positive supply, while negative  $V_{SENSE}$  causes OUT to swing toward GND. This feature allows the output voltage to measure both charge and discharge currents. Use  $V_{REF} = V_{S}/2$  for the maximum dynamic range.

Battery-powered systems require a precise bidirectional current-sense amplifier to accurately monitor the battery's charge and discharge currents. Measurements of OUT with respect to V<sub>REF</sub> yield a positive and negative voltages during charge and discharge cycles.

#### **Choosing the Sense Resistor**

A high  $R_{\text{SENSE}}$  value causes the power-source voltage to drop due to IR loss. For the minimal voltage loss, use the lowest  $R_{\text{SENSE}}$  value. At high current levels, the I<sup>2</sup>R losses in  $R_{\text{SENSE}}$  can be significant. This should be taken into consideration when choosing the resistor value and its power dissipation (wattage) rating. The sense resistor's value will drift if it is allowed to heat up excessively. A high  $R_{\text{SENSE}}$  value allows lower currents to be measured more accurately because offsets are less significant when the sense voltage is larger. Note that the tolerance and temperature coefficient of the chosen resistors directly affect the precision of any measurement system. For best performance, select  $R_{\text{SENSE}}$  to provide approximately the maximum input differential sense voltage with full-scale output voltage for each application. Sense resistors of 5 m $\Omega$  to 100 m $\Omega$  are available with 1% accuracy or better.



### Layout

#### **Layout Guideline**

- Because the high currents may flow through R<sub>SENSE</sub> based on the application, take care to eliminate solder and parasitic
  trace resistance from causing errors in the sense voltage. Either use a four-terminal current sense resistor or use Kelvin
  (force and sense) PCB layout techniques.
- Make sure the sense resistor has as much copper trace area as possible to dissipate heat as the resistor value will
  change slightly with temperature. Also see the resistor manufacturers datasheet or application notes for further layout
  guidelines.
- The power-supply bypass capacitor should be placed as closely as possible to the supply and ground. The recommended value of this bypass capacitor is 0.1 μF. Additional decoupling capacitance can be added to compensate for noisy or high-impedance power supplies.

#### **Layout Example**

Figure 20 shows the location of external components as they appear on the PCB.diagram.

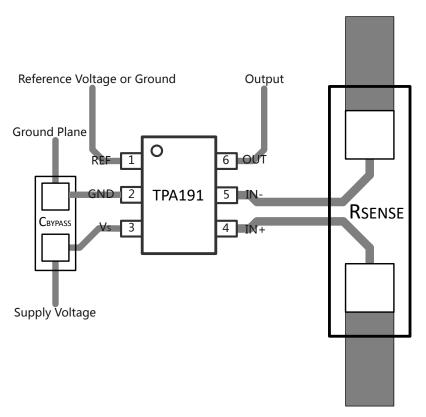
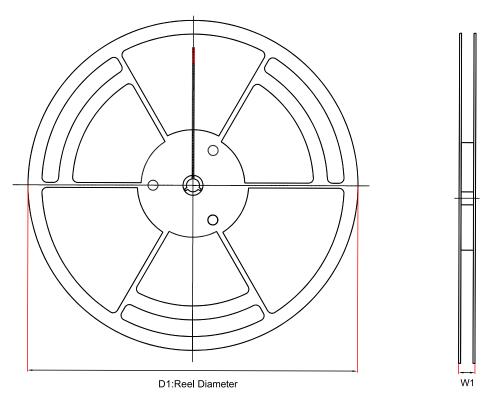
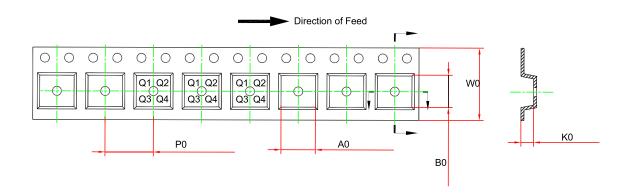


Figure 20. Recommended Layout



# **Tape and Reel Information**



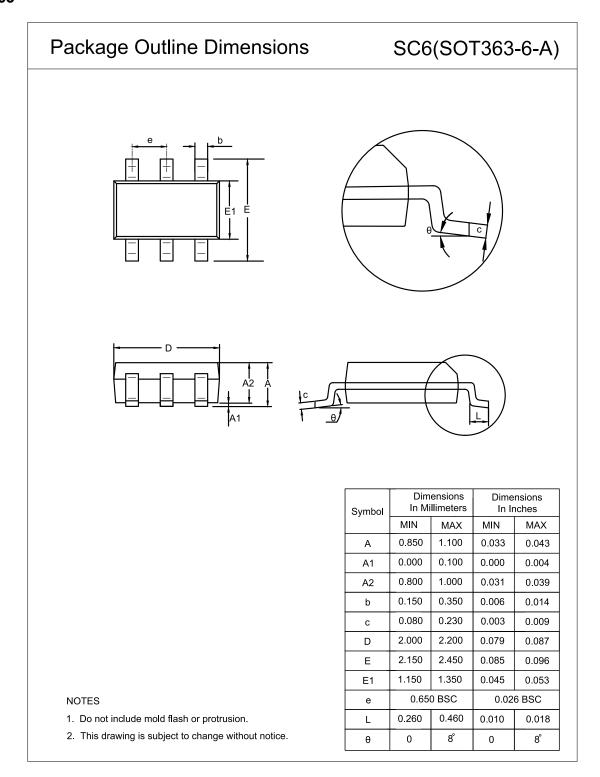


Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	W0 (mm)	Pin1 Quadran t
TPA191Ax-SC6R	SOT363	178.0	12.1	2.4	2.5	1.2	4.0	8.0	Q3



## **Package Outline Dimensions**

#### **SOT363**





### **Order Information**

Order Number	Gain Option	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPA191A1-SC6R (1)	20	−40 to 125°C	SOT363	1A1	1	Tape and Reel, 3000	Green
TPA191A2-SC6R	50	−40 to 125°C	SOT363	1A2	1	Tape and Reel, 3000	Green
TPA191A3-SC6R (1)	75	−40 to 125°C	SOT363	1A3	1	Tape and Reel, 3000	Green
TPA191A4-SC6R	100	−40 to 125°C	SOT363	1A4	1	Tape and Reel, 3000	Green
TPA191A5-SC6R	200	−40 to 125°C	SOT363	1A5	1	Tape and Reel, 3000	Green
TPA191A6-SC6R <sup>(1)</sup>	500	−40 to 125°C	SOT363	1A6	1	Tape and Reel, 3000	Green
TPA191A7-SC6R <sup>(1)</sup>	1000	−40 to 125°C	SOT363	1A7	1	Tape and Reel, 3000	Green

<sup>(1)</sup> For future products, contact the 3PEAK factory for more information and samples.

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.



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