

### Features

- Supply Voltage: 3V to 36V
- Low Supply Current: 100µA per channel
- Input Common-Mode Voltage Range Includes Ground
- Can Work as Comparator
- Rail to Rail Output
- Bandwidth: 0.9 MHz
- Slew Rate: 0.5V/µs
- Excellent EMI Suppress Performance: 71dB at 1GHz
- Offset Voltage: ±3mV Maximum
- Offset Voltage Temperature Drift: 7 µV/°C
- -40°C to 125°C Operation Temperature Range

### Applications

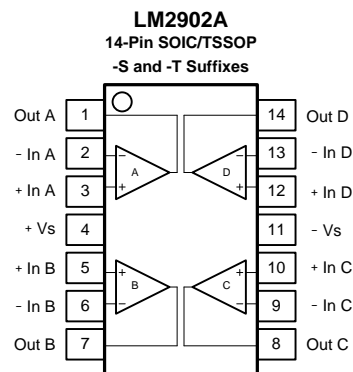
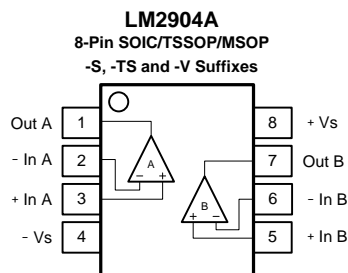
- Power Module
- Sensor Interface
- Motor Control
- Audio

### Description

The LM2904A/2902A series amplifiers are newest high supply voltage amplifiers with low offset, low power and stable frequency response. They incorporate 3PEAK’s proprietary and patented design techniques to achieve very good AC performance with 0.9MHz bandwidth, 0.5V/µs slew rate and maximum 3mV offset while drawing only 100µA of quiescent current per amplifier. The input common-mode voltage range extends to V-, and the outputs swing rail-to-rail. The LM2904A/2902A family can be used as plug-in replacements for many commercially available op-amps to reduce power and improve input/output range and performance.

The combination of features makes the LM2904A/2902A ideal choices for power module, industrial control, motor control and audio application.

### Pin Configuration



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## Revision History

Date	Revision	Notes
2018/3/21	Rev.Pre	Pre-Release Version
2018/10/30	Rev.0	Initial Version
2018/11/11	Rev.0.01	Add LM2902AL1-SR, Correct the condition of VOL,VOH to VS/2, Add mark information.

## Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity
LM2904A-SR	-40 to 125°C	8-Pin SOIC	2904A AAYW <sup>Note 1</sup>	3	Tape and Reel, 4000
LM2904AL1-SR	-40 to 125°C	8-Pin SOIC	2904A AAYW L <sup>Note 1</sup>	1	Tape and Reel, 4000
LM2904A-TSR	-40 to 125°C	8-Pin TSSOP	2904A AAYW <sup>Note 1</sup>	3	Tape and Reel, 3000
LM2904A-VR	-40 to 125°C	8-Pin MSOP	2904A AAYW <sup>Note 1</sup>	3	Tape and Reel, 3000
LM2902A-SR	-40 to 125°C	14-Pin SOIC	2902A AAYW <sup>Note 1</sup>	3	Tape and Reel, 2500
LM2902AL1-SR	-40 to 125°C	14-Pin SOIC	2902A AAYW L <sup>Note 1</sup>	1	Tape and Reel, 2500
LM2902A-TR	-40 to 125°C	14-Pin TSSOP	2902A AAYW <sup>Note 1</sup>	3	Tape and Reel, 3000

Note 1: "AA" identify the manufacture site. "YW" is the date code means manufacture year and week as following, "L" means MSL1 product.

The calendar year and the workweek coding scheme is as follows:

Year	Code	Year	Code	Workweek Code	Workweek Code	Workweek Code	Workweek Code	Workweek Code	
2010	A	2023	N	1	1	14	E	27	R
2011	B	2024	O	2	2	15	F	28	S
2012	C	2025	P	3	3	16	G	29	T
2013	D	2026	Q	4	4	17	H	30	U
2014	E	2027	R	5	5	18	I	31	V
2015	F	2028	S	6	6	19	J	32	W
2016	G	2029	T	7	7	20	K	33	X
2017	H	2030	U	8	8	21	L	34	Y
2018	I	2031	V	9	9	22	M	35	Z
2019	J	2032	W	10	A	23	N	36	a
2020	K	2033	X	11	B	24	O	37	b
2021	L	2034	Y	12	C	25	P	38	c
2022	M	2035	Z	13	D	26	Q	39	d
								40	e
								41	f
								42	g
								43	h
								44	i
								45	j
								46	k
								47	l
								48	m
								49	n
								50	o
								51	p
								52	q
								53	r

## Absolute Maximum Ratings <sup>Note 1</sup>

Parameters	Rating
Supply Voltage, (+V <sub>S</sub> )– (-V <sub>S</sub> )	40 V
Input Voltage	(-V <sub>S</sub> ) – 0.3 to (+V <sub>S</sub> ) + 0.3
Differential Input Voltage	(+V <sub>S</sub> ) - (-V <sub>S</sub> )
Input Current: +I <sub>N</sub> , –I <sub>N</sub> <sup>Note 2</sup>	±10mA
Output Short-Circuit Duration <sup>Note 3</sup>	Infinite
Maximum Junction Temperature	150°C
Operating Temperature Range	–40 to 125°C
Storage Temperature Range	–65 to 150°C
Lead Temperature (Soldering, 10 sec)	260°C

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

Note 2: The inputs are protected by ESD protection diodes to each power supply. If the input extends more than 300mV beyond the power supply, the input current should be limited to less than 10mA.

Note 3: A heat sink may be required to keep the junction temperature below the absolute maximum. This depends on the power supply voltage and how many amplifiers are shorted. Thermal resistance varies with the amount of PC board metal connected to the package. The specified values are for short traces connected to the leads.

## ESD Rating

Symbol	Parameter	Condition	Minimum Level	Unit
HBM	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001	3	kV
CDM	Charged Device Model ESD	ANSI/ESDA/JEDEC JS-002	2	kV

## Thermal Information

Package Type	$\theta_{JA}$	$\theta_{JC}$	Unit
8-Pin SOIC	158	43	°C/W
8-Pin TSSOP	191	44	°C/W
8-Pin MSOP	210	45	°C/W
14-Pin SOIC	120	36	°C/W
14-Pin TSSOP	180	35	°C/W

## Electrical Characteristics

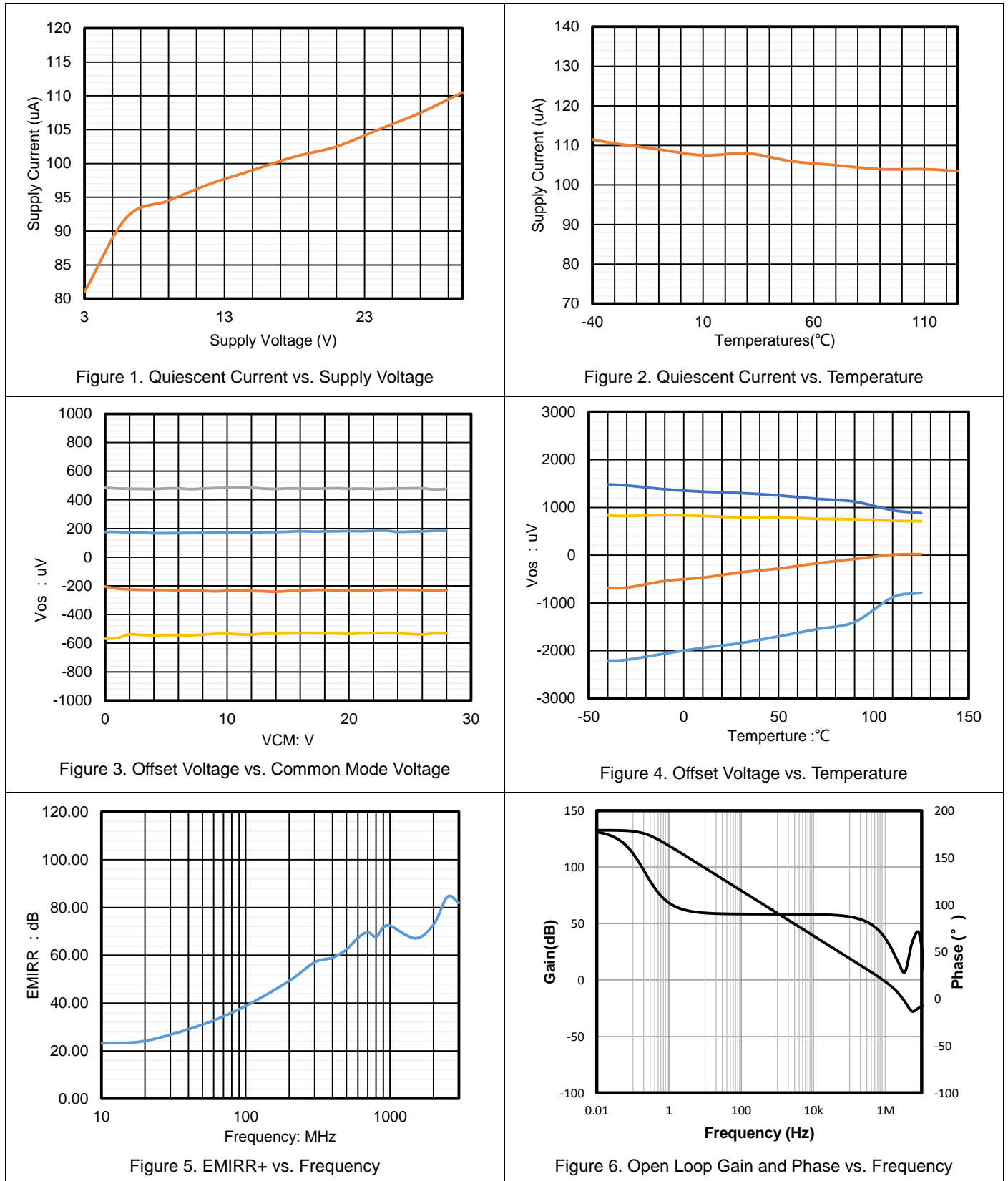
All test condition is  $V_S = 30V$ ,  $T_A = 25^\circ C$ ,  $R_L = 10k\Omega$ ,  $C_L = 100pF$ , unless otherwise noted.

Symbol	Parameter	Conditions	$T_A$	Min	Typ	Max	Unit
<b>Power Supply</b>							
$V_S$	Supply Voltage Range			3		36	V
$I_Q$	Quiescent Current per Amplifier	$V_S = 30V$			110	200	$\mu A$
			Operating Range			250	$\mu A$
		$V_S = 5V$			100	150	$\mu A$
			Operating Range			200	$\mu A$
PSRR	Power Supply Rejection Ratio	$V_S = 5V$ to $36V$		85	120		dB
			Operating Range	80			dB
<b>Input Characteristics</b>							
$V_{OS}$	Input Offset Voltage	$V_S = 30V$ , $V_{CM} = 0V$ to $28V$		-3	0.1	3	mV
			Operating Range	-7		7	mV
		$V_S = 5V$ , $V_{CM} = 0V$ to $3V$		-3	0.1	3	mV
			Operating Range	-7		7	mV
$V_{OS\ TC}$	Input Offset Voltage Drift		Operating Range		7		$\mu V/^\circ C$
$I_B$	Input Bias Current				60		$\mu A$
			Operating Range			600	
$I_{OS}$	Input Offset Current				60		$\mu A$
$I_{IN}$	Different Input Current	$V_S = 36V$ , $V_{ID} = 36V$			5		nA
		$V_S = 36V$ , $V_{ID} = 36V$	Operating Range		20		nA
$C_{IN}$	Input Capacitance	Differential Mode			5		pF
		Common Mode			5		pF
$A_v$	Open-loop Voltage Gain			95	110		dB
			Operating Range	90			dB
$V_{CMR}$	Common-mode Input Voltage Range			(V-)		(V+) - 2	V
CMRR	Common Mode Rejection Ratio	$V_{CM} = 0V$ to $28V$		75	120		dB
			Operating Range	70	90		dB
<b>Output Characteristics</b>							
$V_{OH}, V_{OL}$	Maximum Output Voltage Swing	$R_{LOAD} = 10k\Omega$ to $V_S/2$			200	300	mV
		$R_{LOAD} = 2k\Omega$ to $V_S/2$			1.1	1.3	V
$V_{OL}$	Maximum Output Voltage Swing Low	$V_S = 5V$ , $R_{LOAD} = 10k\Omega$ to $0V$			5	10	mV
$I_{SC}$	Output Short-Circuit Current				30		mA
<b>AC Specifications</b>							
GBW	Gain-Bandwidth Product				0.9		MHz
SR	Slew Rate	$G = 1$ , $2V$ step			0.5		V/ $\mu s$
$t_s$	Settling Time, 0.1%	$G = 1$ , $2V$ step			4		$\mu s$

	Settling Time, 0.01%				5		$\mu\text{s}$
PM	Phase Margin	$V_S = 30\text{V}, R_L = 1\text{K}, C_L = 100\text{pF}$			60		$^\circ$
GM	Gain Margin	$V_S = 30\text{V}, R_L = 1\text{K}, C_L = 100\text{pF}$			15		dB
	Channel Separation	$f = 1\text{ kHz to } 20\text{ kHz}$			120		dB
<b>Noise Performance</b>							
$E_N$	Input Voltage Noise	$f = 0.1\text{Hz to } 10\text{Hz}$			3		$\mu\text{V}_{\text{RMS}}$
$e_N$	Input Voltage Noise Density	$f = 1\text{kHz}$			70		$\text{nV}/\sqrt{\text{Hz}}$
$i_N$	Input Current Noise	$f = 1\text{kHz}$			3		$\text{fA}/\sqrt{\text{Hz}}$
THD+N	Total Harmonic Distortion and Noise	$f = 1\text{kHz}, G = 1, R_L = 10\text{k}\Omega,$ $V_{\text{OUT}} = 6\text{V}_{\text{RMS}}$			0.001		%

### Typical Performance Characteristics

$V_S = \pm 15V$ ,  $V_{CM} = 0V$ ,  $R_L = 10k\Omega$ , unless otherwise specified.



$V_s = \pm 15V$ ,  $V_{CM} = 0V$ ,  $R_L = 10k\Omega$ , unless otherwise specified.

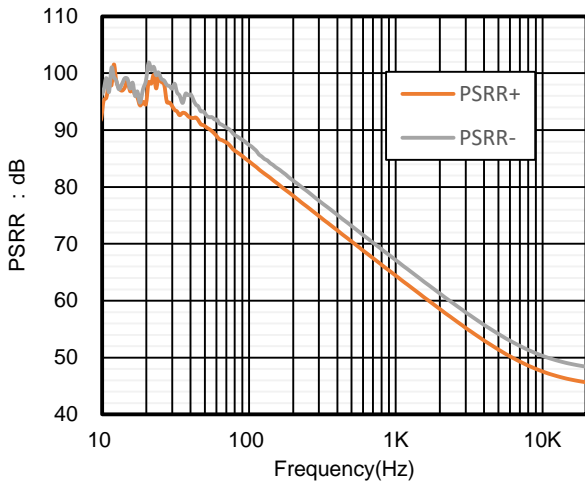


Figure 7. PSRR vs. Frequency

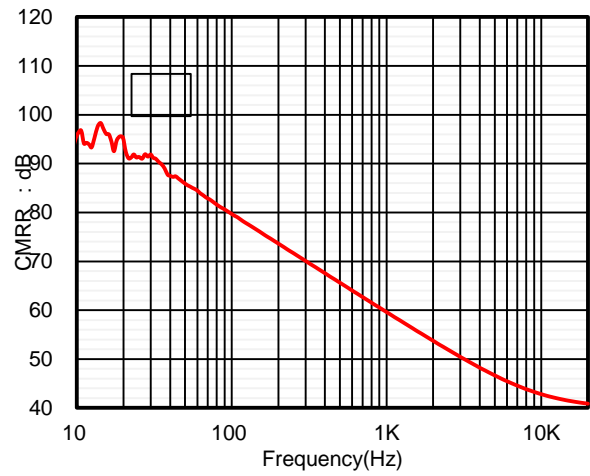


Figure 8. CMRR vs. Frequency

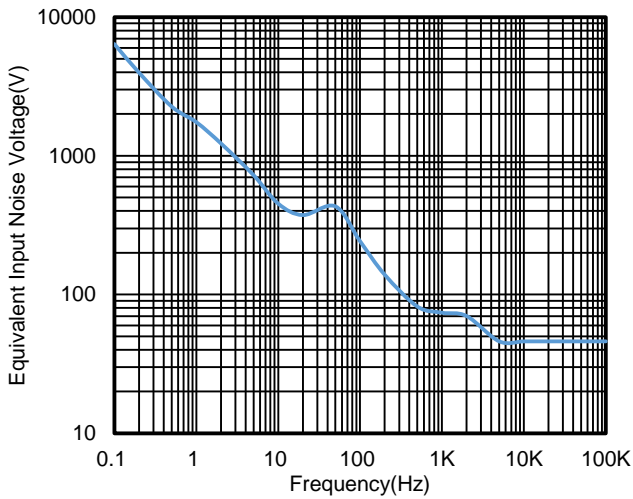


Figure 9. Voltage Noise Spectral Density vs. Frequency

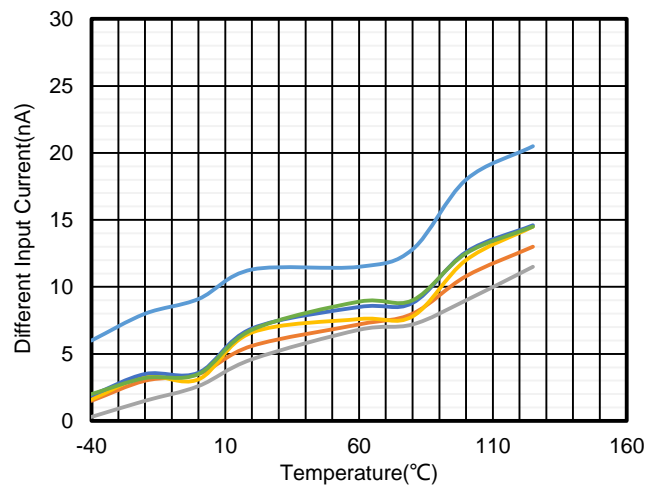
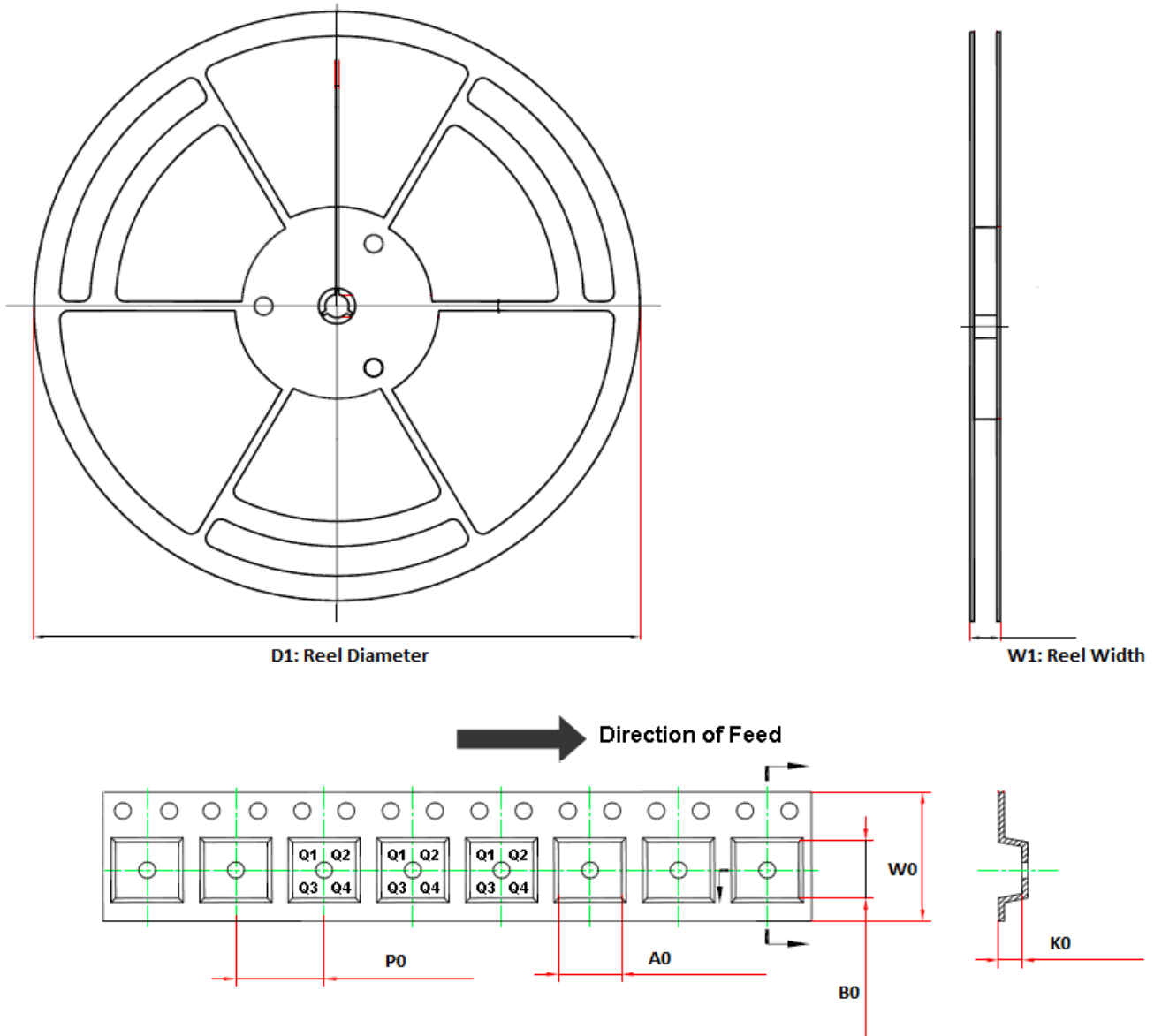


Figure 10. Different Input Current vs. Temperature



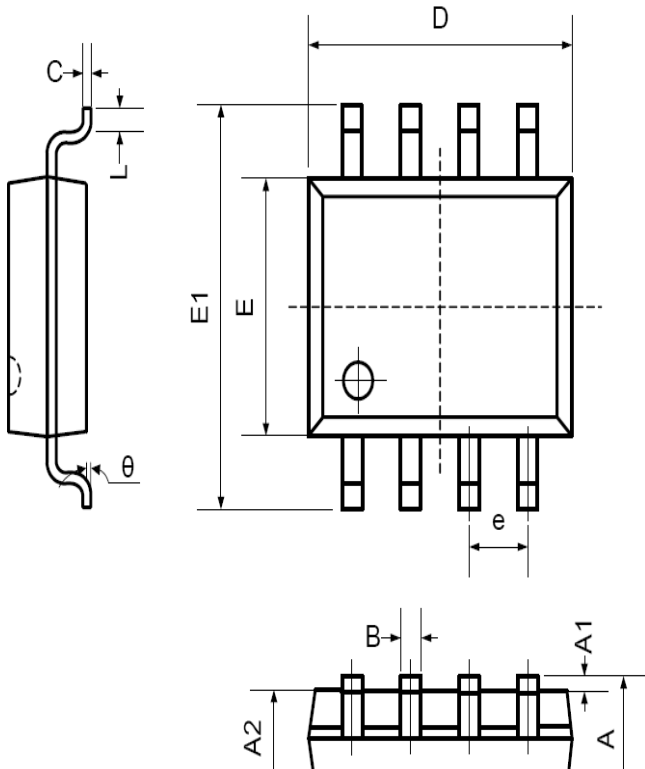
### Tape and Reel Information



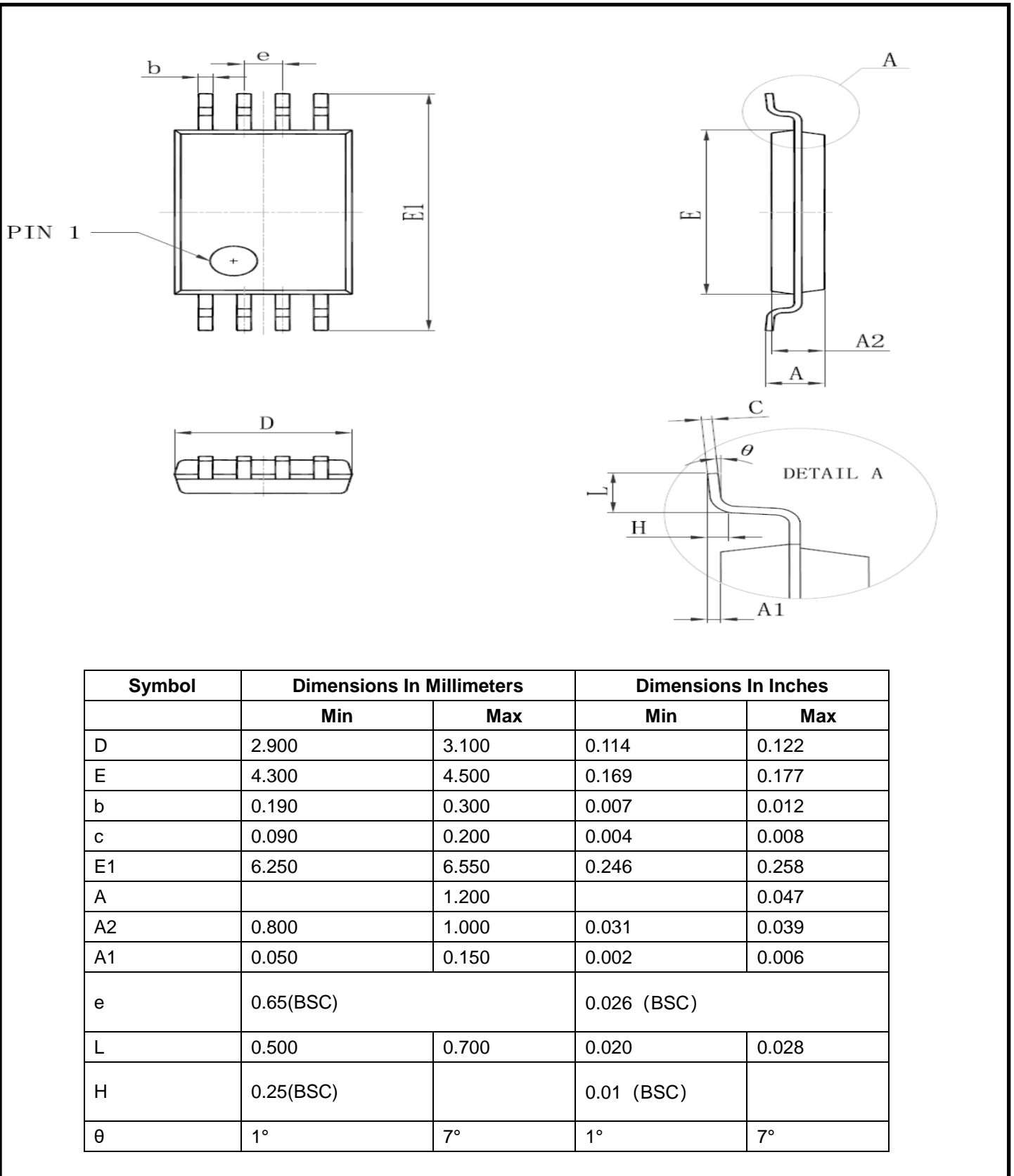
Order Number	Package	D1	W1	A0	B0	K0	P0	W0	Pin1 Quadrant
LM2904A-SR	8-Pin SOIC	330.0	17.6	6.4	5.4	2.1	8.0	12.0	Q1
LM2904AL1-SR	8-Pin SOIC	330.0	17.6	6.4	5.4	2.1	8.0	12.0	Q1
LM2904A-VR	8-Pin MSOP	330.0	17.6	5.2	3.3	1.5	8.0	12.0	Q1
LM2904A-TSR	8-Pin TSSOP	330.0	17.6	6.8	3.3	1.2	8.0	12.0	Q1
LM2902A-SR	14-Pin SOIC	330.0	21.6	6.5	9.0	2.1	8.0	16.0	Q1
LM2902AL1-SR	14-Pin SOIC	330.0	21.6	6.5	9.0	2.1	8.0	16.0	Q1
LM2902A-TR	14-Pin TSSOP	330.0	17.6	6.8	5.4	1.2	8.0	12.0	Q1

Package Outline Dimensions

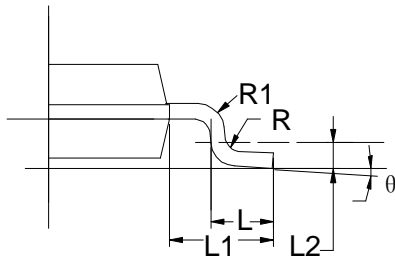
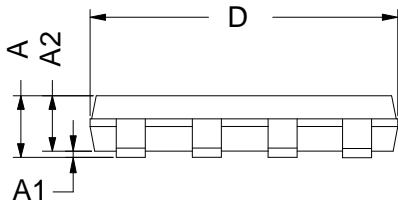
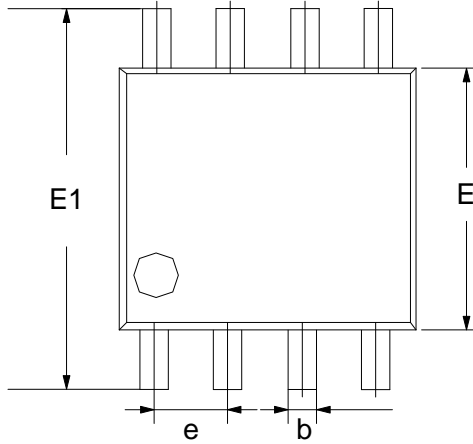
SOIC-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
B	0.330	0.510	0.013	0.020
C	0.190	0.250	0.007	0.010
D	4.780	5.000	0.188	0.197
E	3.800	4.000	0.150	0.157
E1	5.800	6.300	0.228	0.248
e	1.270TYP		0.050TYP	
L1	0.400	1.270	0.016	0.050
theta	0°	8°	0°	8°

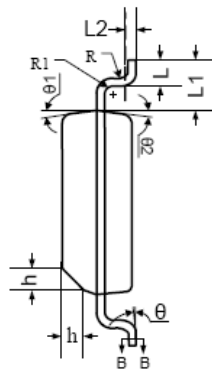
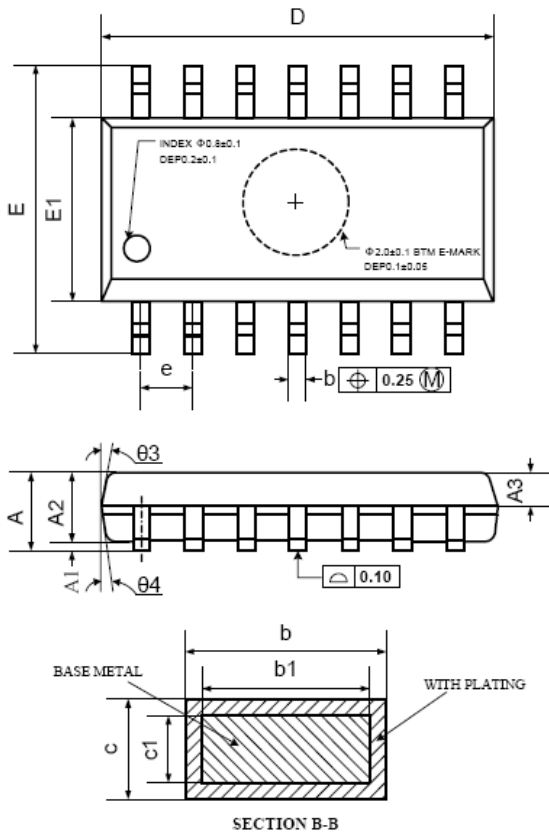
**TSSOP-8**


MSOP-8



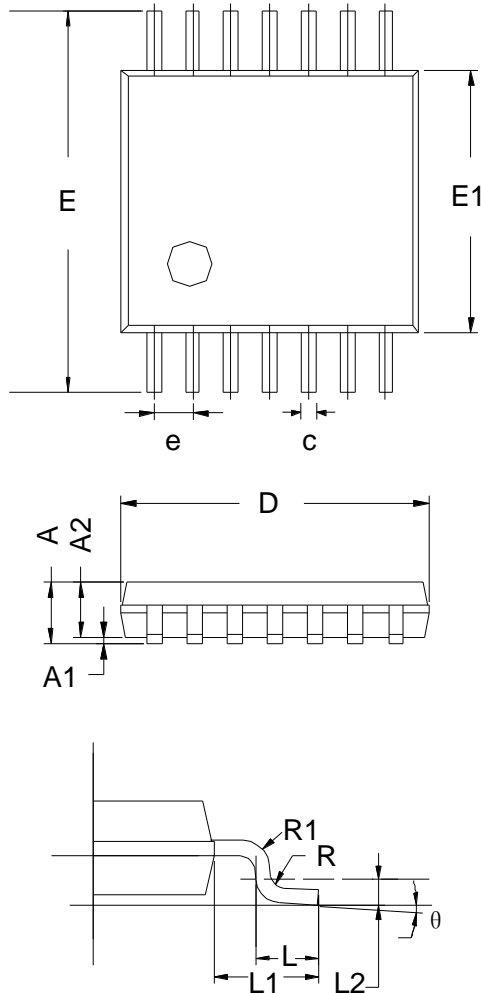
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.800	1.200	0.031	0.047
A1	0.000	0.200	0.000	0.008
A2	0.760	0.970	0.030	0.038
b	0.30 TYP		0.012 TYP	
C	0.15 TYP		0.006 TYP	
D	2.900	3.100	0.114	0.122
e	0.65 TYP		0.026	
E	2.900	3.100	0.114	0.122
E1	4.700	5.100	0.185	0.201
L1	0.410	0.650	0.016	0.026
θ	0°	6°	0°	6°

### SOIC-14



Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	1.35	1.60	1.75
A1	0.10	0.15	0.25
A2	1.25	1.45	1.65
A3	0.55	0.65	0.75
b	0.36		0.49
b1	0.35	0.40	0.45
c	0.16		0.25
c1	0.15	0.20	0.25
D	8.53	8.63	8.73
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.27 BSC		
L	0.45	0.60	0.80
L1	1.04 REF		
L2	0.25 BSC		
R	0.07		
R1	0.07		
h	0.30	0.40	0.50
$\theta$	0°		8°
$\theta 1$	6°	8°	10°
$\theta 2$	6°	8°	10°
$\theta 3$	5°	7°	9°
$\theta 4$	5°	7°	9°

TSSOP-14



Symbol	Dimensions In Millimeters		
	MIN	TYP	MAX
A	-	-	1.20
A1	0.05	-	0.15
A2	0.90	1.00	1.05
b	0.20	-	0.28
c	0.10	-	0.19
D	4.86	4.96	5.06
E	6.20	6.40	6.60
E1	4.30	4.40	4.50
e	0.65 BSC		
L	0.45	0.60	0.75
L1	1.00 REF		
L2	0.25 BSC		
R	0.09	-	-
$\theta$	0°	-	8°

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