

40V 0.8mohm N-channel SGT MOSFET AKG4N008GM-A

Description:

This device is designed for automotive applications and manufactured in IATF16949 certified facilities. Qualified AEC-Q101, PPAP capable.

Features:

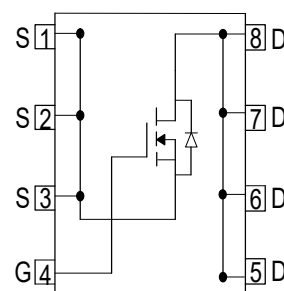
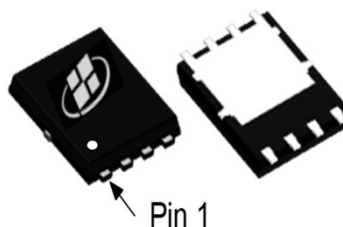
- Low $R_{DS(ON)}$
- 100% UIS Tested
- RoHS compliant ^(Note 1)
- Halogen-free ^(Note 1)
- AEC-Q101 qualified and PPAP capable

Applications:

- Battery Management System
- Motor Drivers

Key Performance Parameters:

Parameter	Value	Unit
V_{DS}	40	V
$R_{DS(ON), max} @V_{GS} = 10V$	0.8	m Ω
I_D	340	A



Ordering Information:

Ordering Code	Package Type	Marking Code	Form	Packing
AKG4N008GM-A	PDFN5X6	G4N008GM	Tape Reel	5000PCS

Notes:

1. Contact ALKAIDSEMI sales for detail information

Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Units
V_{DS}	Drain-Source Voltage	40	V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$) ^(Note 1)	340	A
	Drain Current - Continuous ($T_C = 100^\circ\text{C}$)	240	A
I_{DM}	Drain Current - Pulsed ^(Note 3)	900	A
V_{GS}	Gate-Source Voltage	± 20	V
E_{AS}	Single Pulsed Avalanche Energy ^(Note 4)	900	mJ
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	166	W
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +175	$^\circ\text{C}$

Thermal Characteristics

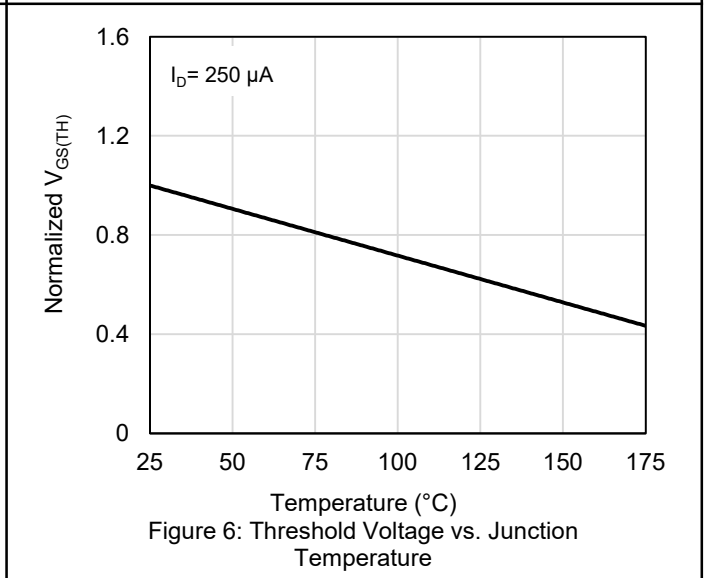
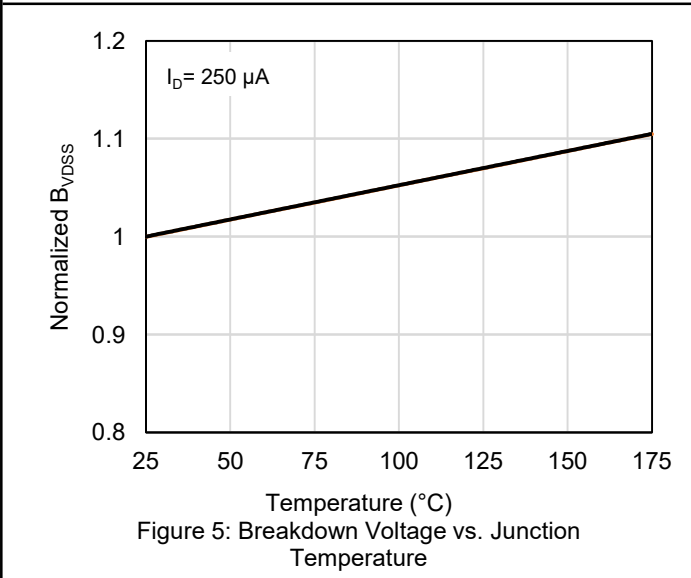
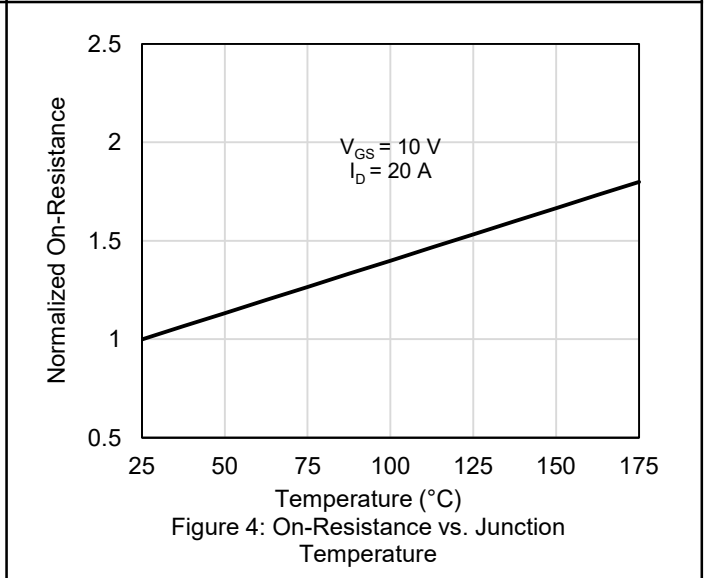
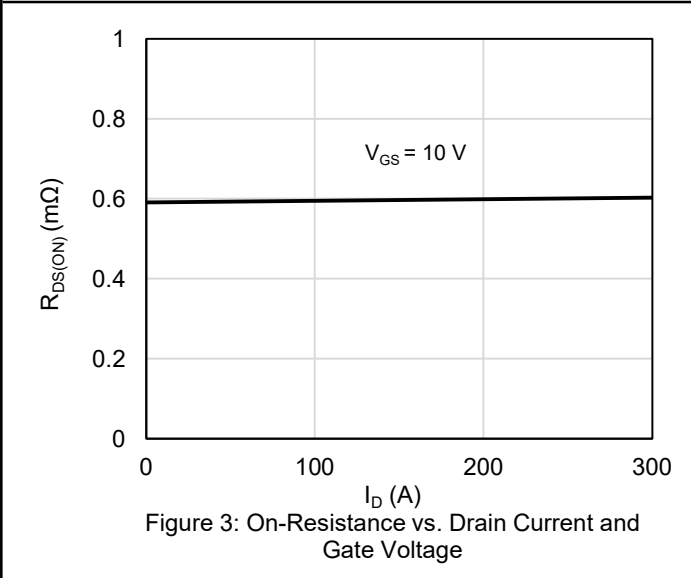
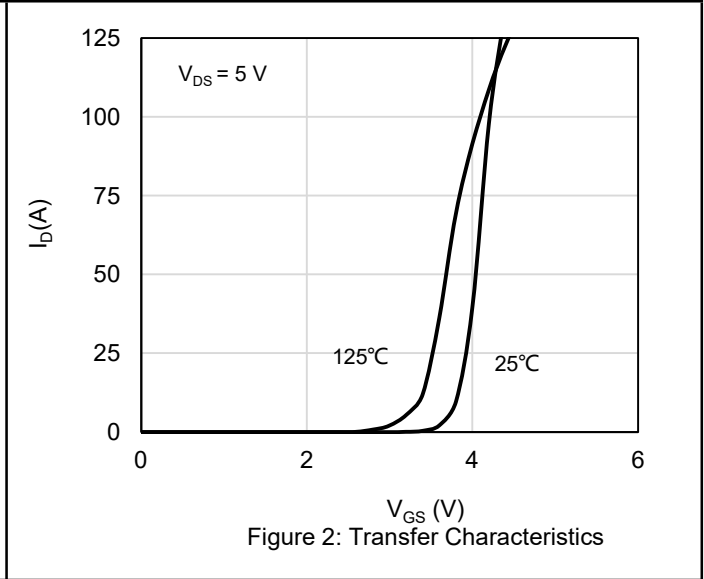
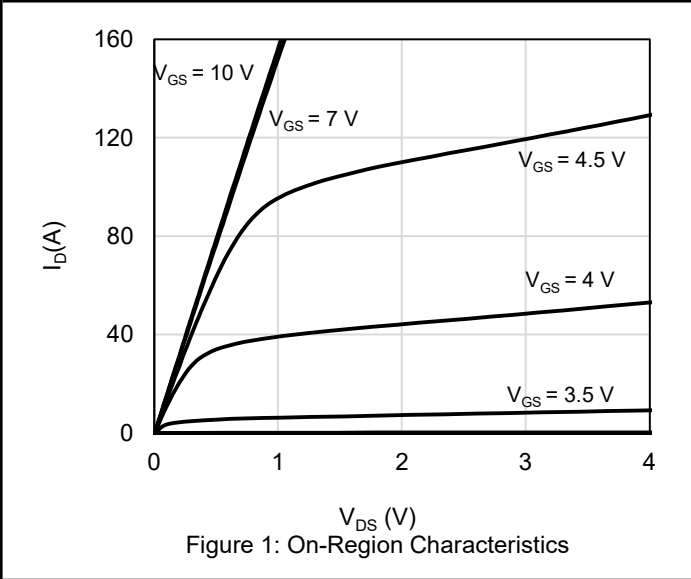
Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Steady-State	0.9	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Steady State ^(Note 5)	45	$^\circ\text{C/W}$

Notes:

1. The max drain current rating is silicon limited
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. $L = 0.5 \text{ mH}$, $V_{DD} = 20 \text{ V}$, $I_{AS} = 60 \text{ A}$, $R_G = 25 \Omega$, Starting $T_J = 25 \text{ }^\circ\text{C}$
4. Mount on minimum PCB layout

Electrical Characteristics (T _J = 25°C unless otherwise noted)							
Symbol	Parameter	Test Conditions	Min	Typ	Max	Units	
Static Characteristics							
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	40			V	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 40 V, V _{GS} = 0 V			1	μA	
		V _{DS} = 40 V, V _{GS} = 0 V, T _J = 125 °C			100	μA	
I _{GSS}	Gate Leakage Current	V _{GS} = ± 20 V, V _{DS} = 0 V			± 100	nA	
V _{GS(TH)}	Gate Threshold voltage	V _{DS} = V _{GS} , I _D = 250 μA	2	2.8	4	V	
R _{DS(ON)}	Drain-Source on-state resistance	V _{GS} = 10 V, I _D = 20 A		0.6	0.8	mΩ	
Dynamic Characteristics							
C _{ISS}	Input Capacitance	V _{DS} = 20 V, V _{GS} = 0 V, F = 1 MHz		8980		pF	
C _{OSS}	Output Capacitance				3560		pF
C _{RSS}	Reverse Transfer Capacitance				36		pF
R _G	Gate Resistance	F = 1 MHz		8		Ω	
Switching Characteristics							
T _{D(ON)}	Turn On Delay Time	V _{DD} = 32 V, R _L = 0.65 Ω, V _{GS} = 10 V, R _G = 2.5 Ω		20		nS	
T _R	Rise Time				67		nS
T _{D(OFF)}	Turn Off Delay Time				120		nS
T _F	Fall Time				78		nS
Q _G	Total Gate Charge	V _{DD} = 20 V, I _D = 50 A, V _{GS} = 10 V		110		nC	
Q _{GS}	Gate-Source Charge				48		nC
Q _{GD}	Gate-Drain Charge				18		nC
Drain-Source Diode Characteristics and Maximum Ratings							
I _S	Maximum Continuous Body-Diode Forward Current			340		A	
I _{SM}	Maximum Pulsed Body-Diode Forward Current ^(NOTE 1)			900		A	
V _{SD}	Diode Forward Voltage	V _{GS} = 0 V, I _S = 50 A		0.78		V	
T _{RR}	Reverse recovery time	V _{DD} = 20 V, I _D = 50 A, di/dt = 100 A/μS		103		nS	
Q _{RR}	Reverse recovery charge				240		nC
I _{RRM}	Peak Reverse Recovery Current				3.5		A

Electrical Characteristics Diagrams



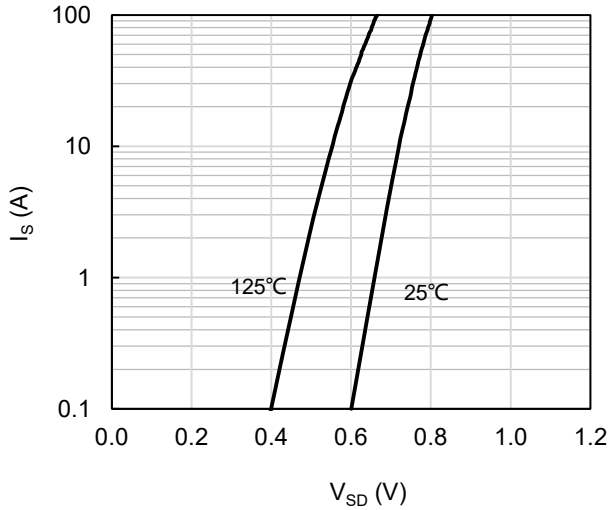


Figure 7: Body-Diode Characteristics

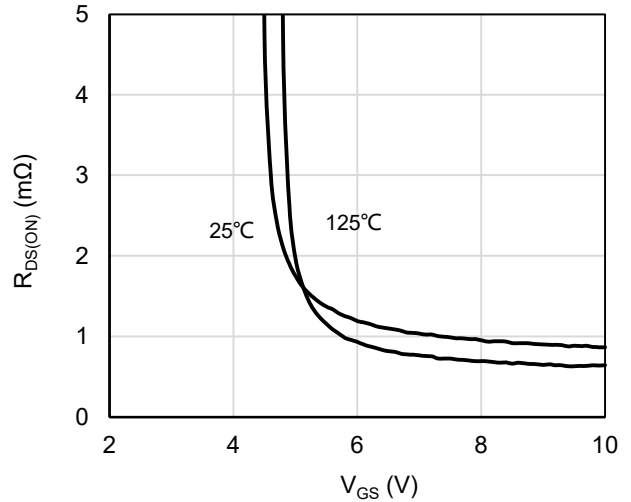


Figure 8: On-Resistance vs. Gate-Source Voltage

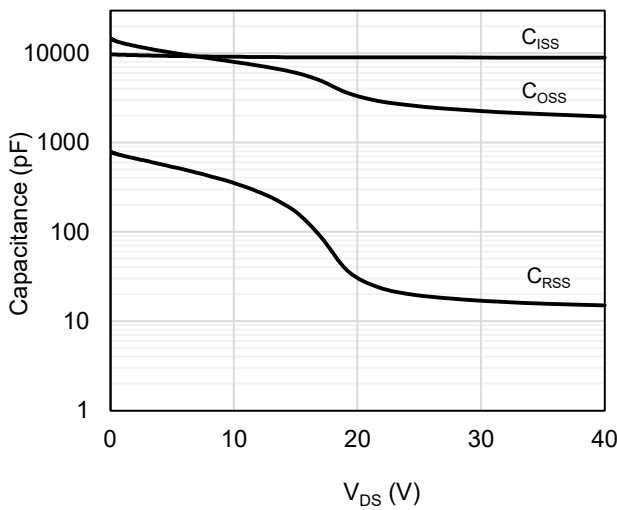


Figure 9: Capacitance Characteristics

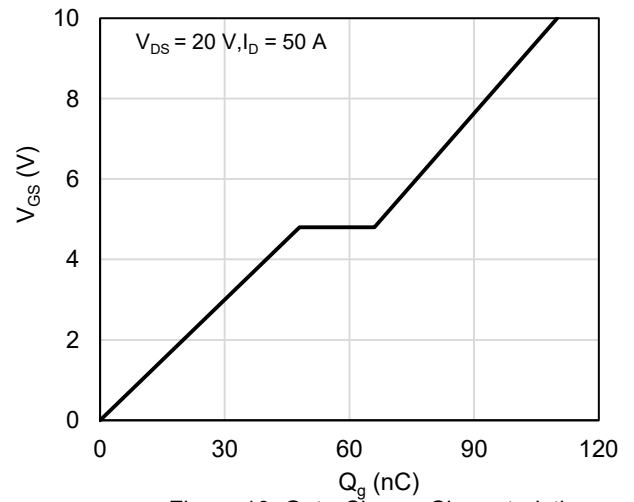


Figure 10: Gate-Charge Characteristics

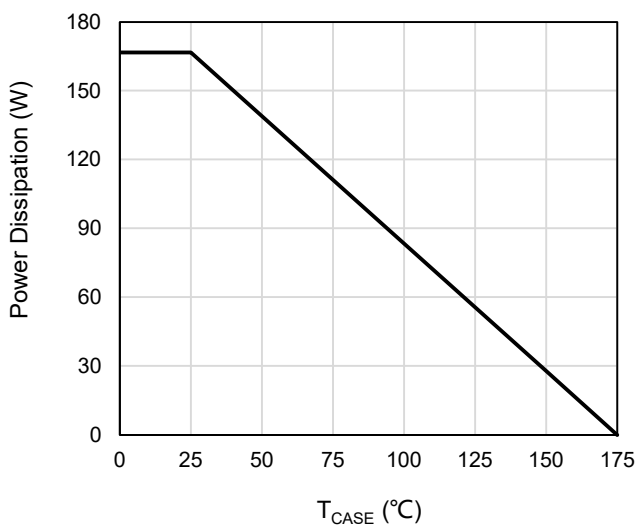


Figure 11: Power De-rating

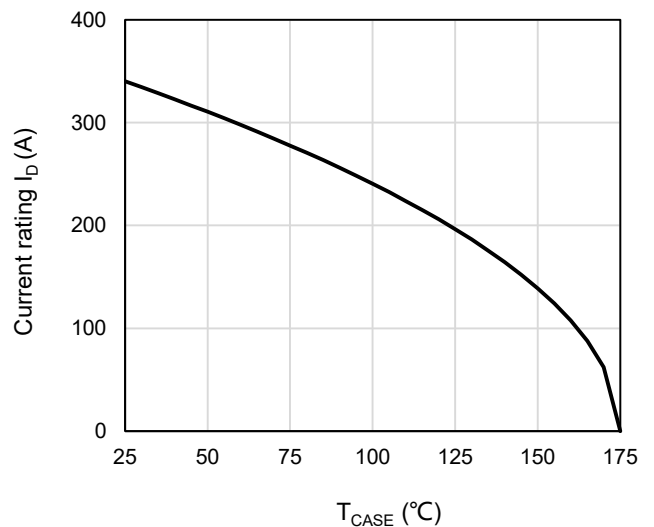


Figure 12: Current De-rating

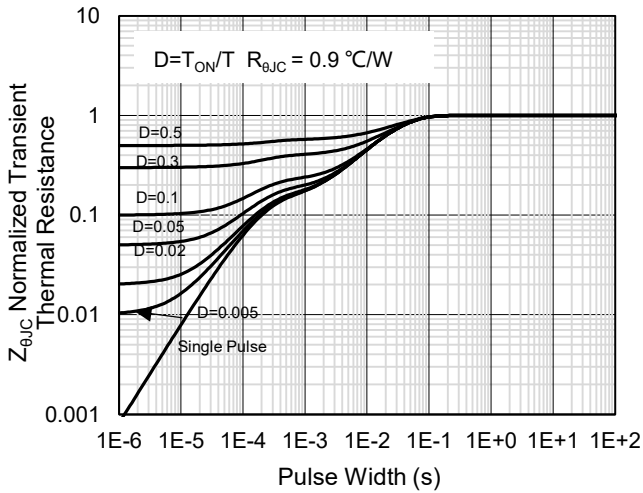


Figure 13: Normalized Maximum Transient Thermal Impedance

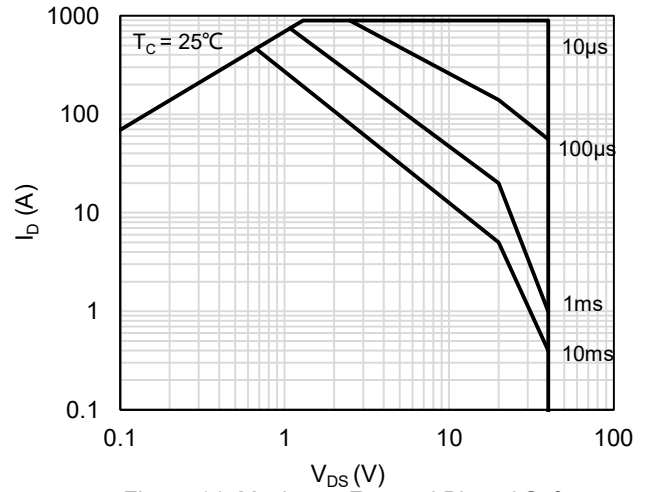
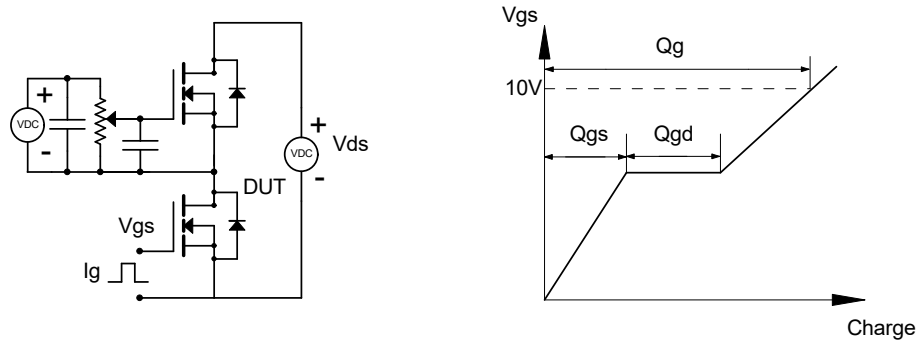


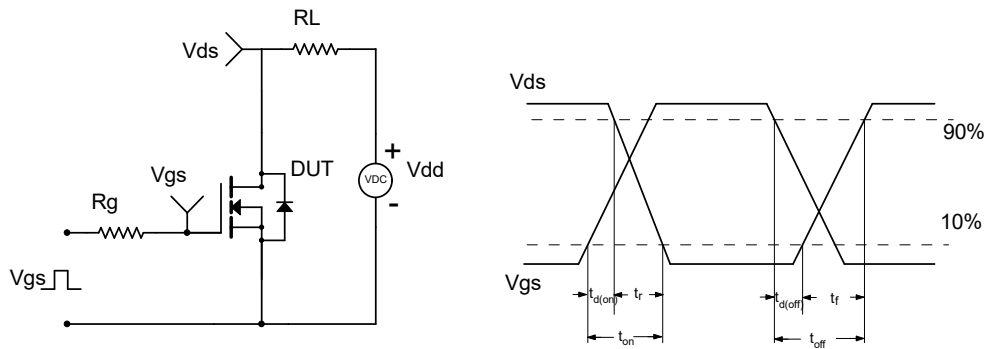
Figure 14: Maximum Forward Biased Safe Operating Area

Test Circuit and Waveform

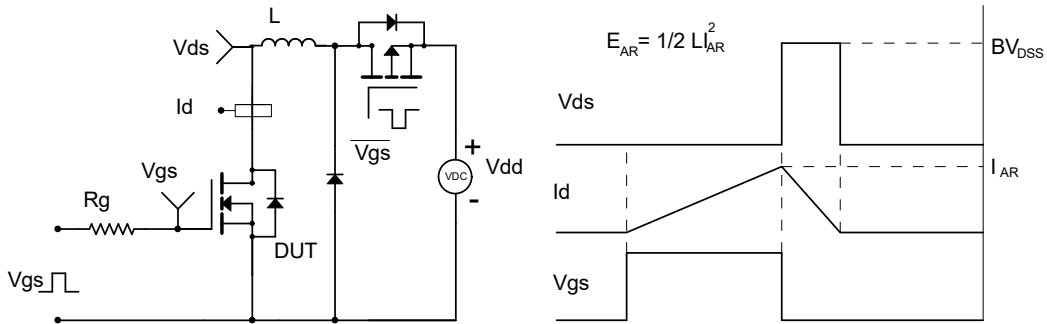
Gate Charge Test Circuit & Waveform



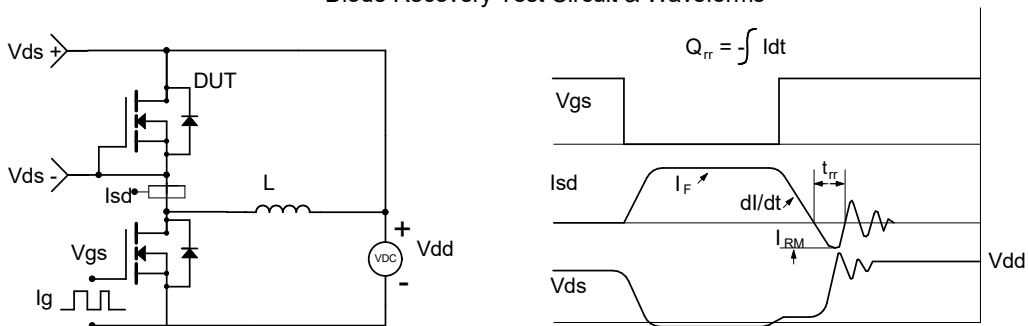
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



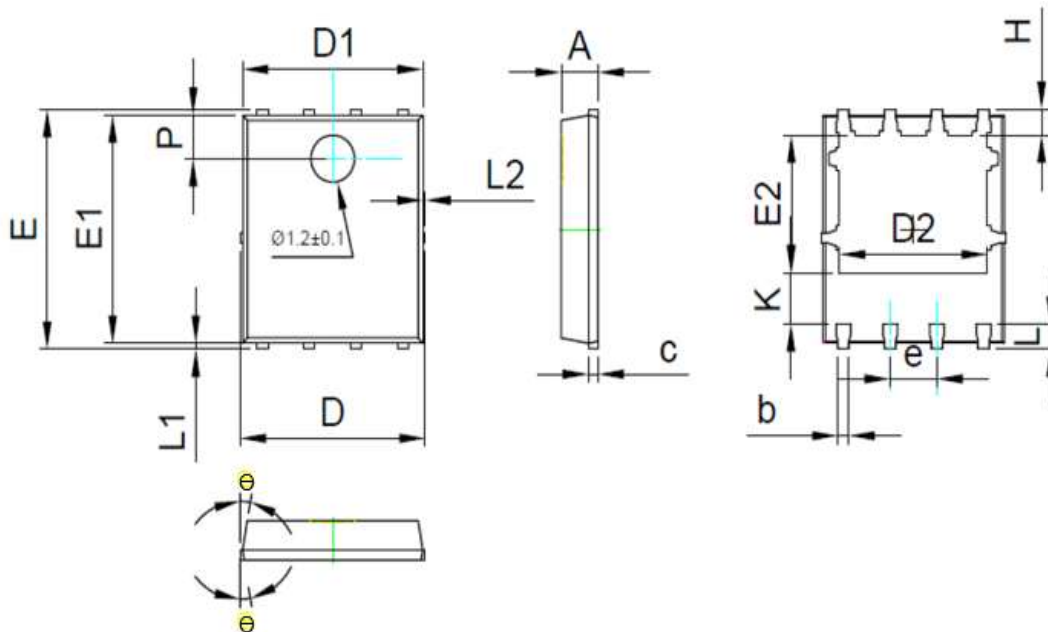
Diode Recovery Test Circuit & Waveforms



Package Outlines

Package Dimensions : PDFN 5*6 PACKAG

E



COMMON DIMENSIONS
(UNITS OF MEASURE = MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.90	1.00	1.10
b	0.35	0.40	0.45
c	0.21	0.25	0.34
D	-	-	5.1
D1	4.85	4.90	4.95
D2	3.96	4.01	4.06
e	1.27 BSC		
E	5.95	6.00	6.05
E1	5.70	5.75	5.80
E2	3.425	3.475	3.525
H	0.60	0.65	0.70
K	1.29	-	-
L	0.60	0.65	0.70
L1	0.05	0.15	0.25
L2	-	-	0.12
ϑ	8°	10°	12°
P	1.05	1.10	1.15

Marking Information



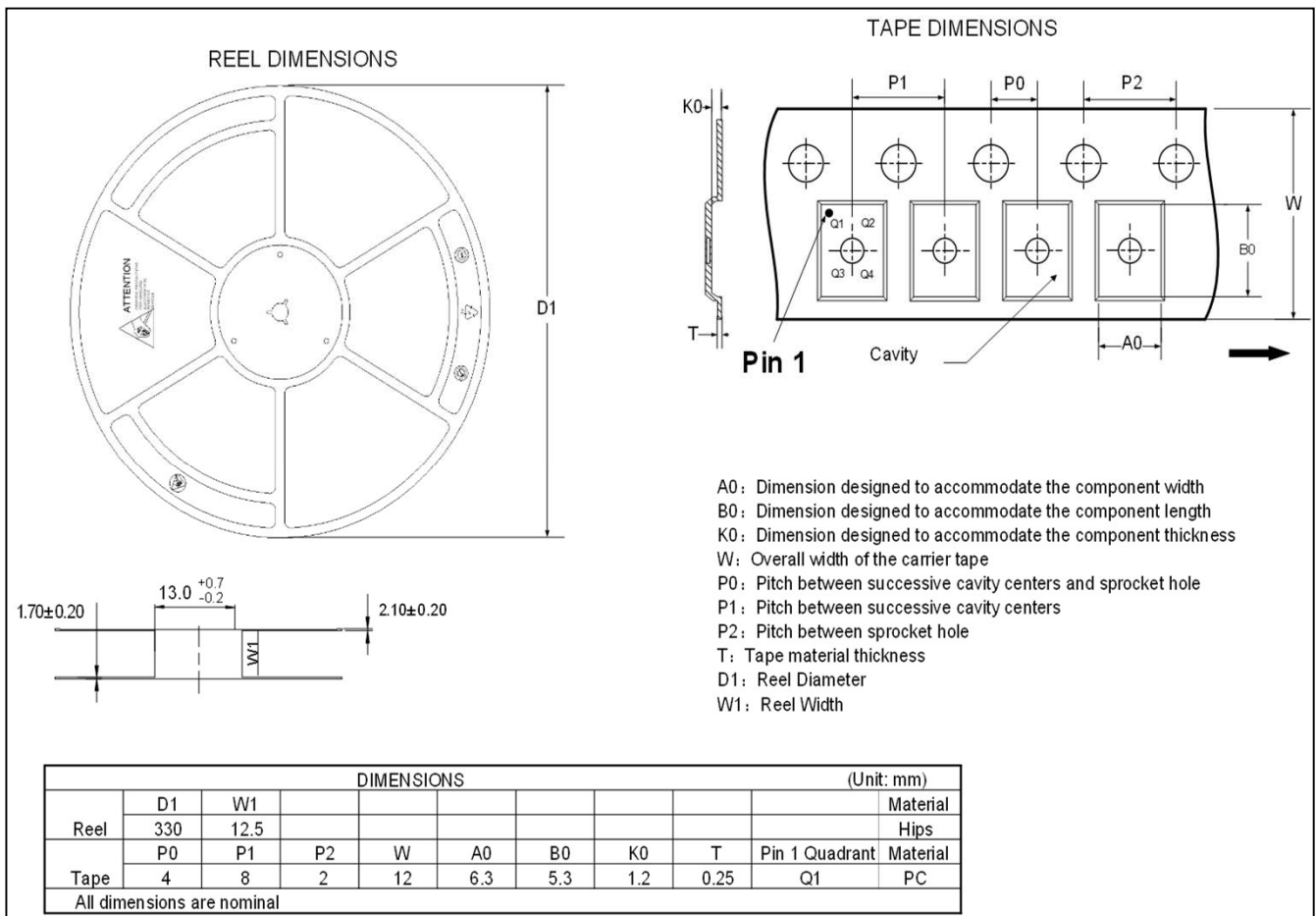
Note:

G4N008GM = Product Name Code

XXXXXXX = Date code

Contact ALKAIDSEMI sales for detail information

Reel Information



Revision History

Revision	Release Date	Remark
Rev.1.0	2022/10/28	Initial Release

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

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