

General Description

The AO4407A uses advanced trench technology to provide excellent $R_{DS(ON)}$, and ultra-low low gate charge with a 25V gate rating. This device is suitable for use as a load switch or in PWM applications.

* RoHS and Halogen-Free Compliant

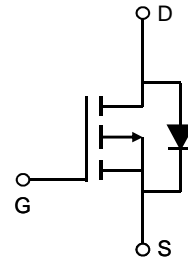
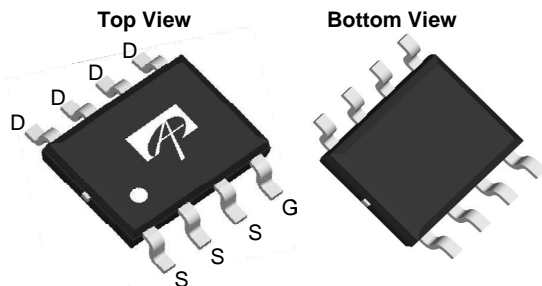
Product Summary

$V_{DS} = -30V$
 $I_D = -12A$ ($V_{GS} = -20V$)
 $R_{DS(ON)} < 11m\Omega$ ($V_{GS} = -20V$)
 $R_{DS(ON)} < 13m\Omega$ ($V_{GS} = -10V$)
 $R_{DS(ON)} < 17m\Omega$ ($V_{GS} = -6V$)

100% UIS Tested
 100% Rg Tested



SOIC-8



Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 25	V
Continuous Drain Current ^A	I_D	$T_A=25^\circ C$	A
		$T_A=70^\circ C$	
Pulsed Drain Current ^B	I_{DM}	-60	
Avalanche Current ^G	I_{AR}	-26	
Repetitive avalanche energy $L=0.3mH$ ^G	E_{AR}	101	mJ
Power Dissipation ^A	P_D	$T_A=25^\circ C$	W
		$T_A=70^\circ C$	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	32	40	$^\circ C/W$
Maximum Junction-to-Ambient ^A		Steady State	60	75
Maximum Junction-to-Lead ^C	$R_{\theta JL}$	17	24	$^\circ C/W$

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D = -250μA, V _{GS} = 0V	-30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -30V, V _{GS} = 0V T _J = 55°C			-1 -5	μA
I _{GSS}	Gate-Body leakage current	V _{DS} = 0V, V _{GS} = ±25V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250μA	-1.7	-2.3	-3	V
I _{D(ON)}	On state drain current	V _{GS} = -10V, V _{DS} = -5V	-60			A
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} = -20V, I _D = -12A T _J =125°C		8.5 11.5	11 15	mΩ
		V _{GS} = -10V, I _D = -12A		10	13	
		V _{GS} = -6V, I _D = -10A		12.7	17	
g _{FS}	Forward Transconductance	V _{DS} = -5V, I _D = -10A		21		S
V _{SD}	Diode Forward Voltage	I _S = -1A, V _{GS} = 0V		-0.7	-1	V
I _S	Maximum Body-Diode Continuous Current				-3	A
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-15V, f=1MHz		2060	2600	pF
C _{oss}	Output Capacitance			370		pF
C _{rss}	Reverse Transfer Capacitance			295		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		2.4	3.6	Ω
SWITCHING PARAMETERS						
Q _g	Total Gate Charge	V _{GS} =-10V, V _{DS} =-15V, I _D =-12A		30	39	nC
Q _{gs}	Gate Source Charge			4.6		nC
Q _{gd}	Gate Drain Charge			10		nC
t _{D(on)}	Turn-On DelayTime	V _{GS} =-10V, V _{DS} =-15V, R _L =1.25Ω, R _{GEN} =3Ω		11		ns
t _r	Turn-On Rise Time			9.4		ns
t _{D(off)}	Turn-Off DelayTime			24		ns
t _f	Turn-Off Fall Time			12		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =-12A, dI/dt=100A/μs		30	40	ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =-12A, dI/dt=100A/μs		22		nC

- A: The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A = 25° C. The value in any given application depends on the user's specific board design. The current rating is based on the t ≤ 10s thermal resistance rating.
- B: Repetitive rating, pulse width limited by junction temperature.
- C: The R_{θJA} is the sum of the thermal impedance from junction to lead R_{θJL} and lead to ambient.
- D: The static characteristics in Figures 1 to 6 are obtained using < 300μs pulses, duty cycle 0.5% max.
- E: These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25° C. The SOA curve provides a single pulse rating.
- F: The current rating is based on the t ≤ 10s thermal resistance rating.
- G: E_{AR} and I_{AR} ratings are based on low frequency and duty cycles to keep T_J=25C.

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

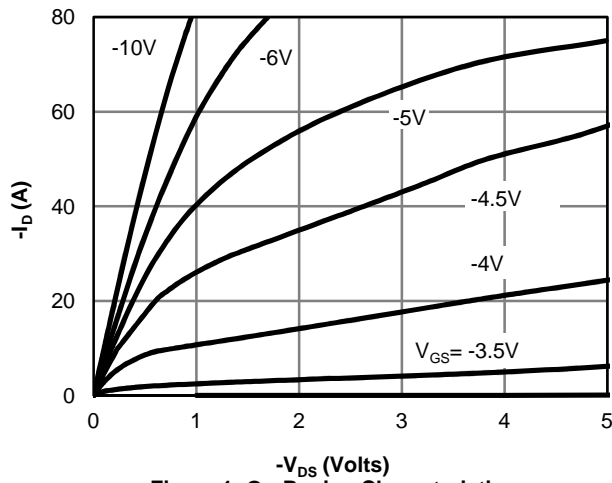


Figure 1: On-Region Characteristics

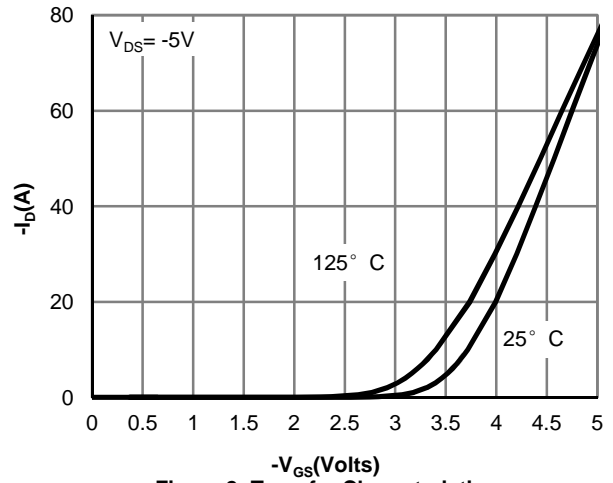


Figure 2: Transfer Characteristics

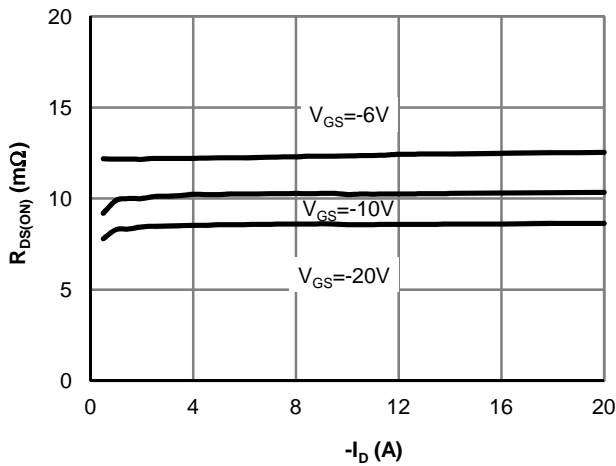


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

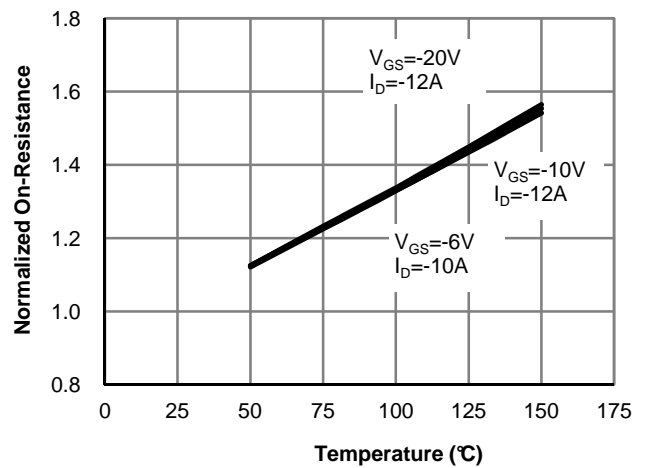


Figure 4: On-Resistance vs. Junction Temperature

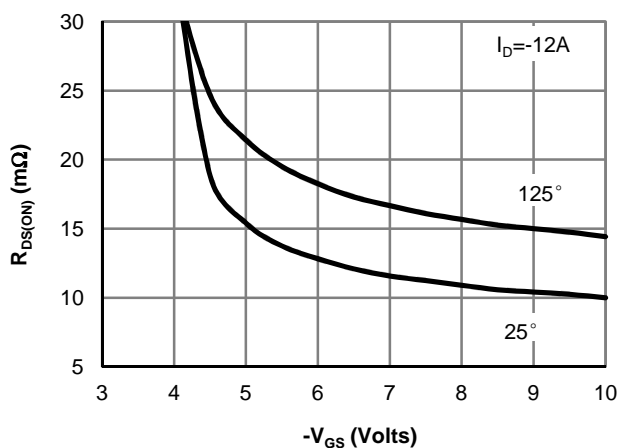


Figure 5: On-Resistance vs. Gate-Source Voltage

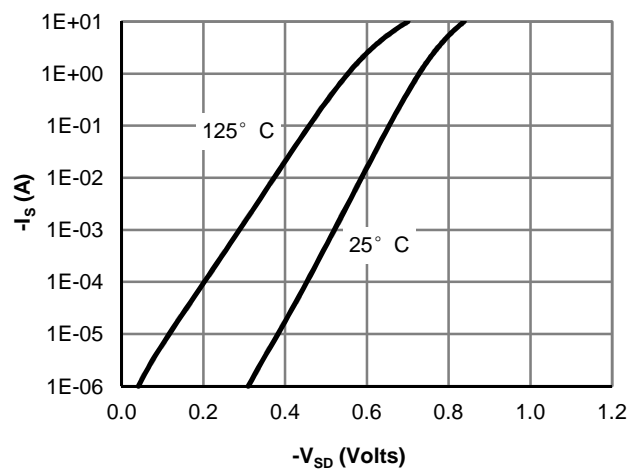


Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

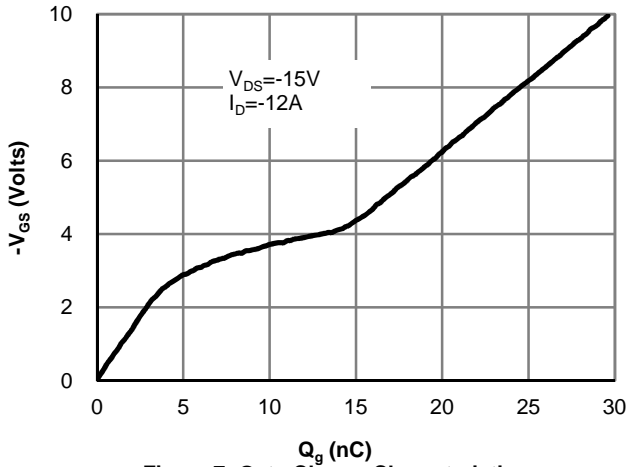


Figure 7: Gate-Charge Characteristics

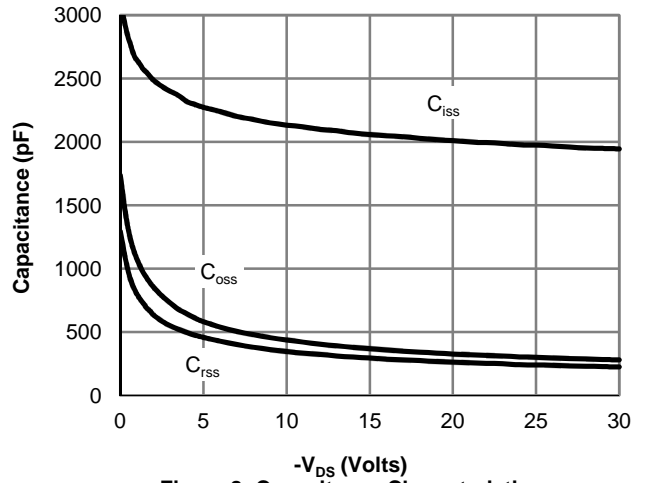


Figure 8: Capacitance Characteristics

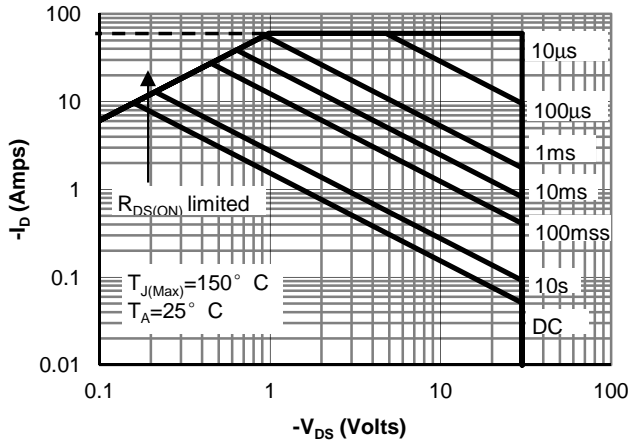


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

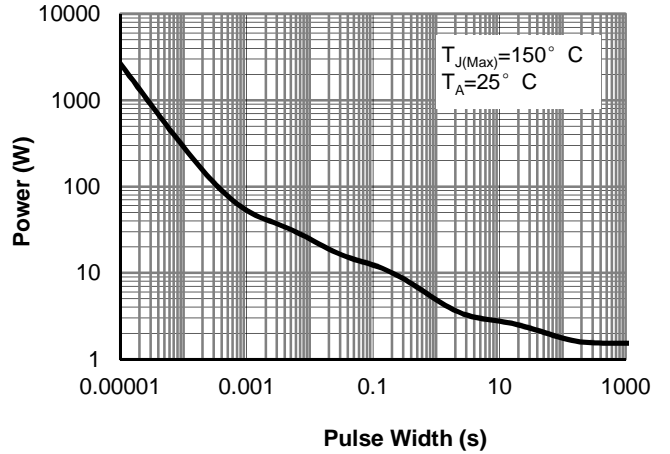


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

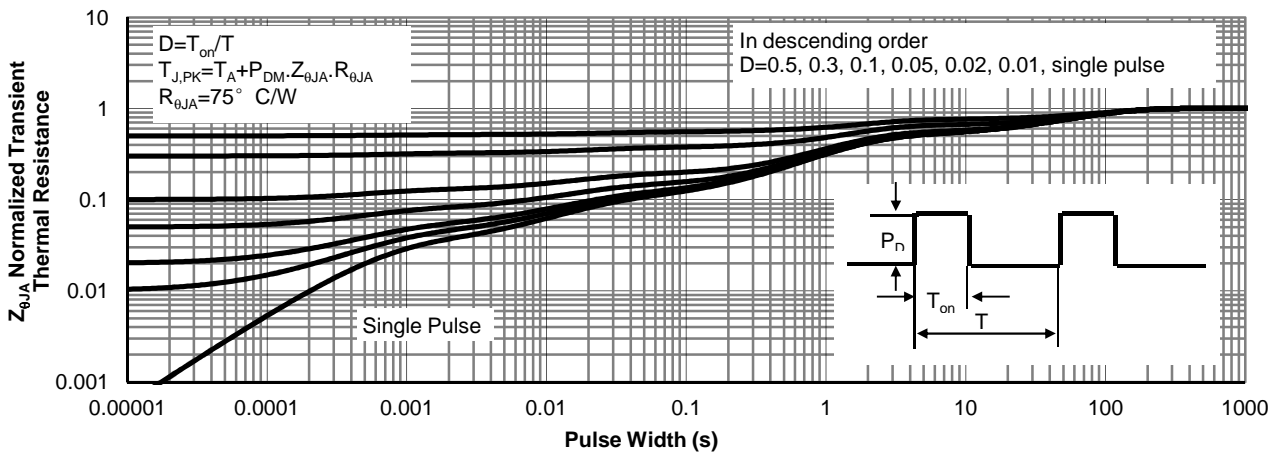
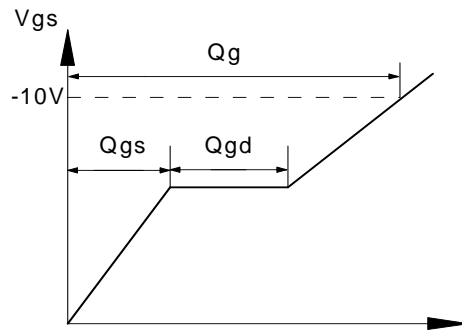
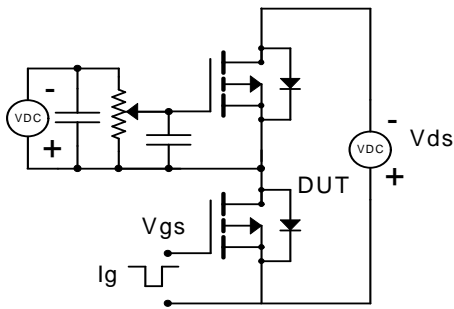
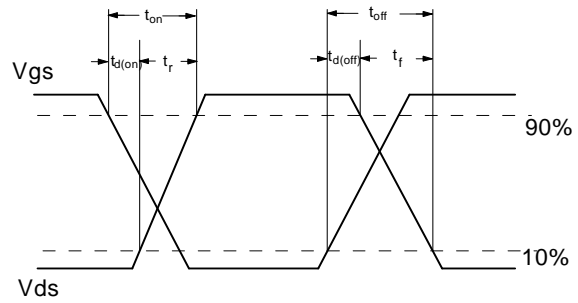
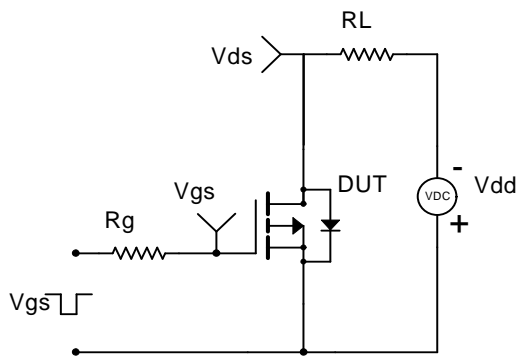


Figure 11: Normalized Maximum Transient Thermal Impedance (Note E)

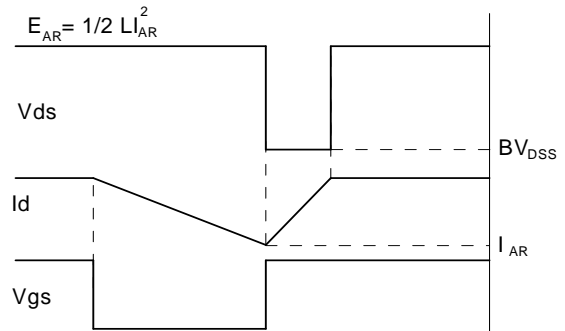
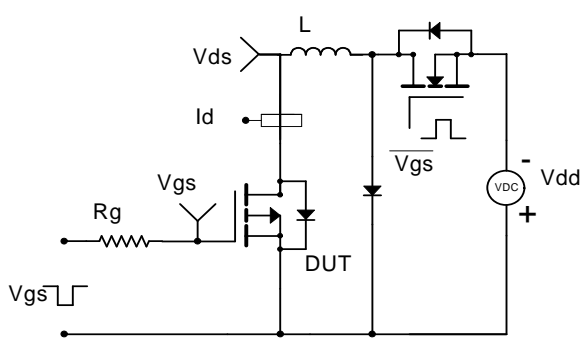
Gate Charge Test Circuit & Waveform



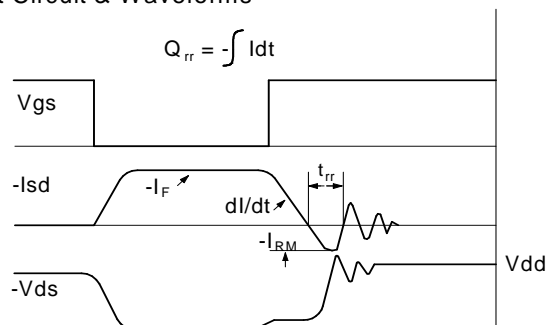
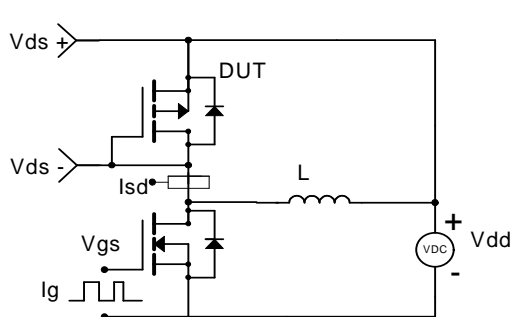
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



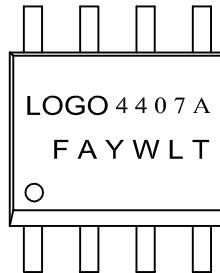
Diode Recovery Test Circuit & Waveforms





Document No.	PD-00405
Version	B
Title	AO4407A Marking Description

SO-8 PACKAGE MARKING DESCRIPTION



Green product

NOTE:

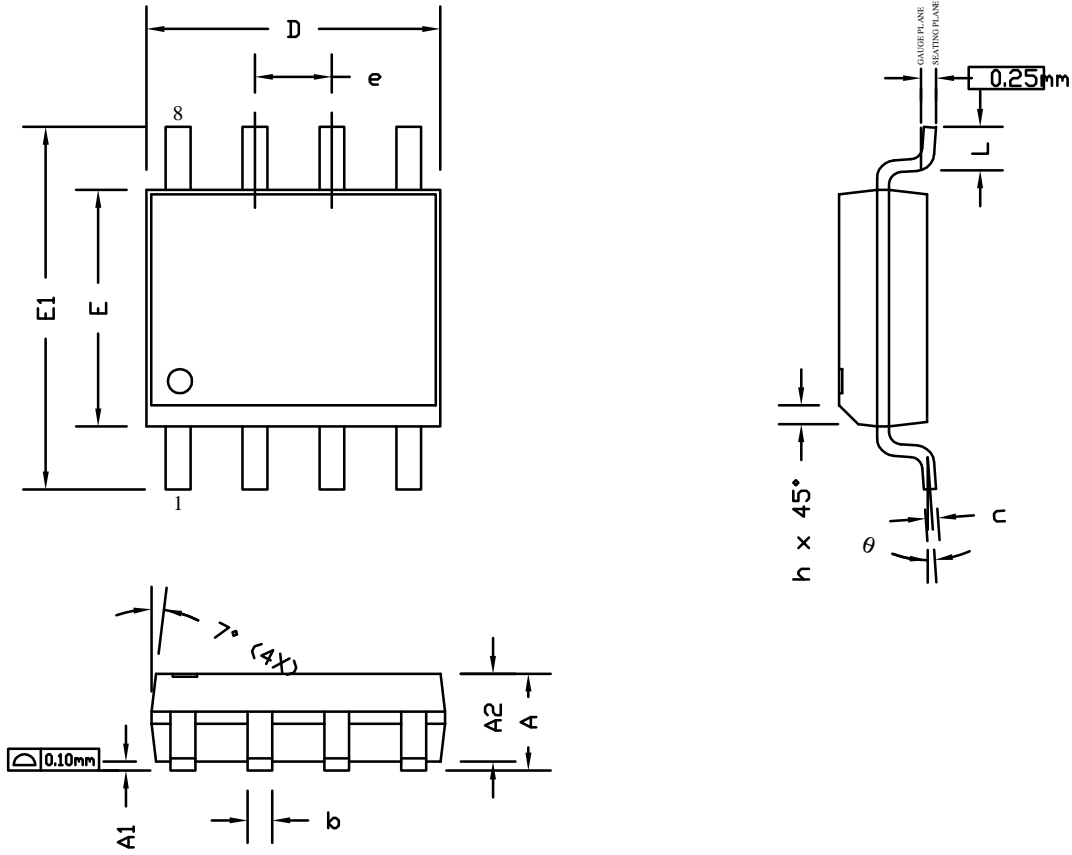
- LOGO - AOS Logo
- 4407A - Part number code
- F&A - Assembly location code
- Y - Year code
- W - Week code
- L&T - Assembly lot code

PART NO.	DESCRIPTION	CODE
AO4407A	Green product	4407A
AO4407AL	Green product	4407A

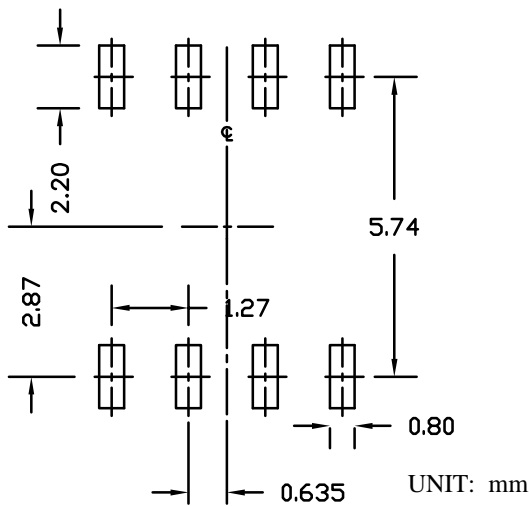


Document No.	PO-00004
Version	I

S08 PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



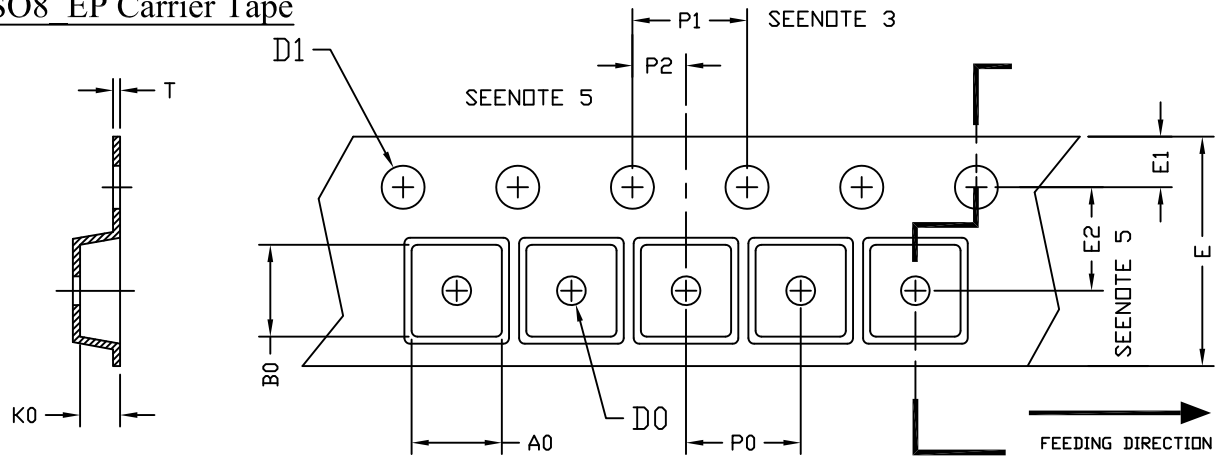
SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.35	1.65	1.75	0.053	0.065	0.069
A1	0.10	0.15	0.25	0.004	0.006	0.010
A2	1.25	1.50	1.65	0.049	0.059	0.065
b	0.31	0.41	0.51	0.012	0.016	0.020
c	0.17	0.20	0.25	0.007	0.008	0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E	3.80	3.90	4.00	0.150	0.154	0.157
e	1.27 BSC			0.050 BSC		
E1	5.80	6.00	6.20	0.228	0.236	0.244
h	0.25	0.30	0.50	0.010	0.012	0.020
L	0.40	0.69	1.27	0.016	0.027	0.050
θ	0°	4°	8°	0°	4°	8°

NOTE

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSIONS ARE INCLUSIVE OF PLATING.
3. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
4. DIMENSION L IS MEASURED IN GAUGE PLANE.
5. CONTROLLING DIMENSION IS MILLIMETER.
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



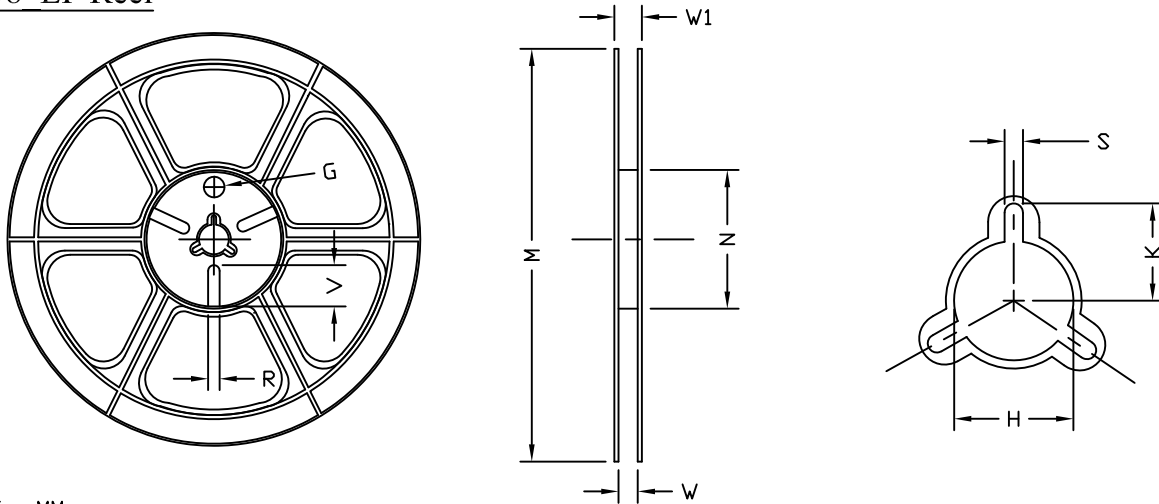
SO8/SO8 EP Carrier Tape



UNIT: MM

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SO-8 (12 mm)	6.40 ±0.10	5.20 ±0.10	2.10 ±0.10	1.60 ±0.10	1.50 +0.10	12.00 ±0.30	1.75 ±0.10	5.50 ±0.05	8.00 ±0.10	4.00 ±0.10	2.00 ±0.05	0.25 ±0.05

SO8/SO8 EP Reel



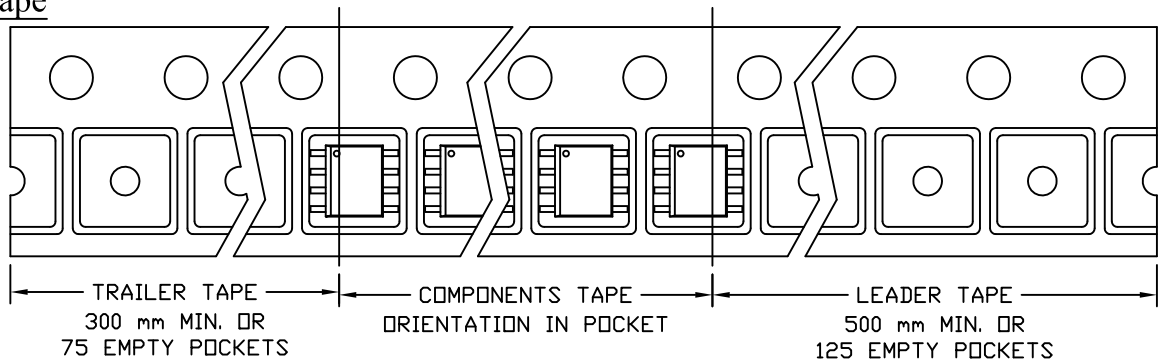
UNIT: MM

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	ø330	ø330.00 ±0.50	ø97.00 ±0.10	13.00 ±0.30	17.40 ±1.00	ø13.00 +0.50 -0.20	10.60	2.00 ±0.50	---	---	---

SO8/SO8 EP Tape

Leader / Trailer
& Orientation

Unit Per Reel:
3000pcs





AOS Semiconductor

Product Reliability Report

AO4407A, rev A

Plastic Encapsulated Device

ALPHA & OMEGA Semiconductor, Inc

www.aosmd.com

This AOS product reliability report summarizes the qualification result for AO4407A. Accelerated environmental tests are performed on a specific sample size, and then followed by electrical test at end point. Review of final electrical test result confirms that AO4407A passes AOS quality and reliability requirements. The released product will be categorized by the process family and be monitored on a quarterly basis for continuously improving the product quality.

Table of Contents:

- I. Product Description
- II. Package and Die information
- III. Environmental Stress Test Summary and Result
- IV. Reliability Evaluation

I. Product Description:

The AO4407A uses advanced trench technology to provide excellent $R_{DS(ON)}$, and ultra-low low gate charge with a 25V gate rating. This device is suitable for use as a load switch or in PWM applications.

- RoHS Compliant
- Halogen Free

Detailed information refers to datasheet.

II. Die / Package Information:

	AO4407A
Process	Standard sub-micron Low voltage P channel
Package Type	SO8
Lead Frame	Cu
Die Attach	Ag Epoxy
Bonding	Cu wire
Mold Material	Epoxy resin with silica filler
MSL (moisture sensitive level)	Level 1 based on J-STD-020

Note * based on information provided by assembler and mold compound supplier

III. Result of Reliability Stress for AO4407A

Test Item	Test Condition	Time Point	Lot Attribution	Total Sample size	Number of Failures	Standard
MSL Precondition	168hr 85°C /85%RH +3 cycle reflow@260°C	-	29 lots	3575pcs	0	JESD22-A113
HTGB	Temp = 150 °C, Vgs=100% of Vgsmax	168hrs 500 hrs 1000 hrs	4 lots	308pcs 77pcs / lot	0	JESD22-A108
HTRB	Temp = 150 °C, Vds=80% of Vdsmax	168hrs 500 hrs 1000 hrs	4 lots	308pcs 77pcs / lot	0	JESD22-A108
HAST	130 +/- 2°C, 85%RH, 33.3 psi, Vgs = 100% of Vgs max	100 hrs	16 lots (Note A*)	880pcs 55 pcs / lot	0	JESD22-A110
Pressure Pot	121°C, 29.7psi, RH=100%	96 hrs	20 lots (Note A*)	1100pcs 55 pcs / lot	0	JESD22-A102
Temperature Cycle	-65°C to 150°C, air to air	250 / 500 cycles	29 lots (Note A*)	1595pcs 55 pcs / lot	0	JESD22-A104

Note A: The reliability data presents total available generic data up to the published date.

IV. Reliability Evaluation

FIT rate (per billion): 6

MTTF = 19828 years

The presentation of FIT rate for the individual product reliability is restricted by the actual burn-in sample size of the selected product (AO4407A). Failure Rate Determination is based on JEDEC Standard JESD 85. FIT means one failure per billion hours.

$$\text{Failure Rate} = \text{Chi}^2 \times 10^9 / [2 (N) (H) (Af)] = 1.83 \times 10^9 / [2 \times (2 \times 4 \times 77 \times 1000) \times 258] = 6$$

$$\text{MTTF} = 10^9 / \text{FIT} = 1.74 \times 10^8 \text{hrs} = 19828 \text{ years}$$

Chi² = Chi Squared Distribution, determined by the number of failures and confidence interval

N = Total Number of units from HTRB and HTGB tests

H = Duration of HTRB/HTGB testing

Af = Acceleration Factor from Test to Use Conditions (Ea = 0.7eV and Tuse = 55°C)

Acceleration Factor [Af] = **Exp** [Ea / k (1/Tj u - 1/Tj s)]

Acceleration Factor ratio list:

	55 deg C	70 deg C	85 deg C	100 deg C	115 deg C	130 deg C	150 deg C
Af	258	87	32	13	5.64	2.59	1

Tj s = Stressed junction temperature in degree (Kelvin), K = C+273.16

Tj u = The use junction temperature in degree (Kelvin), K = C+273.16

K = Boltzmann's constant, 8.617164 X 10⁻⁵eV / K

单击下面可查看定价，库存，交付和生命周期等信息

[>>AOS\(万代\)](#)