

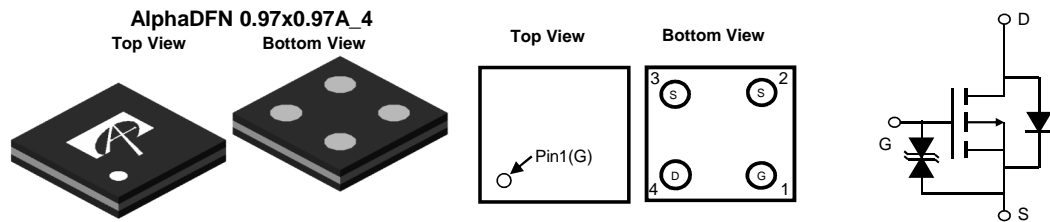
General Description

The AOC2421 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 1.2V while retaining a 5V $V_{GS(MAX)}$ rating.

Product Summary

V_{DS}	-8V
I_D (at $V_{GS}=-2.5V$)	-2.5A
$R_{DS(ON)}$ (at $V_{GS}=-2.5V$)	< 60m Ω
$R_{DS(ON)}$ (at $V_{GS}=-1.8V$)	< 72m Ω
$R_{DS(ON)}$ (at $V_{GS}=-1.5V$)	< 85m Ω
$R_{DS(ON)}$ (at $V_{GS}=-1.2V$)	< 115m Ω

Typical ESD protection **HBM Class 2**



Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-8	V
Gate-Source Voltage	V_{GS}	± 5	V
Source Current (DC) ^{Note1}	I_D	-2.5	A
Source Current (Pulse) ^{Note2}	I_{DM}	-25	
Power Dissipation ^{Note1}	P_D	0.6	W
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	110	140	$^\circ\text{C/W}$
Maximum Junction-to-Ambient ^{A D}		160	200	

Note 1. Mounted on minimum pad PCB

Note 2. PW < 300 μs pulses, duty cycle 0.5% max

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =-250μA, V _{GS} =0V	-8			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-8V, V _{GS} =0V T _J =55°C			-1 -5	μA
I _{GSS}	Gate-Body leakage current	V _{DS} =0V, V _{GS} =±5V			±10	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-0.2	-0.45	-0.7	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-2.5V, I _D =-1.5A T _J =125°C		50	62	mΩ
				63.5	79	
		V _{GS} =-1.8V, I _D =-1A		57	72	mΩ
		V _{GS} =-1.5V, I _D =-1A		65	85	mΩ
		V _{GS} =-1.2V, I _D =-1A		83	115	mΩ
g _{FS}	Forward Transconductance	V _{DS} =-5V, I _D =-1.5A		12		S
V _{SD}	Diode Forward Voltage	I _S =-1A, V _{GS} =0V		-0.6	-1	V
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-4V, f=1MHz		752		pF
C _{oss}	Output Capacitance			178		pF
C _{riss}	Reverse Transfer Capacitance			104		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		1.6		KΩ
SWITCHING PARAMETERS						
Q _g	Total Gate Charge	V _{GS} =-4.5V, V _{DS} =-4V, I _D =-1.5A		7.5	13	nC
Q _{gs}	Gate Source Charge			1.5		nC
Q _{gd}	Gate Drain Charge			1.0		nC
t _{D(on)}	Turn-On DelayTime	V _{GS} =-2.5V, V _{DS} =-4V, R _L =2.67Ω, R _{GEN} =3Ω		285		ns
t _r	Turn-On Rise Time			465		ns
t _{D(off)}	Turn-Off DelayTime			1870		ns
t _f	Turn-Off Fall Time			1900		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =-1.5A, di/dt=100A/μs		12		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =-1.5A, di/dt=100A/μs		4		nC

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

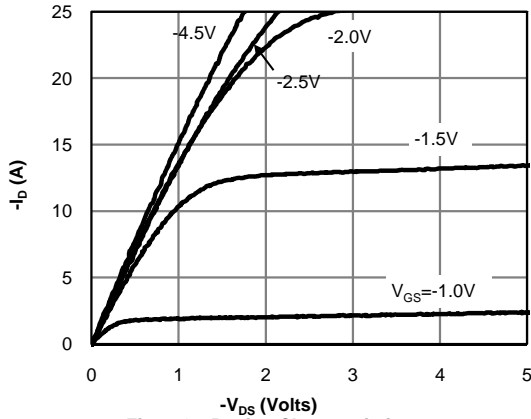


Fig 1: On-Region Characteristics

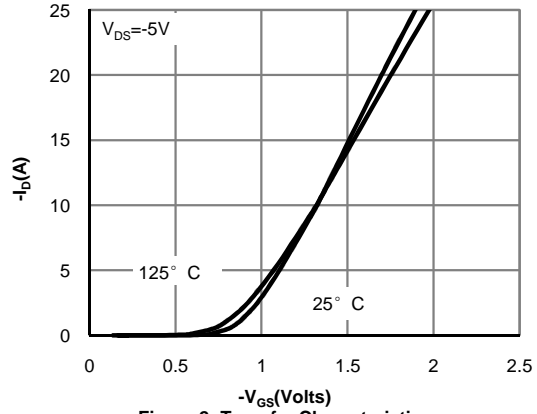


Figure 2: Transfer Characteristics

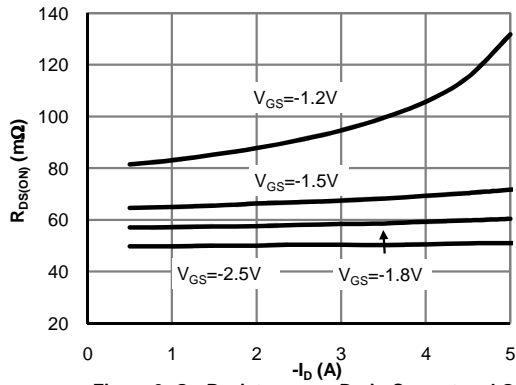


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

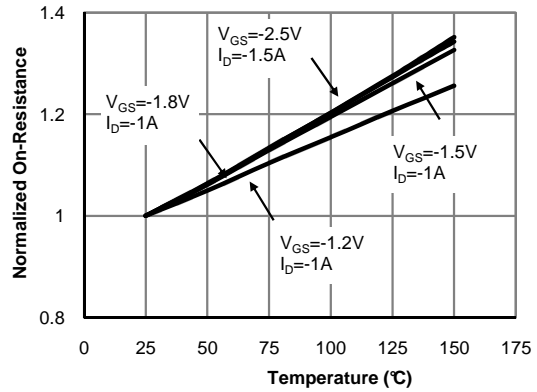


Figure 4: On-Resistance vs. Junction Temperature (Note E)

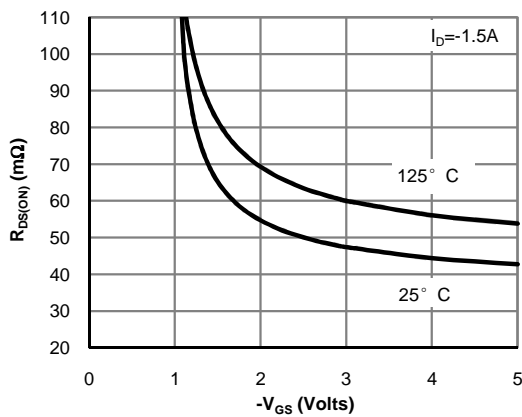


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

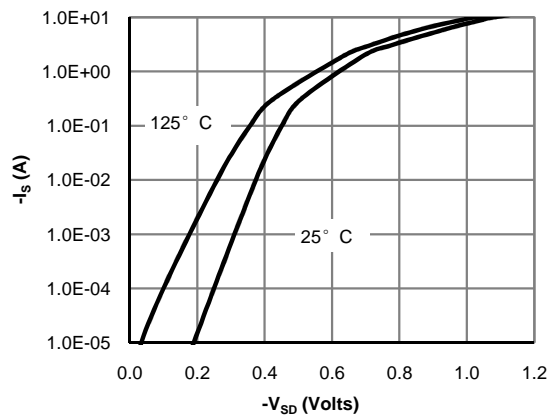


Figure 6: Body-Diode Characteristics (Note E)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

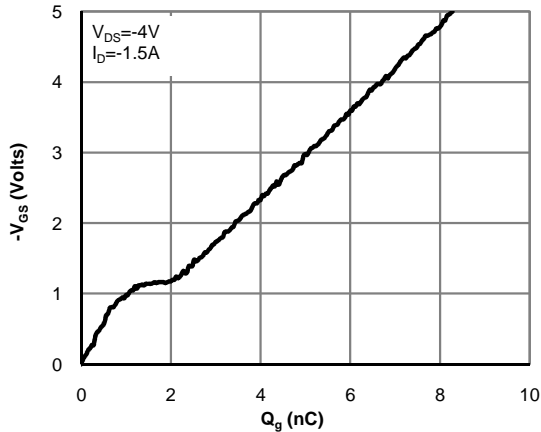


Figure 7: Gate-Charge Characteristics

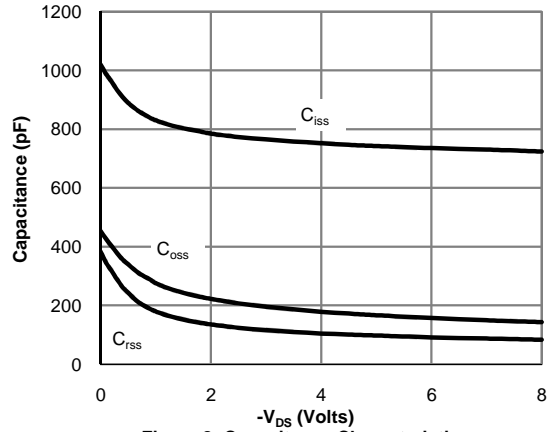


Figure 8: Capacitance Characteristics

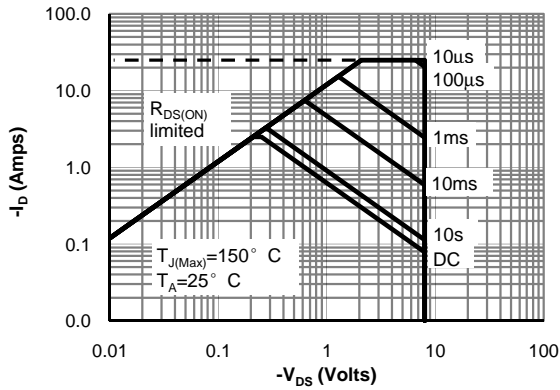


Figure 9: Maximum Forward Biased Safe Operating Area

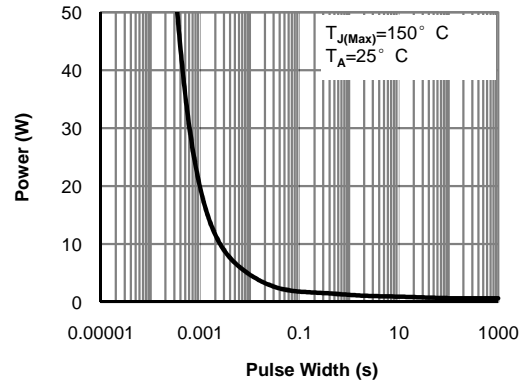


Figure 10: Single Pulse Power Rating Junction-to-Ambient

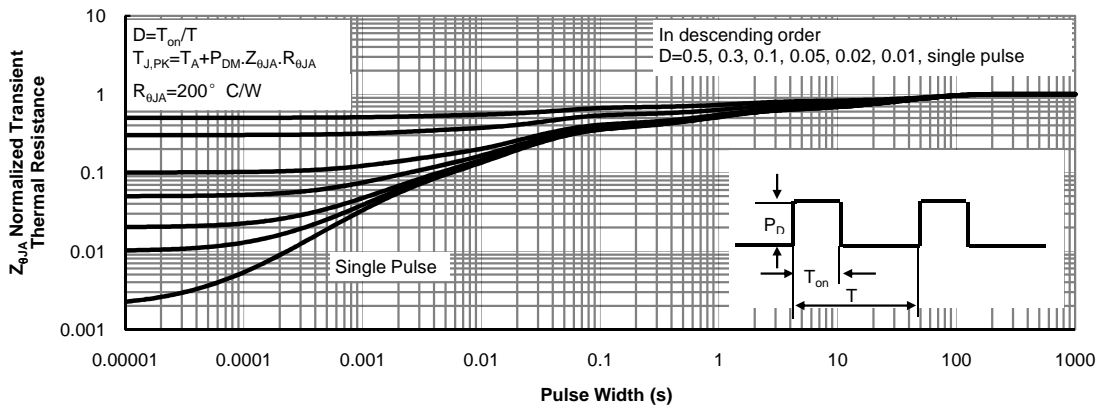


Figure 11: Normalized Maximum Transient Thermal Impedance

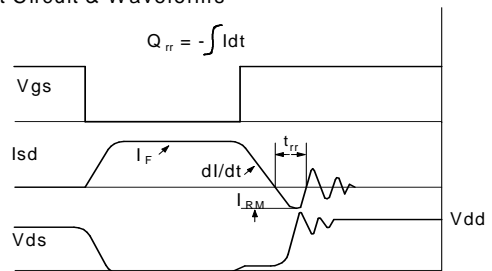
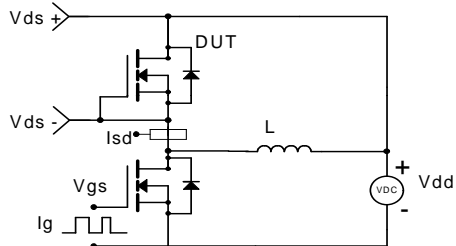
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



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