

AOK40B120N1

1200V, 40A Alpha IGBT™

With Soft and Fast Recovery Anti-Parallel Diode

General Description

- 1200V latest Alpha IGBT (αIGBT) technology
- \bullet Very low $V_{\text{CE}(\text{sat})}$ and V_{F}
- · High short-circuit ruggedness
- Very low turn-on EMI
- · Easy paralleling capability
- Low gate charge Q_g
- · High efficiency and ruggedness in hard switching converters
- Maximum junction temperature 175°C
- · Very soft and fast recovery anti-parallel diode

Applications

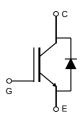
- · Motor drives
- Industrial UPS

Product Summary

 $\begin{array}{ll} V_{CE} & 1200V \\ I_{C} \; (T_{C} \! = \! 100^{\circ}C) & 40A \\ V_{CE(sat)} \; (T_{J} \! = \! 25^{\circ}C) & 1.97V \end{array}$







| | | | kage Type | Form Tube | Minimum Order Quantity | | |
|--|---|-------------------|-----------------|--------------|------------------------|-------|--|
| AOK40B120N1 Absolute Maximum Ratings T _A =25°C unless of | | | TO247 | 240 | | | |
| Parameter | | Symbol | AOK40B120N1 | | Units | | |
| Collector-Emitter Voltage | | V _{CE} | | 1200 | | | |
| Gate-Emitter Voltage | | | V_{GE} | | ±30 | | |
| Continuous Collector Current | T _C =25°C T _C =100°C | | -I _c | 80 | | A | |
| | | | | | 40 | A | |
| Pulsed Collector Current, Limited by T _{Jmax} | | | I _{CM} | | 160 | | |
| Turn-Off SOA, $V_{CE} \le 1200V$, Limited by T_{Jmax} | | I _{LM} | | 160 | A | | |
| Continuous Diode Forward Current | T _C =25°C T _C =100°C | | -I _F | | 80 | | |
| | | | | | 40 | A | |
| Diode Pulsed Current, Limited by T _{Jmax} | | I _{FM} | | 160 | А | | |
| Short Circuit Withstanding Time (1) | | t _{sc} | 10 | | II.6 | | |
| V _{GE} =15V, V _{CC} ≪600V, T _J ≪175°C | | | | 10 | μs | | |
| Power Dissipation | T _C =25°C T _C =100°C | | -P _D | 600 | | w | |
| | | | | | 300 | VV | |
| Junction and Storage Temperature Range | | T_J , T_{STG} | -55 | 5 to 175 | °C | | |
| Maximum Lead Temperature for Soldering Purpose, 1/8" from case for 5 seconds | | TL | 300 | | °C | | |
| Thermal Characteris | tics | | | | | | |
| Parameter | | | Symbol | AOK40B120N1 | | Units | |
| Maximum Junction-to-Ambient | | | $R_{\theta JA}$ | | 40 | °C/W | |
| Maximum IGBT Junction-to-Case | | | $R_{\theta JC}$ | | 0.25 | °C/W | |
| Maximum Diode Junction-to-Case | | | $R_{\theta JC}$ | | 0.4 | °C/W | |

(1) Allowed number of short circuits: <1000; time between short circuits: >1s.

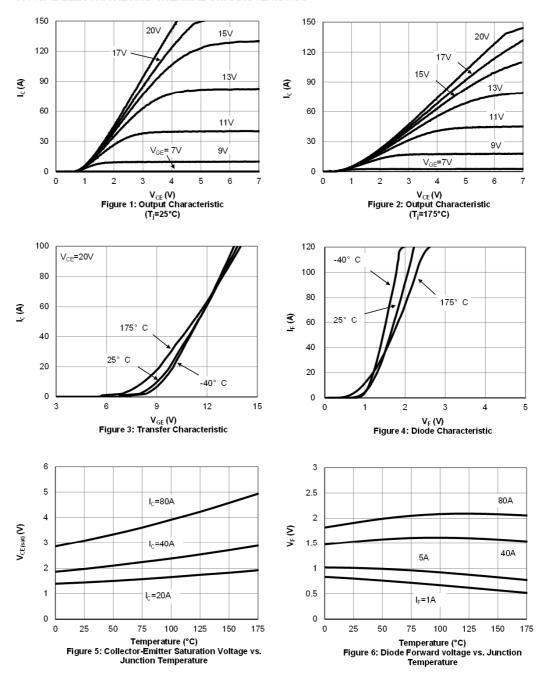


Electrical Characteristics (T_J=25°C unless otherwise noted)

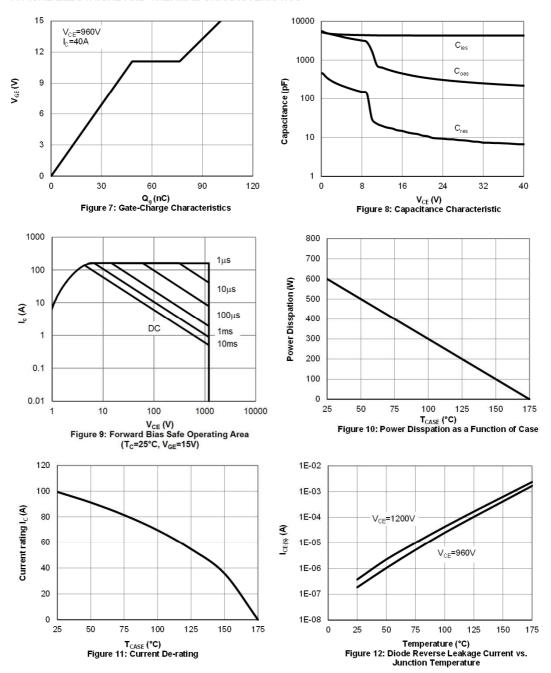
| Symbol | Parameter | Conditions | | Min | Тур | Max | Units |
|------------------------|--|---|-----------------------|------|------|-------|-------|
| _ | PARAMETERS | • | | • | | • | |
| BV _{CES} | Collector-Emitter Breakdown Voltage | I _C =1mA, V _{GE} =0V, T _J =25°C | | 1200 | - | - | V |
| | | | T _J =25°C | - | 1.97 | 2.5 | V |
| V _{CE(sat)} | Collector-Emitter Saturation Voltage | V _{GE} =15V, I _C =40A | T _J =125°C | - | 2.55 | - | |
| | | | T _J =175°C | - | 2.9 | - | |
| | | V _{GE} =0V, I _F =40A | T _J =25°C | - | 1.53 | 2 | V |
| V _F | Diode Forward Voltage | | T _J =125°C | - | 1.6 | - | |
| | | | T _J =175°C | - | 1.54 | - | |
| $V_{GE(th)}$ | Gate-Emitter Threshold Voltage | V _{CE} =5V, I _C =1mA | | - | 6.1 | - | V |
| () | Zero Gate Voltage Collector Current | V _{CE} =1200V, V _{GE} =0V | T _J =25°C | - | - | 10 | μА |
| I _{CES} | | | T _J =125°C | - | - | 2000 | |
| | | | T _J =175°C | - | - | 20000 | |
| I _{GES} | Gate-Emitter Leakage Current | V _{CE} =0V, V _{GE} =±30V | | - | - | ±100 | nA |
| g _{FS} | Forward Transconductance | V _{CE} =20V, I _C =40A | | - | 18 | - | S |
| DYNAMIC | PARAMETERS | • | | • | | | |
| C _{ies} | nput Capacitance | | | | 4300 | - | pF |
| C _{oes} | Output Capacitance | V_{GE} =0V, V_{CC} =25V, f=1MH | /, f=1MHz | | 300 | - | pF |
| C _{res} | Reverse Transfer Capacitance | | - | 9 | - | рF | |
| Q_g | Total Gate Charge | | | - | 100 | - | nC |
| Q_{ge} | Gate to Emitter Charge | e V _{GE} =15V, V _{CC} =960V, I _C =40A | | | 48 | - | nC |
| Q_{gc} | Gate to Collector Charge |] | - | 28 | - | nC | |
| R_g | Gate Resistance | V _{GE} =0V, V _{CC} =0V, f=1MHz | | | 4.4 | - | Ω |
| SWITCH | NG PARAMETERS, (Load Inductive, T _J | =25°C) | | | | | |
| t _{D(on)} | Turn-On Delay Time | | | - | 57 | - | ns |
| t _r | Turn-On Rise Time | T_J =25°C V_{GE} =15V, V_{CC} =600V, I_C =40A, R_G =7.5 Ω | | - | 35 | - | ns |
| t _{D(off)} | Turn-Off Delay Time | | | - | 146 | - | ns |
| t _f | Turn-Off Fall Time | | | - | 42 | - | ns |
| E _{on} | Turn-On Energy | | | - | 3.4 | - | mJ |
| E _{off} | Turn-Off Energy | | | - | 1.4 | - | mJ |
| E _{total} | Total Switching Energy | | - | 4.8 | - | mJ | |
| t _{rr} | Diode Reverse Recovery Time | T _J =25°C I _E =40A, di/dt=200A/μs, V _{CC} =600V | | - | 300 | - | ns |
| Q _{rr} | Diode Reverse Recovery Charge | | | - | 3.0 | - | μС |
| I _{rm} | Diode Peak Reverse Recovery Current | -11- 40/1, αι/αι 200/1/μ3, V _C | - | 18.5 | - | Α | |
| SWITCH | NG PARAMETERS, (Load Inductive, T_J | =175°C) | | | | | |
| t _{D(on)} | Turn-On Delay Time | | | - | 54 | - | ns |
| t _r | Turn-On Rise Time | T _J =175°C V _{GE} =15V, V _{CC} =600V, I _C =40A, R _G =7.5Ω | | - | 55 | - | ns |
| t _{D(off)} | Turn-Off Delay Time | | | - | 168 | - | ns |
| t _f | Turn-Off Fall Time | | | - | 73 | - | ns |
| E _{on} | Turn-On Energy | GE 104, VCC-0004, 1C-4 | - | 4.1 | - | mJ | |
| E _{off} | Turn-Off Energy | | - | 1.9 | - | mJ | |
| E _{total} | Total Switching Energy | | - | 6.0 | - | mJ | |
| t _{rr} | Diode Reverse Recovery Time | T ₁ =175°C | | - | 490 | - | ns |
| Q _{rr} | Diode Reverse Recovery Charge | I _⊏ =40A, di/dt=200A/μs, V _C | - | 6.6 | - | μС | |
| I _{rm} | Diode Peak Reverse Recovery Current | T' ₁ -τολ, αναι-200λ/μο, ν _C | - | 25.5 | - | Α | |

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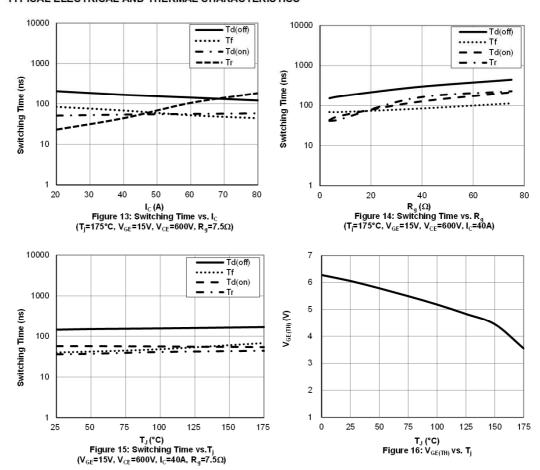




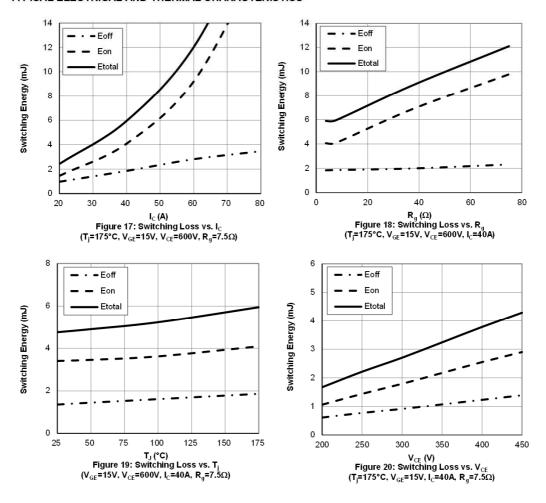












30

25

20

10

5

0

15 **ග**



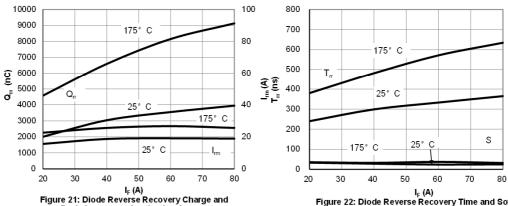
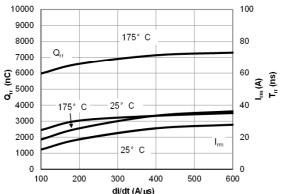


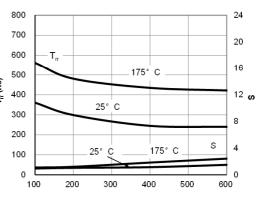
Figure 21: Diode Reverse Recovery Charge and Peak Current vs. Conduction Current (V_{GE}=15V, V_{CE}=600V, di/dt=200A/μs)

 $I_F~(A) \label{eq:interpolation}$ Figure 22: Diode Reverse Recovery Time and Softness Factor vs. Conduction Current $(V_{GE}{=}15V, V_{CE}{=}600V, dl/dt{=}200A/\mu s)$



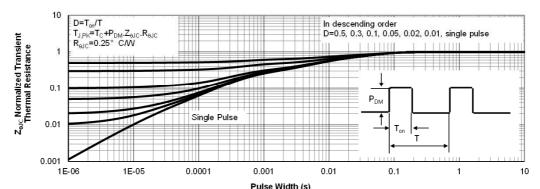
di/dt (A/ μ s)

Figure 23: Diode Reverse Recovery Charge and Peak Current vs. di/dt (V_{GE} =15V, V_{CE} =600V, I_F =40A)

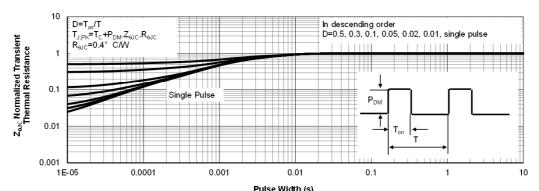


di/dt (A/ μ s)
Figure 24: Diode Reverse Recovery Time and Softness Factor vs. di/dt (V_{GE} =15V, V_{CE} =600V, I_F =40A)





Pulse Width (s) Figure 25: Normalized Maximum Transient Thermal Impedance for IGBT



Pulse Width (s)
Figure 26: Normalized Maximum Transient Thermal Impedance for Diode

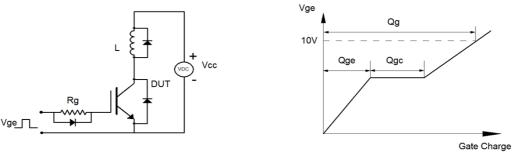


Figure A: Gate Charge Test Circuit & Waveforms

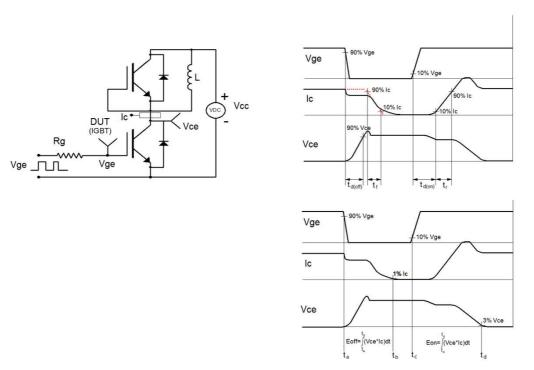


Figure B: Inductive Switching Test Circuit & Waveforms

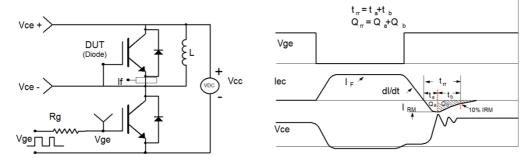


Figure C: Diode Recovery Test Circuit & Waveforms

单击下面可查看定价,库存,交付和生命周期等信息

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