

AOZ8849DI 8-Channel Ultra-Low Capacitance TVS Diode Array

General Description

The AOZ8849DI is a transient voltage suppressor array designed to protect high speed data lines such as HDMI 1.4/2.0, USB 3.0, V-by-one and Thunderbolt from damaging ESD events.

This device incorporates a numbers of surge rated, low capacitance steering diodes and a TVS in a single package. During transient conditions, the steering diodes direct the transient to either the positive side of the power supply line or to ground.

The AOZ8849DI provides a typical line-to-line capacitance of 0.06 pF and low insertion loss providing greater signal integrity making it ideally suited for HDMI 1.4/2.0 or USB 3.0/3.1 applications, such as Digital TVs, DVD players, computing, set-top boxes and MDDI applications in mobile computing devices.

The AOZ8849DI comes in a RoHS compliant and Halogen Free 5.5 mm x 1.5 mm x 0.5 mm DFN-18L package and is rated for -40° C to $+125^{\circ}$ C junction temperature range.

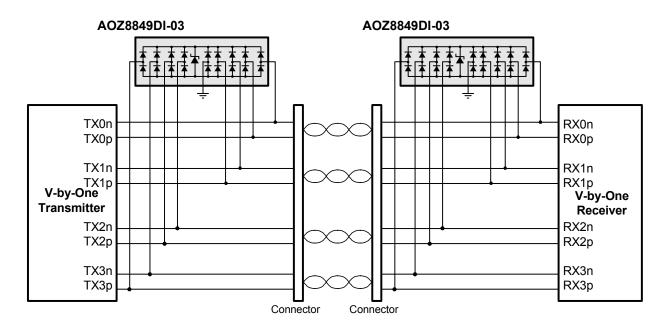
Features

- IEC 61000-4-2 (ESD Immunity):
 - Air discharge: ±15 kV
 Contact discharge: ±12 kV
- IEC 61000-4-5 (Lightning, 8/20 μs) 3 A
- Human Body Model (HBM) ±8 kV
- Protects eight data lines
- Low capacitance between I/O lines: 0.06 pF
- Low clamping voltage
- Low operating voltage: 3.3, 5.5 V

Applications

- HDMI 1.4/2.0, USB 3.0/3.1, V-by-One, Thunderbolt
- Monitors and flat panel displays
- Set-top box
- Video graphics cards
- Notebook computers





Typical Applications



Ordering Information

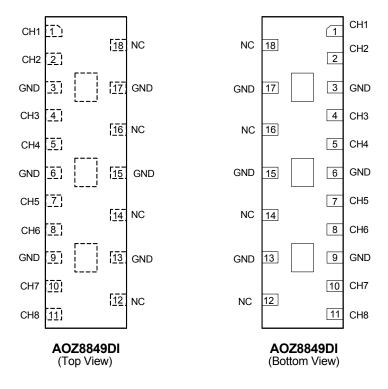
Part Number	Ambient Temperature Range	Package	Environmental
AOZ8849DI-03	40°C to +125°C	DFN 5.5×1.5-18L	Green Product
AOZ8849DI-05	40 C t0 +125 C	DEN 3.3×1.3-18E	Green Floddol



AOS Green Products use reduced levels of Halogens, and are also RoHS compliant.

Please visit www.aosmd.com/media/AOSGreenPolicy.pdf for additional information.

Pin Configuration



Absolute Maximum Ratings

Exceeding the Absolute Maximum ratings may damage the device.

Parameter	Rating
Storage Temperature (T _S)	-65 °C to +150 °C
ESD Rating per IEC 61000-4-2, contact ⁽¹⁾	±12 kV
ESD Rating per IEC 61000-4-2, air ⁽¹⁾	±15 kV
ESD Rating per Human Body Model ⁽²⁾	±8 kV

Notes:

1. IEC 61000-4-2 discharge with C_Discharge = 150pF, R_Discharge = 330 $\Omega.$

2. Human Body Discharge per MIL-STD-883, Method 3015 C_{Discharge} = 100 pF, R_{Discharge} = 1.5 k Ω .

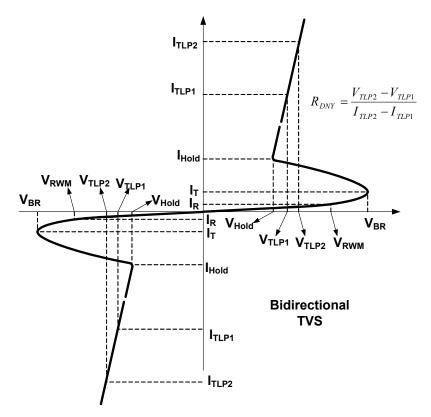
Maximum Operating Ratings

Parameter	Rating
Junction Temperature (T _J)	-40 °C to +125 °C



Electrical Characteristics

 $T_A = 25^{\circ}C$ unless otherwise specified.



AOZ8849DI-03

Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
V _{RWM}	Reverse Working Voltage	I/O Pin-to-Ground			3.3	V
V _{BR}	Reverse Breakdown Voltage	I _T = 100 μA, I/O Pin-to-Ground	5		11	V
I _R	Reverse Leakage Current	Max. V _{RWM,} I/O Pin-to-Ground		1	50	nA
V _{HOLD}	Hold Voltage of Snapback ⁽¹⁾		1.8			V
I _{HOLD}	Hold Current of Snapback ⁽¹⁾		20			mA
	Clamping Voltage ⁽¹⁾⁽²⁾	I _{TLP} = 1 A		3		V
V _{CL}	(100 ns Transmission Line Pulse, I/O Pin to GND)	I _{TLP} = 16 A		16		V
R _{DNY}	Dynamic Resistance ⁽¹⁾	I _{TLP} = 1 to 16 A		0.5		Ω
CJ	Junction Capacitance	V _{I/O} = 0V, f = 1MHz, I/O Pin-to-Ground		0.12	0.15	pF
CJ	Sunction Capacitance	V _{I/O} = 0V, f = 1MHz, I/O Pin-to-I/O Pin		0.06		pF

AOZ8849DI-05

Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
V _{RWM}	Reverse Working Voltage	I/O Pin-to-Ground			5.5	V
V _{BR}	Reverse Breakdown Voltage	I _T = 100 μA, I/O Pin-to-Ground	6.5		11	V
I _R	Reverse Leakage Current	Max. V _{RWM,} I/O Pin-to-Ground		1	50	nA
V _{HOLD}	Hold Voltage of Snapback ⁽¹⁾		1.8			V
I _{HOLD}	Hold Current of Snapback ⁽¹⁾		20			mA
	Clamping Voltage ⁽¹⁾⁽²⁾	I _{TLP} = 1 A		4		V
V _{CL}	(100 ns Transmission Line Pulse, I/O Pin to GND)	I _{TLP} = 16 A		17		V
R _{DNY}	Dynamic Resistance ⁽¹⁾	I _{TLP} = 1 to 16 A		0.5		Ω
		V _{I/O} = 0V, f = 1 MHz I/O Pin-to-I/O Pin		0.12	0.15	pF
CJ	Junction Capacitance	V _{PIN 3,8} = 0 V, V _{I/O} = 0 V, f = 1 MHz, I/O Pin-to-I/O Pin		0.06		pF

Notes:

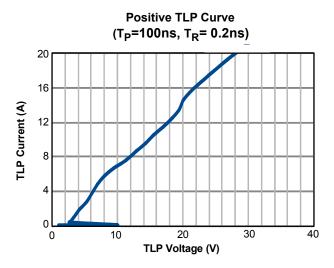
1. These specifications are guaranteed by design and characterization.

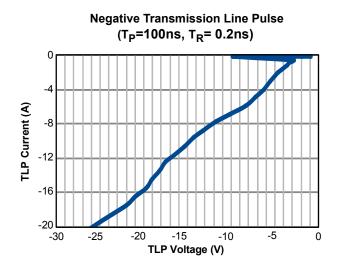
2. Measurements performed using a 100ns Transmission Line Pulse (TLP) system.



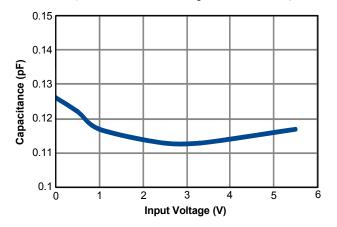
AOZ8849DI

Typical Characteristics

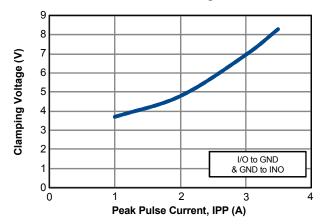




Typical Variations of C_J vs. Input Voltage

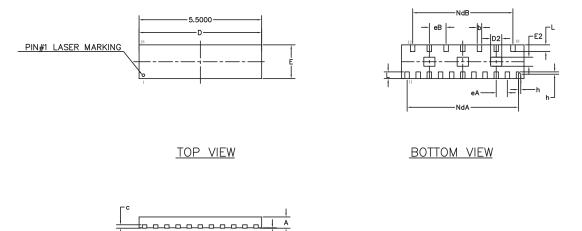


IEC61000-4-5 Surge 8/20µs



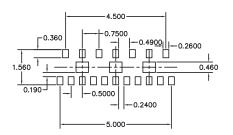


Package Dimensions, DFN 5.5mm x 1.5mm x 0.5mm, 18L, EP3 S



SIDE VIEW

RECOMMENDED LAND PATTERN



	DIMENSIO	NS IN MILLI	DIMEN	DIMENSIONS IN INCHES			
Symbol	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
Α	0.45	0.50	0.55	0.018	0.020	0.022	
A1		0.02	0.05		0.001	0.002	
b	0.15	0.20	0.25	0.006	0.008	0.010	
с	0.10	0.15	0.20	0.004	0.006	0.008	
D	5.45	5.50	5.55	0.215 0.217		0.219	
D2	0.45	0.50	0.55	0.018	0.020	0.022	
NdA		5.00 BSC		0.197 BSC			
eA		0.50 BSC		0.020 BSC			
eB		0.75 BSC			0.030 BSC		
NdB		4.50 BSC			0.177 BSC		
E	1.45	1.50	1.55	0.057	0.059	0.061	
E2	0.35	0.40	0.45	0.014	0.016	0.016	
L	0.20	0.30	0.40	0.008	0.012	0.016	
h	0.05	0.10	0.15	0.002	0.004	0.006	

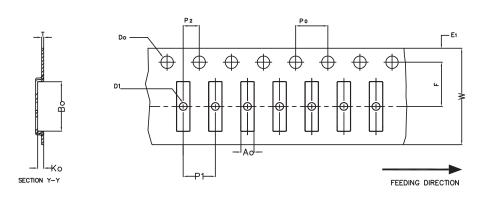
UNIT: mm

Note: 1. Dimensioning and tolerancing conform to ASME Y14.5M-1994.

2. All dimensions are in millimeters.

Tape and Reel Dimensions, DFN 5.5mm x 1.5mm x 0.5mm, 18L, EP3 S

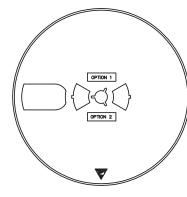
Carrier Tape

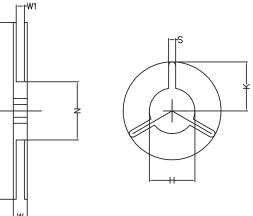


UNIT: MM

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PACKAGE	AO	В0	К0	DO	D1	W	E1	F	P0	P1	P2	Т
DFN5.5x1.5	1.75 ±0.10	5.75 ±0.10	0.70 ±0.10	1.50 +0.10 -0.00	1.00 MIN.	12.00 ±0.3	1.75 ±0.10	5.50 ±0.05	4.00 ±0.10	4.00 ±0.10	2.00 ±0.10	0.30 ±0.05

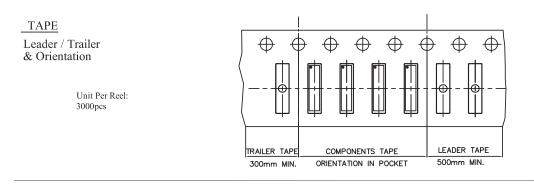






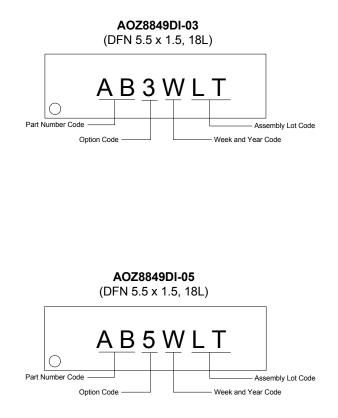
UNIT: MM

TAPE SIZE	REEL SIZE	М	Ν	W	W1	Н	S	к
12	ø178	ø178.0 ±1.0	ø54.0 ±0.5	17.00 ±2.0	13.5 ±0.5	Ø13.0 +0.5 -0.2	2.20 ±0.30	10.25 ±0.2
		1.0	10.0	12.0	10.0	-0.2	10.00	10.2





Part Marking



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