

Depletion-Mode Power MOSFET

eneral Features

- Depletion Mode (Normally On)
- Proprietary Advanced Planar Technology
- Small Package
- ➤ Low Leakage Current
- > RoHS Compliant
- > Halogen-free Available

Part Number	BV _{DSX}	R _{DS(ON)} (Max.)	I_D
DMX4022E	400V	25Ω	0.20A
DMS4022E	400V	25Ω	0.24A

Applications

- > Transient Protect
- ➤ Start-up
- Converters
- Normally On Switches
- ➤ LED Drive Circuits
- ➤ Power Supplies
- Current Source
- Voltage Source



Ordering Information

Part Number	Part Number Package		Remark
DMX4022E	SOT-89	4022	Halogen Free
DMS4022E	SOT-223	4022	Halogen Free

Absolute Maximum Ratings

 $T_A=25$ °C unless otherwise specified

Symbol	Parameter	DMX4022E	DMS4022E	Unit
V_{DSX}	Drain-to-Source Voltage ^[1]	400		V
V_{DGX}	Drain-to-Gate Voltage ^[1]	400	0	V
I_D	Continuous Drain Current	0.20	0.24	Δ.
I_{DM}	Pulsed Drain Current ^[2]	0.80	0.96	A
D	Power Dissipation	1	1.5	W
P_D	Derating Factor above 25℃	0.008	0.012	W/°C
V_{GS}	Gate-to-Source Voltage	±20		V
V	Gate Source ESD ^[3]	3000		V
V_{ESD}	Source to Gate ESD ^[3]	3000		V
$T_{ m L}$	Soldering Temperature Distance of 1.6mm from case for 10 seconds	300		°C
$T_{\text{J}}\text{and}T_{STG}$	Operating and Storage Temperature Range	-55 to 150		

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

Thermal Characteristics

Symbol	Parameter	DMX4022E	DMS4022E	Unit
$R_{ heta JC}$	Thermal Resistance, Junction-to- Case	125	83	°C/W

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Electrical Characteristics OFF Characteristics

 $T_A=25$ °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
BV_{DSX}	Drain-to-Source Breakdown Voltage	400			V	V_{GS} =-5V, I_{D} =250 μ A
	Drain-to-Source Leakage Current Gate-to-Source Leakage Current	-		1	μA	V_{DS} =400V, V_{GS} =-5V
$I_{D(OFF)}$		1		1	mA	V_{DS} =400V, V_{GS} =-5V T_J =125°C
ī		-		5	μΑ	$V_{GS}=+20V, V_{DS}=0V$
I_{GSS}				-5		V_{GS} =-20V, V_{DS} =0V

ON Characteristics

 $T_A = 25$ °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
I _{DSS}	Saturated Drain-to-Source Current	0.2			A	V _{GS} =0V, V _{DS} =25V ^[4]
D	Static Drain-to-Source On-Resistance		12	25	Ω	$V_{GS}=0V$, $I_D=200mA^{[4]}$
N _{DS(ON)}			11	23	Ω	$V_{GS}=10V$, $I_{D}=200 \text{mA}^{[4]}$
V _{GS(OFF)}	Gate-to-Source Cut-off Voltage	-3.3		-1.5	V	$V_{DS}=3V$, $I_D=8\mu A$
gfs	Forward Transconductance		193		mS	V _{DS} =10V, I _D =200mA ^[4]

Dynamic Characteristics

Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
C _{ISS}	Input Capacitance		103.2			$V_{GS}=-5V$
Coss	Output Capacitance		17.7		pF	V_{DS} =25V f=1.0MH _Z
C_{RSS}	Reverse Transfer Capacitance		5.2			
Q_{G}	Total Gate Charge		359.6		nC	V _{GS} =-5V~5V V _{DD} =100V, I _D =200mA
Q_{GS}	Gate-to-Source Charge		61.6			
Q_{GD}	Gate-to-Drain (Miller) Charge		130			

Resistive Switching Characteristics

Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
$t_{d(ON)}$	Turn-on Delay Time		6.6			
t _{rise}	Rise Time		9.2		20	$V_{GS}=-5V\sim5V$
$t_{d(OFF)}$	Turn-off Delay Time		18.8		ns	V_{DD} =100V, I_D =200mA R_G =10 Ω
t_{fall}	Fall Time		356			-

Source-Drain Diode Characteristics

 $T_A=25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min	Тур.	Max.	Units	Test Conditions
V_{SD}	Diode Forward Voltage		0.8	1.5	V	I _{SD} =200mA ^[4] ,V _{GS} =-10V

NOTE:

- [1] $T_J = +25^{\circ}C$ to $+150^{\circ}C$
- [2] Repetitive rating, pulse width limited by maximum junction temperature.
- [3] The test is based on JEDEC EIA/JESD22-A114(HBM).
- [4] Pulse width \(380\mu s; \) duty cycle \(2\% \).

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Typical Characteristics

Figure 1. Maximum Power Dissipation vs. Case Temperature

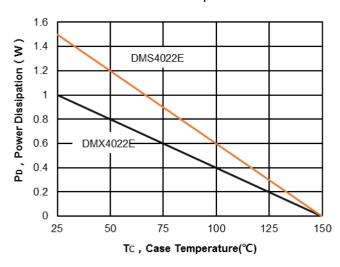


Figure 3. Typical Output Characteristics

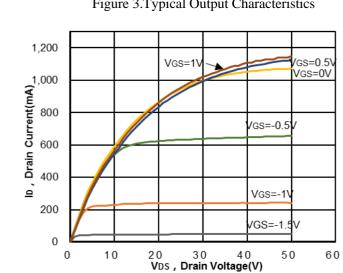


Figure 5. Typical Capacitance vs. Drain-to-Source Voltage

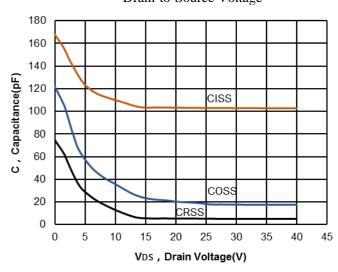


Figure 2. Maximum Continuous Drain Current vs. Case Temperature

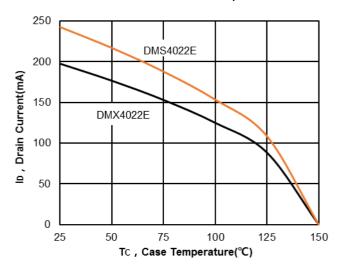


Figure 4. Typical Transfer Characteristics

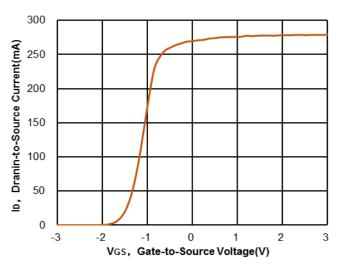
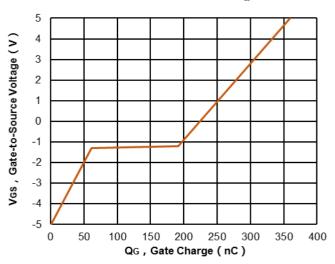


Figure 6. Typical Gate Charge vs. Gate-to-Source Voltage



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Figure 7. Maximum Rated Safe Operating Area

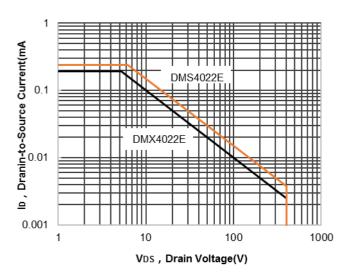


Figure 9. Drain-to-Source On-Resistance vs. Junction Temperature

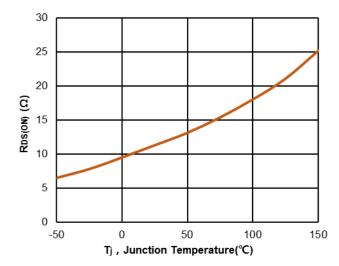


Figure 8. Drain-to-Source On-Resistance vs. Drain Current

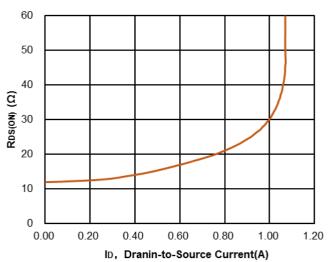
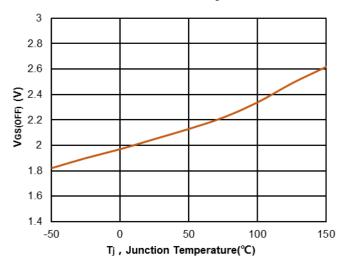


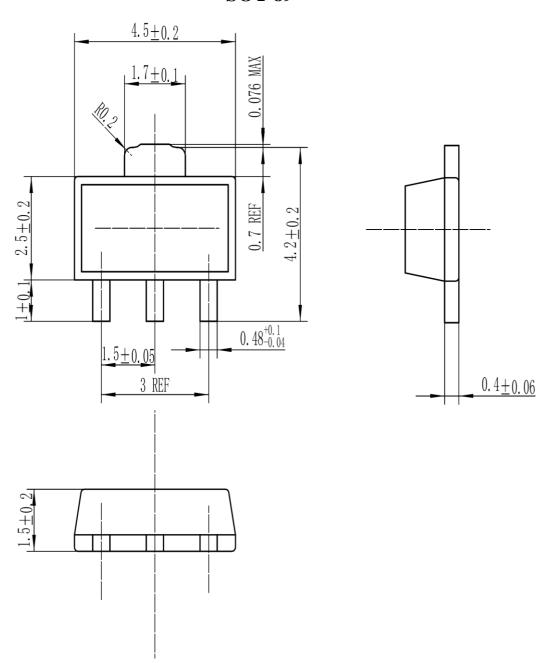
Figure 10. Gate-to-Source Cut-off Voltage vs. Junction Temperature





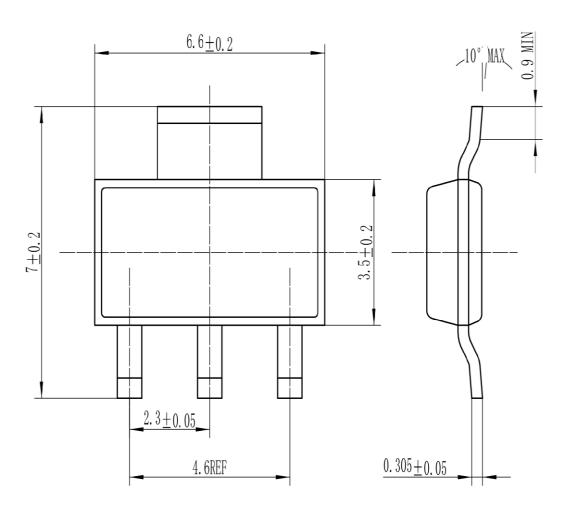
Package Dimensions

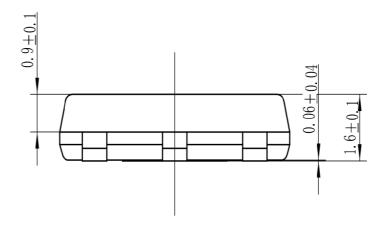
SOT-89





SOT-223







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