

Dual N-Channel 350V Enhancement Mode MOSFETs

General Features

- Proprietary Advanced Planar Technology
- Rugged Polysilicon Gate Cell Structure
- Fast Switching Speed
- > RoHS Compliant
- ➤ Halogen-free available

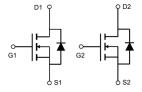
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- ➤ High Efficiency SMPS
- ➤ Adaptor/Charger
- > Active PFC

BV _{DSS}	RDS(ON) (Max.)	ID
350V	15 Ω	1A

PDFN3333





Ordering Information

Part Number Package		Marking	Remark	
FTF15N35D	PDFN3333	15N35D	Halogen Free	

Absolute Maximum Ratings

T_A=25 °C unless otherwise specified

Symbol	Parameter	FTF15N35D	Unit
V _{DSS}	Drain-to-Source Voltage ^[1]	350	V
I_D	Continuous Drain Current	1	Δ.
I_{DM}	Pulsed Drain Current ^[2]	4	A
P_D	Power Dissipation	16	W
V_{GS}	Gate-to-Source Voltage	±20	V
$T_{\rm L}$	Soldering Temperature Distance of 1.6mm from case for 10 seconds	300	Ŝ
T _J and T _{STG}	Operating and Storage Temperature Range	-55 to 150	

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

Thermal Characteristics

Symbol	Parameter	FTF15N35D	Unit
$R_{ heta JA}$	Thermal Resistance, Junction-to-Ambient	34	K/W



Electrical Characteristics

OFF Characteristics

 $T_A = 25$ °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
BV _{DSS}	Drain-to-Source Breakdown Voltage	350			V	V_{GS} =0V, I_{D} =250 μ A
I _{DSS}	Drain-to-Source Leakage Current			1	μΑ	V_{DS} =350V, V_{GS} = 0V
				100	μA	V_{DS} =350V, V_{GS} = 0V T_J =125°C
I _{GSS}	Gate-to-Source Leakage Current			20	4	$V_{GS} = +20V, V_{DS} = 0V$
				-20	μΑ	V_{GS} =-20V, V_{DS} =0V

ON Characteristics

$T_A = 25$ °C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
R _{DS(ON)}	Static Drain-to-Source On-Resistance		8	15	Ω	$V_{GS}=10V$, $I_D=200$ mA [3]
$V_{\text{GS(TH)}}$	Gate Threshold Voltage	1		3	V	$V_{GD} = 0V, I_D = 250 \mu A$

Dynamic Characteristics

Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
C_{ISS}	Input Capacitance		32.58			V _{GS} =0V
Coss	Oput Capacitance		5.36		pF	$V_{DS}=25V$
C _{RSS}	Reverse Transfer Capacitance		0.75			$f=1.0MH_Z$
$t_{d(ON)}$	Turn-on Delay Time		14			
t _{rise}	Rise Time		10		ns	$V_{DD} = 25V, I_D=80mA$ $R_G = 25Ohm$ $V_{GS} = 10V\sim0V$
$t_{d(OFF)}$	Turn-off Delay Time		24			
t_{fall}	Fall Time		36			

Source-Drain Diode Characteristics

Symbol	Parameter	Min	Тур.	Max.	Units	Test Conditions
V_{SD}	Diode Forward Voltage			1.8	V	$I_{SD}=200~\text{mA},~V_{GS}=0~\text{V}$

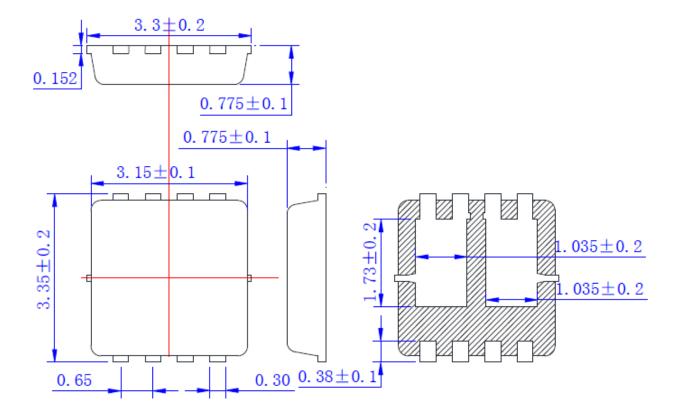
NOTE:

- [1] $T_J = +25$ °C to +150°C
- [2] Repetitive rating, pulse width limited by maximum junction temperature.
- [3] Pulse width≤380μs; duty cycle≤2%.



Package Dimensions

PDFN3333





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