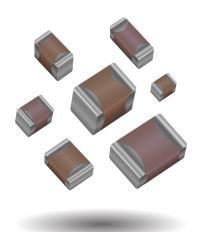
MLCC Medical Applications – MM Series

General Specifications





The AVX MM series is a multi-layer ceramic capacitor designed for use in medical applications other than implantable/life support. These components have the design & change control expected for medical devices and also offer enhanced LAT including reliability testing and 100% inspection.

APPLICATIONS

Implantable, Non-Life Supporting Medical Devices

· e.g. implanted temporary cardiac monitor, insulin pumps

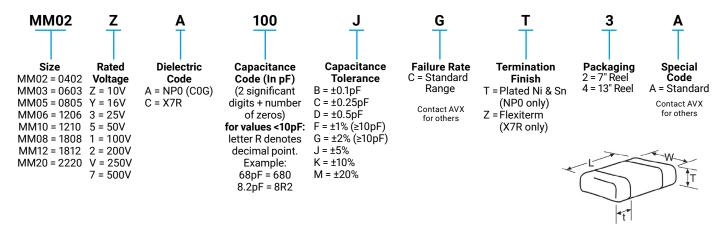
External, Life Supporting Medical Devices

· e.g. heart pump external controller

External Devices

· e.g. patient monitoring, diagnostic equipment

HOW TO ORDER



COMMERCIAL VS MM SERIES PROCESS COMPARISON

	Commercial	MM Series
Administrative	Standard part numbers; no restriction on who purchases these parts	Specific series part number, used to control supply of product
Design	Minimum ceramic thickness of 0.020" on all X7R product	Minimum ceramic thickness of 0.022" (0.56mm)
Dicing	Side & end margins = 0.003" min	Side & end margins = 0.004" min Cover layers = 0.003" min
Lot Qualification Destructive Physical Analysis (DPA)	As per EIA RS469	Increased sample plan – stricter criteria
Visual/Cosmetic Quality	Standard process and inspection	100% inspection
Application Robustness	Standard sampling for accelerated wave solder on X7R dielectrics	Increased sampling for accelerated wave solder on X7R and NP0 followed by lot by lot reliability testing
Design/Change Control	Required to inform customer of changes in: form fit function	AVX will qualify and notify customers before making any change to the following materials or processes: • Dielectric formulation, type, or supplier • Metal formulation, type, or supplier • Termination material formulation, type, or supplier • Manufacturing equipment type • Quality testing regime including sample size and accept/ reject criteria



NP0 (C0G) - Specifications & Test Methods

Parame	ter/Test	NP0 Specification Limits	Measuring Conditions											
Operating Tem		-55°C to +125°C	Temperature Cycle Chamber											
Capac	itance Q	Within specified tolerance <30 pF: Q≥ 400+20 x Cap Value ≥30 pF: Q≥ 1000	Freq.: 1.0 MHz ± 10% for cap ≤ 1000 pF 1.0 kHz ± 10% for cap > 1000 pF Voltage: 1.0Vrms ± .2V											
Insulation	Resistance	100,000MΩ or 1000MΩ - μF, whichever is less	Charge device with rated voltage for 60 ± 5 secs @ room temp/humidity											
Dielectric	Strength	No breakdown or visual defects	Charge device with 300% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max) Note: Charge device with 150% of rated voltage for 500V devices.											
	Appearance	No defects	Deflection: 2mm											
Resistance to	Capacitance Variation	±5% or ±.5 pF, whichever is greater	Test Time: 30 seconds 1mm/sec											
Flexure Stresses	Q	Meets Initial Values (As Above)												
	Insulation Resistance	≥ Initial Value x 0.3	90 mm —											
Solderability		≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic solder at 230 ± 5°C for 5.0 ± 0.5 seconds											
	Appearance	No defects, <25% leaching of either end terminal												
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater												
Resistance to Solder Heat	Q	Meets Initial Values (As Above)	Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2											
Solder Heat	Insulation Resistance	Meets Initial Values (As Above)	hours before measuring electrical properties.											
	Dielectric Strength	Meets Initial Values (As Above)												
	Appearance	No visual defects	Step 1: -55°C ± 2° 30 ± 3 minutes											
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Step 2: Room Temp ≤ 3 minutes											
	Q	Meets Initial Values (As Above)	Step 3: +125°C ± 2° 30 ± 3 minutes											
o.i.oux	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp ≤ 3 minutes											
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles and measure after 24 hours at room temperature											
	Appearance	No visual defects												
	Capacitance Variation	≤ ±3.0% or ± .3 pF, whichever is greater	Charge device with twice rated voltage in test chamber set at 125°C ± 2°C											
Load Life	Q	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C	for 1000 hours (+48, -0). Remove from test chamber and stabilize at											
Shock	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	room temperature for 24 hours before measuring.											
	Dielectric Strength	Meets Initial Values (As Above)	selete mededing.											
	Appearance	No visual defects												
	Capacitance Variation	≤ ±5.0% or ± .5 pF, whichever is greater	Store in a test chamber set at 85°C ± 2°C/ 85%											
Load Humidity	Q	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C	± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.											
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber and stabilize at room temperature for 24 ± 2 hours before measuring.											
	Dielectric Strength	Meets Initial Values (As Above)												



NP0/C0G Capacitance Range

PREFERRED SIZES ARE SHADED

S	ΖE			06	03				0805		1206							
	WVDC		16	25	50	100	16	25	50	100	16	25	50	100				
Cap (0.5	0R5																
	1.0	1R0																
	1.2	1R2																
	1.5	1R5																
	1.8	1R8																
	2.2	2R2																
	2.7	2R7																
	3.3	3R3																
	3.9	3R9																
	1.7	4R7																
	5.6	5R6																
	5.8	6R8																
	3.2	8R2																
	10	100																
	12	120																
	15	150																
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		330																
	33 39	330																
	47 47	470																
	56	560																
	68	680				-												
	82	820																
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5	60	561				ĺ												
	80	681																
	20	821																
	00	102																
	00	122																
	00	152																
W	VDC		16	25	50	100	16	25	50	100	16	25	50	100				
S	ΖE			06	03				0805	1206								



X7R Specifications and Test Methods

Parameter/Test Operating Temperature Range		X7R Specification Limits	Measuring Conditions									
Operating Tem	perature Range	-55°C to +125°C	Temperature Cycle Chamber									
Capac	itance	Within specified tolerance										
(Q.	≤ 10% for ≥ 50V DC rating ≤ 12.5% for 25V DC rating ≤ 12.5% for 25V and 16V DC rating ≤ 12.5% for ≤ 10V DC rating	Freq.: 1.0 kHz ± 10% Voltage: 1.0Vrms ± .2V									
Insulation	Resistance	100,000MΩ or 1000MΩ - μF, whichever is less	Charge device with rated voltage for 120 ± 5 secs @ room temp/humidity									
Dielectric	: Strength	No breakdown or visual defects	Charge device with 300% of rated voltage for 1-5 seconds, w/charge and discharge current limited to 50 mA (max) Note: Charge device with 150% of rated voltage for 500V devices.									
	Appearance	No defects	Deflection	n: 2mm								
Resistance to	Capacitance Variation	≤ ±12%	Test Time: 3									
Flexure Stresses	Dissipation Factor	Meets Initial Values (As Above)										
Insulation Resistance Solderability		≥ Initial Value x 0.3	90 n									
Solderability Appearance		≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic for 5.0 ± 0.5	solder at 230 ± 5°C seconds								
	Appearance	No defects, <25% leaching of either end terminal	_									
	Capacitance Variation	≤ ±7.5%										
Resistance to	Dissipation Factor	Meets Initial Values (As Above)	Dip device in eutectic s seconds. Store at room	temperature for 24 ± 2								
Solder Heat	Insulation Resistance	Meets Initial Values (As Above)	hours before measuring	gelectrical properties.								
	Dielectric Strength	Meets Initial Values (As Above)										
	Appearance	No visual defects	Step 1: -55°C ± 2°	30 ± 3 minutes								
	Variation	≤ ±7.5%	Step 2: Room Temp	≤ 3 minutes								
Thermal Shock	Factor	Meets Initial Values (As Above)	Step 3: +125°C ± 2°	30 ± 3 minutes								
o.i.oux	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp	≤ 3 minutes								
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles a 24 ± 2 hours at ro									
	Appearance	No visual defects										
	Capacitance Variation	≤ ±12.5%	Charge device with 1.5 r test chamber set	ated voltage (≤ 10V) in at 125°C ± 2°C								
Load Life	Insulation Resistance Dielectric Strength Appearance Capacitance Variation Dissipation Factor Insulation Resistance Dielectric Strength Appearance Capacitance Variation Dissipation Factor Insulation Resistance Variation Dissipation Factor Insulation Resistance Dielectric Strength	≤ Initial Value x 2.0 (See Above)	for 1000 hou	rs (+48, -0)								
		≥ Initial Value x 0.3 (See Above)	Remove from test chai room temperature for	24 ± 2 hours before								
		Meets Initial Values (As Above)	measu	ring.								
	Appearance	No visual defects										
	Capacitance Variation	≤ ±12.5%	Store in a test chamber s ± 5% relative humid									
Load	Dissipation Factor	≤ Initial Value x 2.0 (See Above)	(+48, -0) with rated									
Humidity	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber temperature an	d humidity for								
	Dielectric Strength	Meets Initial Values (As Above)	24 ± 2 hours befo	ore measuring.								



X7R Capacitance Range

PREFERRED SIZES ARE SHADED

SIZE		()40	2	0603										080	5						12	06							12	10				1	808	В		18	12		2220		
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单击下面可查看定价,库存,交付和生命周期等信息

>>AVX