# SMD Low ESR Conductive Polymer Capacitors in Hermetic package, COTS-Plus





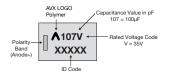
#### **FEATURES**

- · Aerospace & Hi-Rel applications
- · Low ESR conductive polymer electrode
- · Endurance up to 10 000 hrs. on selected codes
- · Ceramic case hermetic packaging
- · Stability under humidity and ambient atmosphere exposure
- · Large case sizes including CTC-21D provide high capacitance values
- · Developed with ESA to suit aerospace applications
- · Ongoing ESA qualification



Elektra Award 2015

### MARKING 9 CASE



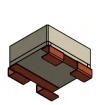
### **APPLICATIONS**

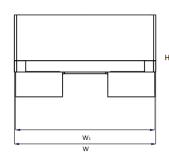
- Aerospace
- Defence
- · Power supplies
- · Pulse power

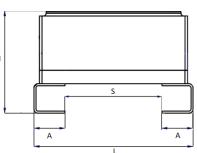
### **CASE DIMENSIONS:** millimeters (inches)

			•				
Code	Туре	L	W	H Max.	W <sub>1</sub>	Α	S Min.
9 (CTC-21D)	J-lead (C-shape)	12.00 ± 0.50 (0.472 ± 0.020)	12.50 ± 0.50 (0.492 ± 0.020)	8.45 (0.333)	12.30 ± 0.50 (0.484 ± 0.020)	2.30 ± 0.50 (0.091 ± 0.020)	6.50 (0.256)
9 (CTC-21D)	J-lead (L-shape)	11.50 ± 0.50 (0.453 ± 0.020)	12.50 ± 0.50 (0.492 ± 0.020)	6.15 (0.242)	12.50 ± 0.50 (0.492 ± 0.020)	1.90 ± 0.50 (0.075 ± 0.020)	7.00 (0.276)
9 (CTC-21D)	Undertab	11.00 ± 0.20 (0.433 ± 0.008)	12.50± 0.20 (0.492 ± 0.008)	5.95 (0.234)	10.50± 0.20 (0.413 ± 0.008)	1.50± 0.20 (0.059 ± 0.008)	7.80 (0.307)

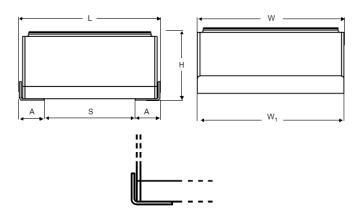
### 'J' Lead Termination (C-shape)



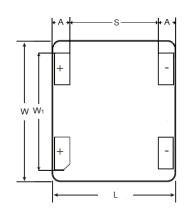


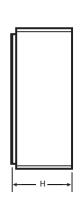


### 'J' Lead Termination (L-shape)



### **Undertab Termination**







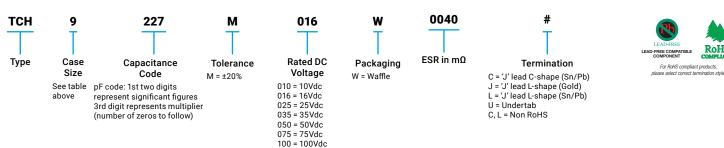
# SMD Low ESR Conductive Polymer Capacitors in Hermetic package, COTS-Plus



### **TECHNICAL SPECIFICATIONS**

Technical Data:	All techn	All technical data relate to an ambient temperature of +25°C								
Capacitance Range:	22 μF to 330 μF (for extended range under development, contact manufacturer)									
Capacitance Tolerance:		±20%								
Leakage Current DCL:		0.1CV								
Rated Voltage (VR)	≤ +85°C:	10	16	25	35	50	75	100		
Category Voltage (VC)	≤ +125°C:	7	11	17	23	33	50	66		
Temperature Range:		-55°C to	+125°C							
Termination Finish:	Gold Plat	Gold Plating (Undertab), Gold Plating (J-lead/L-shape), Sn/Pb Plating (J-lead/ C-shape, L-shape)								

# HOW TO ORDER AVX PART NUMBER



# CAPACITANCE AND VOLTAGE RANGE (CASE CODE BEFORE THE BRACKETS)

Capacitance		Rated Voltage DC (VR) at 85°C										
Code	10V	16V	25V	35V	50V	75V	100V					
226							9(150)					
336						9(120)						
476					9(70)							
686												
107				9(55)								
157			9(50)	9(55)								
227		9(40)										
337	9(40)											
	226 336 476 686 107 157 227	Code 10V   226 336   476 686   107 157   227 337 9(40)	Code 10V 16V   226 336 476   476 686 107   157 227 9(40)   337 9(40)	Code 10V 16V 25V   226 336 476 686 9	Code 10V 16V 25V 35V   226 336 476 686 9(55) 9(55) 9(55) 9(55) 9(50) 9(55) 9(55) 9(50) <	Code 10V 16V 25V 35V 50V   226 336 9(70)	Code 10V 16V 25V 35V 50V 75V   226 336 9(120) 9(120)   476 9(70) 9(50) 9(55) 9(55)   157 9(50) 9(55) 9(50) 9(55) 9(50) </td					

 ${\sf Released\ ratings, (ESR\ ratings\ in\ mOhms\ in\ parentheses)}$ 

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#### **RATINGS & PART NUMBER REFERENCE**

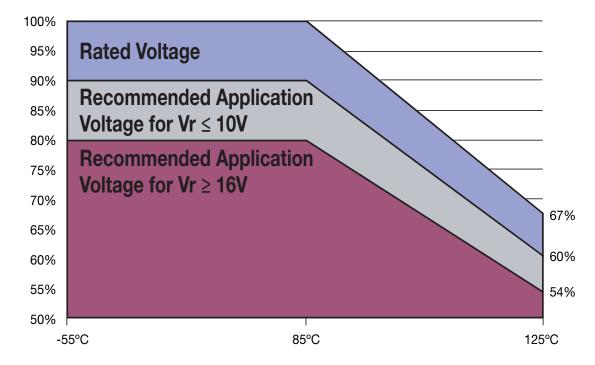
AVX Part No.	Case Size			Capacitance (µF)	Rated Voltage (V)	Rated Temperature (°C)	Category Voltage (V)	Category Temperature (°C)	DCL Max. (µA)	DF Max. (%)	ESR Max. @ 100kHz		0kHz R urrent		Endurance at 85°C (hrs)
			(V)	( 0)	(*)	( 6)	(μΑ)	(%)	(mΩ)	25°C	85°C	125°C	(1113)		
10 Volt @ 85°C															
TCH9337M010W0040#	9	330	10	85	7	125	330	8	40	3.16	2.84	1.26	2000		
16 Volt @ 85°C															
TCH9227M016W0040#	9	220	16	85	10	125	352	8	40	3.16	2.84	1.26	10000		
25 Volt @ 85°C															
TCH9157M025W0050#	9	150	25	85	17	125	375	8	50	2.83	2.55	1.13	10000		
				3	5 Volt @ 85	5°C									
TCH9107M035W0055#	9	100	35	85	23	125	350	8	55	2.69	2.42	1.08	10000		
TCH9157M035W0055#	9	150	35	85	23	125	525	8	55	2.69	2.42	1.08	2000		
				5	0 Volt @ 85	5°C									
TCH9476M050W0070#	9	47	50	85	33	125	235	8	70	2.39	2.15	0.96	10000		
				7	5 Volt @ 85	5°C									
TCH9336M075W0120#	9	33	75	85	50	125	248	8	120	1.82	1.64	0.73	2000		
				10	00 Volt @ 8	5°C									
TCH9226M100W0150#	9	22	100	85	66	125	220	8	150	1.63	1.47	0.65	10000		

All technical data relates to an ambient temperature of +25°C. Capacitance and DF are measured at 120Hz, 0.5RMS with a maximum DC bias of 2.2V. DCL is measured at rated voltage after 5 minutes.

Moisture Sensitivity Level (MSL) is defined according to J-STD-020. All TCH products are MSL1.

#### RECOMMENDED DERATING FACTOR

Voltage and temperature derating as percentage of Vr



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### **QUALIFICATION TABLE**

TEST	TCH low ESR hermetic series (Temperature range -55°C to +125°C)												
1531		Condi	tion	Characteristics									
	Determin	ne after application o	f rated voltage for 2000	Visual examination	no visible	e damage							
	(10 000)	+48/0 hours at 85±2	°C and then leaving min.	DCL	1.25 x ini	tial limit							
Endurance			Also determine of 125°C e for 2000 +48/-0 hours	ΔC/C	within ±20% of initial value								
	and then	leaving min. 2 hours	at room temperature.	DF	1.5 x initial limit								
	Power su	upply impedance to b	oe < 3Ω.	ESR	2 x initial	2 x initial limit							
				Visual examination	no visible	e damage							
	Store at	125°C no voltage an	plied, for 2000 hours.	DCL	2 x initial	limit							
Storage Life			e for 1-2 hours before	ΔC/C	within ±2	20% of init	ial value						
	measurir	ng.		DF	1.5 x initi	al limit							
				ESR	2 x initial limit								
Humidity				Visual examination	no visible damage								
	0.1	4000 1000 1	. L	DCL	1.25 x initial limit								
			e humidity for 56 days, lize at room temperature	ΔC/C	within ±10% of initial value								
		idity for min. 2 hours		DF	initial limit								
				ESR	1.25 x initial limit								
	Step	Temperature°C	Duration (min)		+20°C	-55°C	+20°C	+85°C	+125°C	+20°C			
	1	+20	15	DCL	11*	n/a	IL*	10 x IL*	12.5 x IL*	11*			
Temperature	2	-55	15		<del>                                     </del>								
Stability	3 4	+20 +85	15 15	ΔC/C	n/a	+0/-20%	±5%	+20/-0%	+30/-0%	±5%			
	5	+125	15	− DF	IL*	1.5 x IL*	IL*	1.5 x IL*	2 x IL*	IL*			
	6	+20	15	ESR	1.25 x IL*	1.25 x IL*	1.25 x IL*	1.5 x IL*	1.5 x IL*	1.25 x IL			
		perature: 85°C+3/0° Itage: 1.3 x rated volt		Visual examination	no visible damage								
	1.15 x rat	ed voltage (for Ur > 5	50V)	DCL	initial limit								
Surge Voltage	1000Ω (f	rotection resistance: or Ur > 50V)	33Ω (for Ur ≤ 50V),	ΔC/C	within ±20% of initial value								
voltage		je resistance: 33Ω of cycles: 1000x		DF	initial lim	initial limit							
		ration: 6 min; 30 sec 5 min 30 sec		ESR	1.25 x initial limit								
		3 111111 30 Sec	aisonal ye	Visual examination									
	MILOTO	-202 Mathad 212 Cr	ondition C, 100 G peak	DCL	initial limit								
Mechanical		-202, Method 213, Co -202, Method 204, Co		ΔC/C	within ±10% of initial value								
Shock/Vibration		2,000 Hz, 20 G peak		DF	initial limit								
		•		ESR	1.25 x initial limit								

# 单击下面可查看定价,库存,交付和生命周期等信息

## >>AVX