# 0.1-6GHz SPDT Switch for High Power Applications

#### **Features**

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- Broadband frequency range: 0.1 to 6 GHz
- High power handing capability of up to 38 dBm
- Low insertion loss : 0.7 dB typical @ 6.0 GHz
- High isolation: 22 dB typical @ 6.0 GHz
- High switching speed: 1 us typical
- Low harmonic generation
- Small FCDFN 1.1mm x 0.7mm x 0.37mm-6L package

#### **Applications**

- Multi-Mode GSM/CDMA/WCDMA/LTE and NR including n77, n78, n79 bands
- Cellular modems, tablets and USB Devices
- Other RF front-end modules

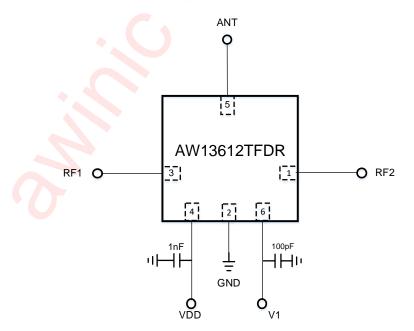
#### **General Description**

The AW13612TFDR is a single-pole dual-throw switch with high power handing capability of up to 38dBm and low insertion loss. It can be used to support band switching and mode switching for GSM, WCDMA, LTE, and NR applications.

The symmetrical design of internal ports makes it convenient for PCB routing and adjustment of receiving and transmitting signals. The band/mode switching is realized by the GPIO pins as referenced in the chip block diagram and the control logic.

The AW13612TFDR is provided in a compact FCDFN 1.1mm x 0.7mm x 0.37mm-6L package.

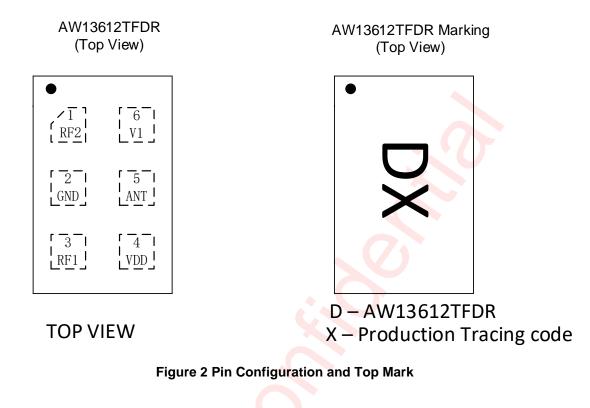
#### **Typical Application Circuit**



#### Figure 1 Typical Application Circuit of AW13612TFDR

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### **Pin Configuration And Top Mark**



#### **Pin Definition**

No.	NAME	DESCRIPTION			
1	RF2	RF I/O path 2			
2	GND	Ground			
3	RF1	RF I/O path 1			
4	VDD	DC power supply			
5	ANT	Antenna port			
6	V1	DC control voltage 1			

#### **Functional Block Diagram**

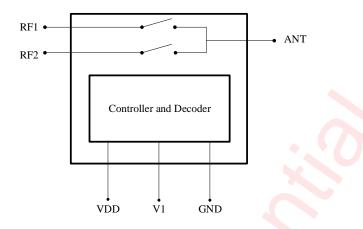


Figure 3 Functional Block Diagram

#### **Ordering Information**

Part Number	Temperature	Package	Marking	Moisture Sensitivity Level	Environmental Information	Delivery Form
AW13612 TFDR	-40°C∼85°C	FCDFN 1.1mmX0.7mm- 6L	D	MSL1	RoHS+HF	9000 units/ Tape and Reel

# Absolute Maximum Ratings(NOTE1)

PARAM	RANGE				
Supply voltag	-0.3V to 3.6V				
Control Voltage Range	Control Voltage Range V1				
RF input pow	RF input power(RF1/RF2)				
Operating Free-air	Operating Free-air Temperature Range				
Storage temp	-65°C to 150°C				
Lead temperature (s	260°C				
ESD					
HBM(Human Bo	±1000V				
CDM (Charged De	±500V				

NOTE1: Conditions out of those ranges listed in "absolute maximum ratings" may cause permanent damages to the device. In spite of the limits above, functional operation conditions of the device should within the ranges listed in "recommended operating conditions". Exposure to absolute-maximum-rated conditions for prolonged periods may affect device reliability.

NOTE2: The human body model is a 100pF capacitor discharged through a  $1.5k\Omega$  resistor into each pin. Test method: ESDA/JEDEC JS-001-2017.

NOTE3: All pins. Test Condition: ESDA/JEDEC JS-002-2018.

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### **Electrical Characteristics**

VDD=1.8V, V1=0/1.8V, PIN=0dBm, TOP=+25°C, Z<sub>0</sub>=50Ω. (unless otherwise noted)

Supply Voltage Supply Current Control Voltage High		1.62			
Supply Current Control Voltage		1.62			
Control Voltage			1.8	3.3	V
-			40	100	μA
-					
-		0.9	1.8	3.3	V
Low		-0.3		0.3	
Control Current	VCTL = 1.8V		0.1	1	μA
Turn-on Switching Time	50% of final control voltage to 90% of final RF power, switching between RF1/2		1.0	2.0	μs
ns			1		
	617-960MHz		0.24	0.33	dB
	960-2170 MHz		0.29	0.38	dB
Incontion loss	2170-2700 MHz		0.34	0.45	dB
Insertion loss	33 <mark>0</mark> 0-3800 MHz		0.41	0.57	dB
	3800-5000 MHz		0.53	0.69	dB
	5150-5925 MHz		0.7	0.79	dB
	617-960MHz	37	42		dB
	960-2170 MHz	30	34		dB
Isolation	2170-2700 MHz	25	30		dB
ISUIAIIUT	3300-3800 MHz	22	26		dB
	3800-5000 MHz	21	25		dB
	5150-5925 MHz	18	22		dB
	617-960MHz	21	25		dB
	960-2170 MHz	16	19		dB
Input return loss	2170-2700 MHz	15	18		dB
	3300-3800 MHz	14	17		dB
	3800-5000 MHz	12	15		dB
	5150-5925 MHz	10	12		dB
Second harmonics	PIN=+35dBm,		60	50	dD
	915MHz,CW		-02	-52	dBm
Third harmonics	PIN=+35dBm,		-58	-50	dBm
	Second harmonics	Second harmonics PIN=+35dBm, 915MHz,CW PIN=+35dBm,	5150-5925 MHz10Second harmonicsPIN=+35dBm, 915MHz,CWThird harmonicsPIN=+35dBm,	5150-5925 MHz 10 12   Second harmonics PIN=+35dBm, 915MHz,CW -62   Third harmonics PIN=+35dBm, -58 -58	5150-5925 MHz 10 12   Second harmonics PIN=+35dBm, 915MHz,CW -62 -52   Third harmonics PIN=+35dBm, -58 -50

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	PARAMETER	TEST CONDITION	MIN	ТҮР	MAX	UNIT
P0.1dB	0.1dB Compression Point	0.1-6 GHz		38		dBm

#### **Power ON and OFF Sequence**

It is very important that the user adheres to the correct power-on/off sequence in order to avoid

damaging the device. The control signal V1 should be set to 0V unless VDD is set in the operating voltage range.

Power ON:

- 1) Apply voltage supply --- VDD
- 2) Set Controls---V1
- 3) Apply RF input

Change switch position from one RF port to another:

- 1) Remove RF input
- 2) Change control voltages V1 to set the switch to desired RF port
- 3) Apply RF input

#### Power OFF:

- 1) Remove RF input
- 2) Remove control voltages-V1
- 3) Remove VDD input

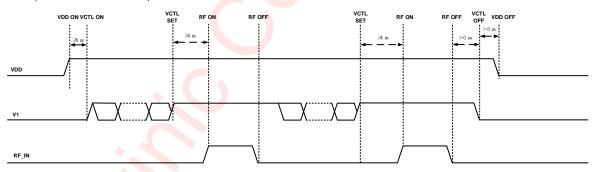
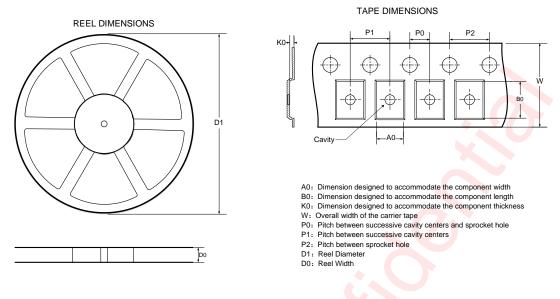


Figure 4 Power on/Change switch/Power off sequence

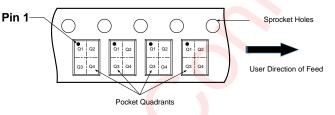
## **Control Logic**

State	Active Path	V1		
0	ANT to RF1	0		
1	ANT to RF2	1		

## **Tape And Reel Information**



QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



DIMENSIONS AND PIN1 ORIENTATION

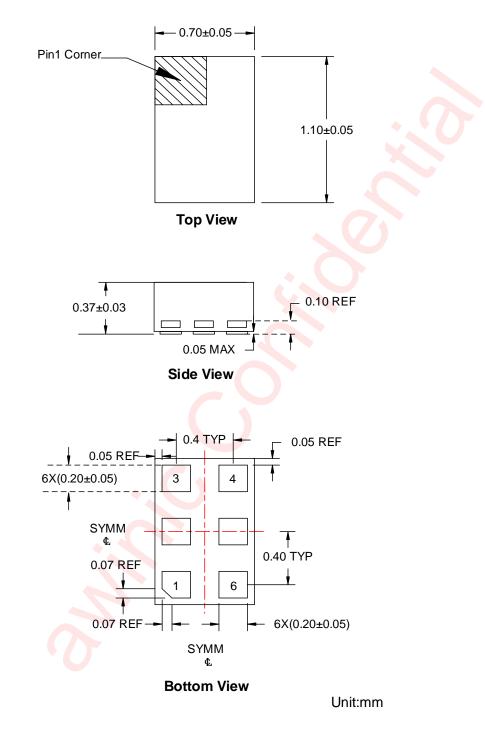
DINERC									
D1	D0	A0	<b>B0</b>	K0	P0	P1	P2	w	Pin1 Quadrant
(mm)	(mm)	(mm)	(mm)	(mm)	(mm)	(mm)	(mm)	(mm)	
178.0	8.40	0.82	1.22	0.46	2.00	2.00	4.00	8.00	Q1
	neione	are nomi	nal						•

All dimensions are nominal

Figure 5 Tape and Reel



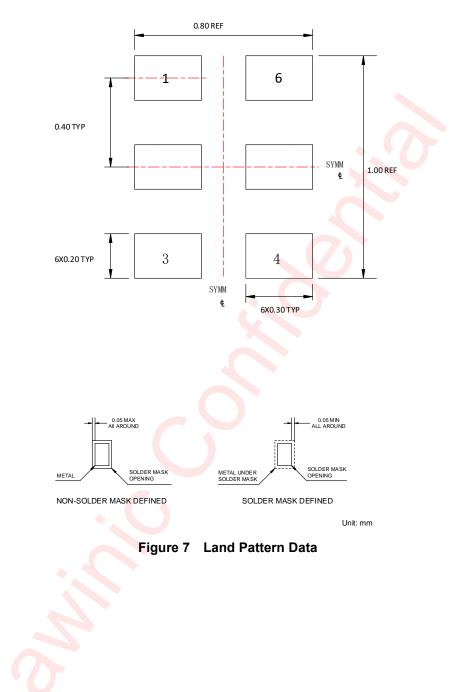
### **Package Description**







#### Land Pattern Data





## **Revision History**

Version	Date	Change Record
V1.0	Nov. 2021	Officially Released

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