

Features

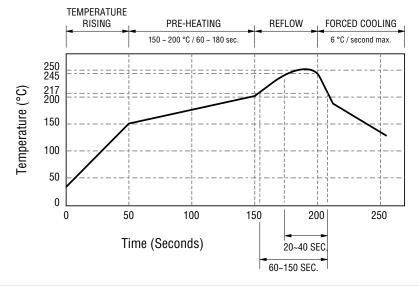
- High rated current
- High resistance to heat and humidity
- Resistance to mechanical shock and pressure
- Accurate dimensions for automatic placement
- AEC-Q200 compliant
- RoHS compliant*

CC322522A Series – Chip Inductors

Electrical Specifications @ 25 °C

Bourns® Part No.	Inductance		Q Min.	L & Q Test Frequency	SRF (MHz)	DCR (Ω)	Irms (A)	Isat (A)
	L (μH)	Tol.		(MHz)	Min.	Max.	Тур.	Тур.
CC322522A-1R0K	1.00	±10 %	8	7.960	100	0.156	1.450	1.350
CC322522A-1R5K	1.50	±10 %	8	7.960	80	0.195	1.350	1.100
CC322522A-2R2K	2.20	±10 %	8	7.960	65	0.250	1.150	1.000
CC322522A-3R3K	3.30	±10 %	8	7.960	55	0.320	0.950	0.750
CC322522A-4R7K	4.70	±10 %	8	7.960	46	0.380	0.900	0.700
CC322522A-6R8K	6.80	±10 %	8	7.960	35	0.580	0.700	0.500
CC322522A-100K	10.00	±10 %	15	2.520	28	0.890	0.600	0.450
CC322522A-120K	12.00	±10 %	15	2.520	26	1.100	0.500	0.400
CC322522A-150K	15.00	±10 %	15	2.520	25	1.450	0.450	0.350
CC322522A-220K	22.00	±10 %	15	2.520	20	2.000	0.370	0.300
CC322522A-330K	33.00	±10 %	15	2.520	15	2.800	0.300	0.250
CC322522A-470K	47.00	±10 %	20	2.520	11	4.800	0.270	0.205
CC322522A-680K	68.00	±10 %	20	2.520	10	6.900	0.220	0.170
CC322522A-101K	100.00	±10 %	20	0.796	8	9.000	0.180	0.150

Soldering Profile (Lead Free Solder)



Reflow Profile

Peak Temperature	250 °C max.
Max. Time Above 245 °C	
Max. Time Above 217 °C	60-150 sec. max.
200 °C ~ 250 °C Average Ramp-up Rate	3 °C/sec. max.

General Specifications

Operating Temperature

.....-40 °C to +150 °C (Temperature rise included) Storage Temperature ..- 40 °C to +150 °C Rated Current:

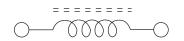
..... Inductance drops 10% at Isat Temperature Rise40 °C at rated current

Moisture Sensitivity Level.....1 ESD Classification (HBM).....N/A

Materials

Core	Ferrite
Wire	Enameled Copper
Enclosure	Epoxy resin
Terminal	Sn
Packaging	. 2000 pcs. per 7-inch reel

Schematic





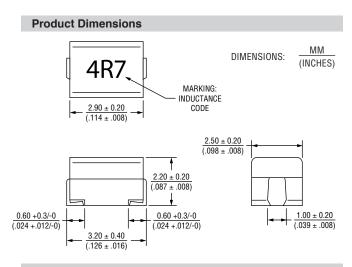
*RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011. Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

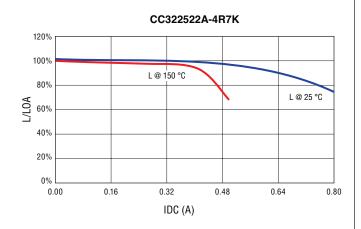
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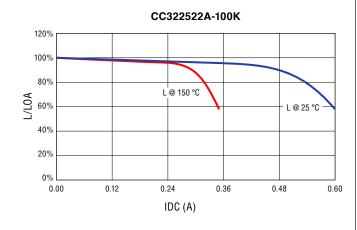
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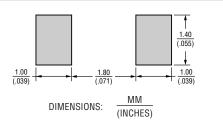


L vs. IDC





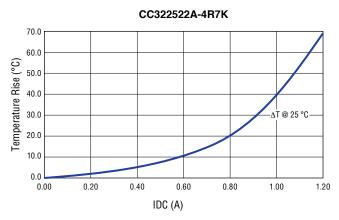
Recommended Layout

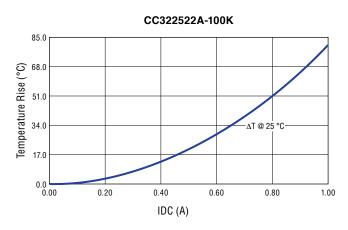


How to Order



Temperature Rise vs. IDC



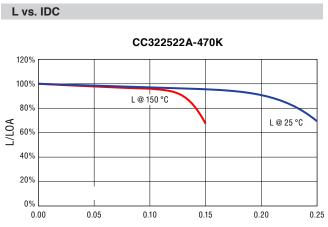


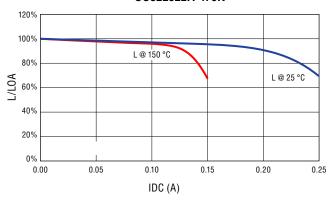
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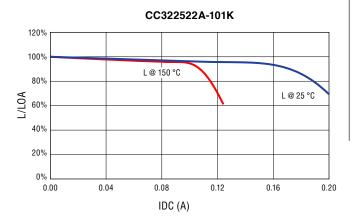
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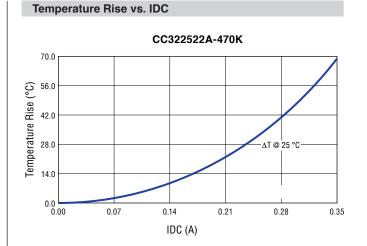
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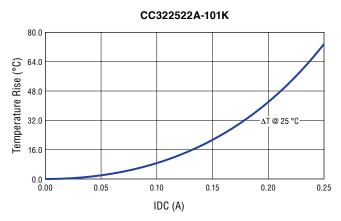
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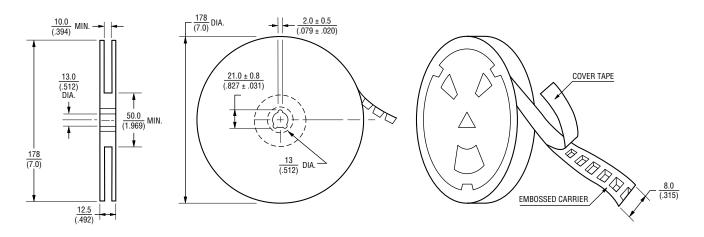


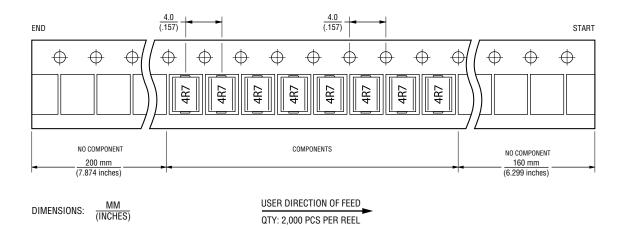


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Packaging Specifications





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Users should verify actual device performance in their specific applications.

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