

#### **Features**

- Low voltage operation
- Low current consumption
- Fast response time
- I<sup>2</sup>C communication protocol
- Reliable capacitive technology
- Relative humidity accuracy of ±2 % (Typical)

### **Applications**

#### Industrial:

- HVAC systems
- Process monitoring
- Climate monitoring, interior and exterior
- \*\*Medical Devices (low/medium risk):
- Diagnostic equipment
- Analysis equipment

# **BPS240 Series - 2 mm Humidity Sensor**

Absolute Maximum Ratings	Additional Information
Supply Voltage ( $V_{\rm CC}$ )0.3 to 7.0 V Input Voltage ( $V_{\rm I}$ )	Click these links for more information:
CE0.3 to V <sub>CC</sub> + 0.3 V	
SCL/SDA0.3 to 7.0 V	
Output Voltage (VO)0.3 to V <sub>CC</sub> + 0.3 V	
Hi-level Output Current (IOH)	PRODUCT TECHNICAL INVENTORY SAMPLES CONTACT
1 Terminal5 mA	<u>LIBRARY</u>
All Terminals Total20 mA	
Low-level Output Current (IOL)	
1 Terminal	
All Terminals Total	
Operating Temperature (T <sub>a</sub> )40 °C to +105 °C	
Storage Temperature (T <sub>stg</sub> )50 °C to +125 °C	1
Recommended Operating Conditions	
Power Supply Voltage (V <sub>CC</sub> )	
Capacitance between V <sub>CC</sub> and V <sub>SS</sub> (C <sub>p</sub> )	
Pull Up Resistor Value on SDA <sup>1</sup> (R1)	
Pull Up Resistor Value on SCL <sup>1</sup> (R2)	5 kΩ typical
<sup>1</sup> Select the resistance value to meet AC characteristics.	
Electrical Characteristics	
Humidity Detection	
Measurement Range	0 to 100 % RH
Resolution (10-bit)	0.1 % RH typical
Humidity Accuracy - Typical (see Humidity Sensor Accuracy Graph for Maximum Rating)	
@ 25 °C (20 to 80 % RH)	±2 % RH
@ 5 °C to 45 °C (0 to 100 % RH)	
Hysteresis @ 5 °C to 45 °C (0 to 100 % RH)	± 1 % RH typical
Response Time	
Reach (τ 63 % @ 25 °C, wind velocity @ 1.0 m/s)	1 second
Unless otherwise specified: $V_{CC}$ = 1.62 to 5.5 V, $V_{SS}$ = 0 V, $T_a$ = -30 °C to 100 °C	
Temperature Detection	
Measurement Range	-30 °C to +100 °C
Resolution (11 bit)	
-30 °C to +100 °C	0.1 °C
Temperature Accuracy	
<sup>'</sup> @ 5 °C to 60 °C	
@ -20 °C to 85 °C	
Reproducibility @ -30 °C to 100 °C	± 0.1 °C
Response Time	60
Reach ( $\tau$ 63 % (dependent on surrounding heat conduction NOTE 1)	

# Current Consumption

NOTE 1 Extended exposure to >90 % RH causes a shift of up to 3 % RH which is reversible after a period of 14 days.

Unless otherwise specified:  $V_{CC}$  = 1.62 to 5.5 V,  $V_{SS}$  = 0 V,  $T_a$  = 0 °C to 60 °C

Unless otherwise specified:  $V_{CC}$  = 1.62 to 5.5 V,  $V_{SS}$  = 0 V,  $T_a$  = -30 °C to 100 °C



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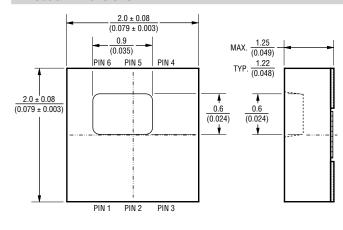
#### **Input/Output Terminal Characteristics**

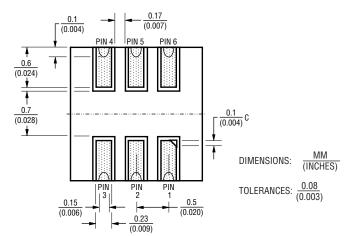
Unless otherwise specified:  $V_{CC}$  = 1.62 to 5.5 V,  $V_{SS}$  = 0 V,  $T_a$  = -30 °C to 100 °C

#### **Product Characteristics**

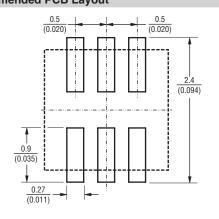
Moisture Sensitivity Level	1
ESD Classification (HBM)	1 kV
Marking	<b>∄</b> , Date Code
Standard Packaging	
Weight	10.45 milligrams

#### **Product Dimensions**





### Recommended PCB Layout



#### **Terminal Assignment**

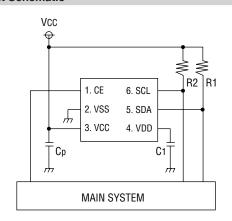
No.	Terminal Name	Function
1	CE	Chip enable terminal
2	$V_{SS}$	Power supply terminal (-)
3	$V_{CC}$	Power supply terminal (+)
4	$V_{DD}$	Internal constant voltage output terminal
5	SDA	I <sup>2</sup> C serial data
6	SCL	I <sup>2</sup> C serial clock

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#### **Basic Circuit Schematic**



C<sub>p</sub> ..... 0.1 μF C1 ..... 0.47 μF R1-R2 ......  $5k \Omega$ 

NOTE: R1 and R2 are reference values. Resistor values should be selected to meet the AC characteristics.

#### **Operation Mode**

Operation	Terminal Setup	Operation State of Each Functional Block						
Mode	CE	V <sub>pp</sub>	Power Supply	Oscillation	Temp. Detection	Capacitance Detection	OTP Memory	I <sup>2</sup> C-Bus
Sleep *1	0	NC	Stop	Stop	Stop	Stop	Stop	Stop
Standby	1	NC	Operation	Operation	Stop	Stop	Read-out Possible	Operation

<sup>\*1</sup> In case of power control mode, there is no sleep operation. I<sup>2</sup>C slave address (SADR) is defined as "111 1111" (7Fh).

#### **Control Register Map**

Address	Bit	Bit Name	Function	Value	ReadOut	Write-In	R/W	Init.		
	D7-1	-	Reserved	-			R	0		
00h	D0	RESET	ET Reset	0	Normal Operation	None	R/W	0		
				1	-	Reset Action				
	D7-6	MANMODE	Manual Detection Mode	00	Normal Operation Mode					
		HAVE(3:0)		value Avg.	000	No Averagi	ing Process	R/W	0	
	01h D5-3 HAVE[2				,	001	2 Times Av	erage Mode		
01h			5-5   TIAVE[2.0]		01x	4 Times Average Mode			ı	
				Mode	1xx	8 Times Av	erage Mode			
			Temperature	Temp	0	8 Times Av	erage Mode			
D2	1)2   IAVE	Detection Value Avg. Mode	1	16 Times Av	verage Mode	R/W	0			

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### **Control Register Map (Continued)**

Address	Bit	Bit Name	Function	Value	ReadOut	Write-In	R/W	Init.
	D1	-	Reserved	-			R	0
01h	DO MAN	MAN	Manual Detection	0	Standby State	Detection Operation Stop	B/W	0
		IVIAIN	Mode	1	Under Detection Operation	Detection Operation Start	H/VV	U
	D7-1	-	Reserved	-			R	0
03h	D0	FDD	Manual	0	No Error	Nothing is Done	DAM	
	D0	ERR	Detection Error Flag	1	Error Occurred	Error Flag Reset	R/W	0
04h	D7-0	HC[7:0]	Humidity Detection Result (After Correction Operation)		000h-3FFh		R	Х
	D7-2	-	Reserved	-			R	0
05h	D1-0	HC[9:8]	Humidity Detection Result (After Correction Operation)				R	Х
06h	D7-0	TC[7:0]	Temperature Detection Result (After Correction Operation)		000h-7FFh		R	Х
	D7-3	-	Reserved	-			R	0
07h	D2-0	TC[10:8]	Temperature Detection Result (After Correction Operation)				R	Х
0Ah	D7-0	K[7:0]	Capacity Detection Result (Before Correction Operation)		000h-FFFFh		R	0

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#### **Control Register Map (Continued)**

Address	Bit	Bit Name	Function	Value	ReadOut	Write-In	R/W	Init.
0Bh	D7-0	K[15:8]	Capacity Detection Result (Before Correction Operation)				R	0
	D7-5	-	Reserved	-	-	-	R	0
			Standard	0	Outside Cap	acity Cutting		
2Ch	D4	D4 SCR_ON_R	Capacity Connection Control	1	Outside Capacity Connection		R/W	0
	D3-0	SCI_ ON_R[3:0]	Internal Capacity Connection Control	0h~Fh x 0.6 pF Example: At the time of 8 hours, access to internal capacity of 4.8 pF		R/W	Х	
	D7-1	-	Reserved	-			R	0
03h	D0 ERR		Manual	0	No Error	Nothing is Done	5	
		Detection Error Flag	1	Error Occurred	Error Flag Reset	R/W	0	

#### **Transfer Function Formula**

#### Humidity

RH = 
$$\frac{100}{2^{10}}$$
 x RH<sub>IC</sub> (0 ~ 100 % RH)

RH<sub>IC</sub>: IC Humidity Output Data (10 bit)

Refer to Register Map:

 $RH_{IC}$  = Data of the addresses 04H and 05H (000h ~ 3FFh) It changes into a decimal and is operation.

#### **Temperature**

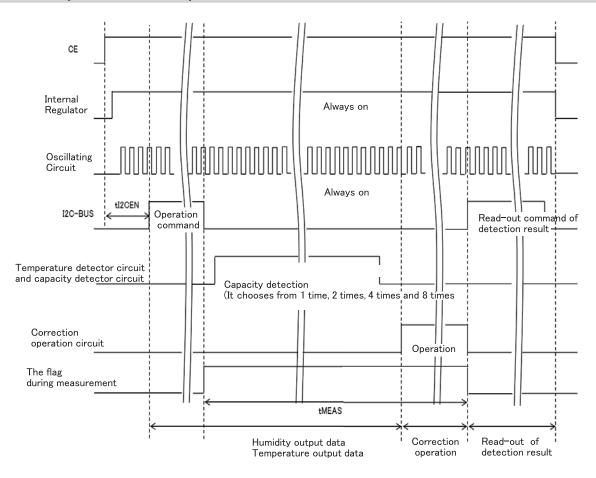
T = 
$$[T_{IC} - (2^{10} - \frac{25}{0.1})] \times 0.1$$
 (-30 ~ 100 °C)

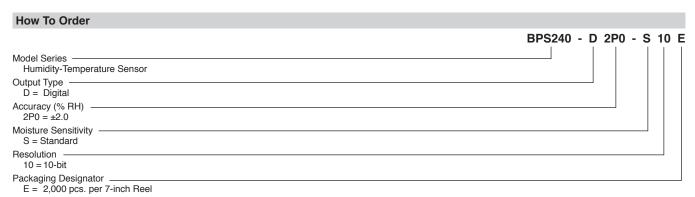
T<sub>IC</sub>: IC Temperature Output Data (11 bit)

Refer to Register Map:

T<sub>IC</sub> = Data of the addresses 06H and 07H (000h ~ 7FFh) It changes into a decimal and is operation.

#### Capacitance/Temperature Detection Sequence





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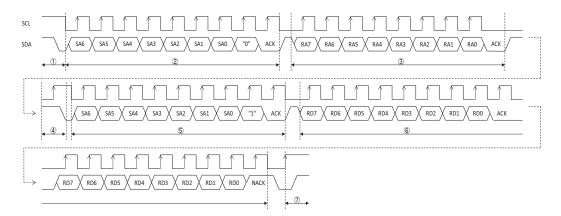
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#### **Output Type Waveform and Data Read/Write Procedure**

#### I<sup>2</sup>C-BUS Data Read-out Procedure

- (1) I<sup>2</sup>C master device releases START condition.
- 2) I<sup>2</sup>C master device transmits slave address and WRITE mode selection.
- (3) I<sup>2</sup>C master device transmits register address of this IC.
- (4) I<sup>2</sup>C master device releases repeated START condition. (Release method is same as START condition.)
- (5) I<sup>2</sup>C master device again transmits slave address and READ mode selection. (Read mode can be selected by transmitting "1" in 8th bit.)
- 6 I<sup>2</sup>C master device reads-out data from register address designated at ③.
  It is possible to read-out data while register address increments one, by reading-out multiple data continuously. However, during continuous read-out, please return ACK to this IC as a reply of master.
- (7) After the completion of all read-out, I<sup>2</sup>C master device releases STOP condition.



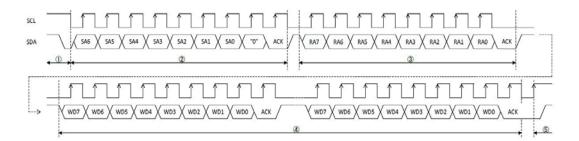
~ Continued ~

#### **Output Type Waveform and Data Read/Write Procedure (Continued)**

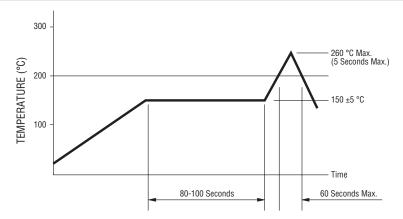
#### I<sup>2</sup>C-BUS Data Write-in Procedure

- (1) I<sup>2</sup>C master device releases START condition. (Start condition can be released by changing SDA from "H" to "L" while SCL is in "H" state.)
- (2) I<sup>2</sup>C master device transmits slave address and WRITE mode selection. (Write mode can be selected by transmitting "0" in 8th bit while 1~7th bits are slave address.)
- (3) I<sup>2</sup>C master device transmits register address of this IC.
- (4) I<sup>2</sup>C master device transmits write-in data.
- (5) It is possible to write-in data while register address increments one, by transmitting multiple write-in data continuously.

After the completion of transmitting all write-in data, I<sup>2</sup>C master device releases stop condition. (Stop condition can be released by changing SDA from "L" to "H" while SCL is in "H" state.)



#### Solder Profile



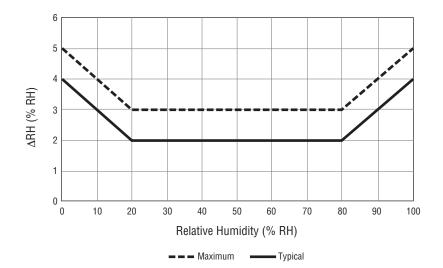
Processing Method: Reflow soldering with infrared heat or forced air convection (only once).

#### Notes:

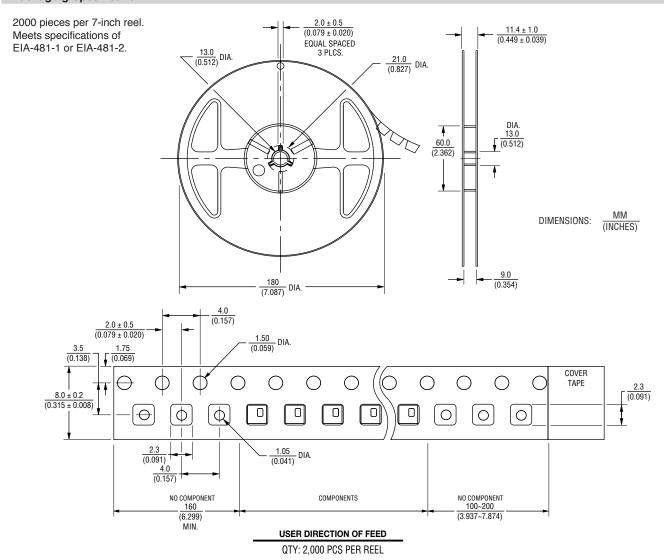
- 1. No clean solder paste is recommended.
- 2. Aqueous wash is not recommended.
- Use of water soluble soldering flux should be avoided due to possible corrosion.
- 4. Multiple passes through the soldering process is not recommended.
- Other SMD processes and profiles should be verified by the customer.

### **Humidity Sensor Accuracy**

Relative Humidity (% RH)	Maximum	Typical
0	5	4
10	4	3
20	3	2
30	3	2
40	3	2
50	3	2
60	3	2
70	3	2
80	3	2
90	4	3
100	5	4



#### **Packaging Specification**



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