### WS3A020065K Silicon Carbide Schottky Diode

#### **Features**

- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage •
- Positive Temperature Coefficient on V<sub>F</sub> •
- Temperature-independent Switching •
- 175°C Operating Junction Temperature

#### **Benefits**

- Replace Bipolar with Unipolar Device •
- Reduction of Heat Sink Size
- Parallel Devices Without Thermal Runaway •
- Essentially No Switching Losses

#### Applications

- Switch Mode Power Supplies
- Power Factor Correction
- Motor drive, PV Inverter, Wind Power Station

Maximum Ratings						
Symbol	Parameter	Value	Unit	Test Conditions	Note	
V <sub>RRM</sub>	Repetitive Peak Reverse Voltage	650	V	T <sub>C</sub> = 25°C		
$V_{\text{RSM}}$	Surge Peak Reverse Voltage	650	V	T <sub>C</sub> = 25°C		
$V_{R}$	DC Blocking Voltage	650	V	T <sub>C</sub> = 25°C		
l <sub>F</sub>	Forward Current (Per leg/Device)	27.5/55 13/26 10/20	A	T <sub>C</sub> ≤ 25°C T <sub>C</sub> ≤ 135°C T <sub>C</sub> ≤ 149°C		
IFSM	Non-Repetitive Forward Surge Current	90*	А	$T_{C}$ = 25 $^{\circ}C$ , $t_{p}$ = 8.3ms, Half Sine Wave		
P <sub>tot</sub>	Power Dissipation (Per leg/Device)	141/ 282	W	$T_{\rm C} = 25^{\circ}{\rm C}$	Fig.3	
Tc	Maximum Case Temperature	149	°C			
$T_{J},T_{STG}$	Operating Junction and Storage Temperature	-55 to 175	°C			
	TO-247 Mounting Torque	1	Nm	M3 Screw		

\*Per Leg, \*\*Per Device

Package



VRRM

Qc

I<sub>F</sub>(T<sub>C</sub>≤135°C) =

650

26

=

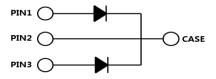
=

V

A\*\*

50 nC\*\*

TO-247-3



Part Number	Package	Marking	
WS3A020065K	TO-247-3	WS3A020065K	

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#### **Electrical Characteristics (Per Leg)**

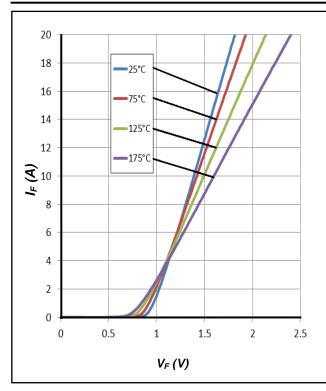
Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note	
V <sub>F</sub>	Forward Voltage	1.4	1.65	V	$I_{F} = 10A, T_{J} = 25^{\circ}C$	Fig 1	
		1.75	2.3		I <sub>F</sub> = 10A, T <sub>J</sub> = 175°C	Fig.1	
I <sub>R</sub>	Reverse Current	1	20	•	$V_{R} = 650V, T_{J} = 25^{\circ}C$		
		5	100	μA	$V_{R} = 650V, T_{J} = 175^{\circ}C$	Fig.2	
		618			$V_R = 0V, T_J = 25^{\circ}C, f = 1MHz$		
С	Total Capacitance	61	/	pF	$V_R = 200V, T_J = 25^{\circ}C, f = 1MHz$	Fig.5	
		51			$V_R = 400V, T_J = 25^{\circ}C, f = 1MHz$		
Qc	Total Capacitive Charge	25	,	nC	$V_{R} = 650V, I_{F} = 10A$	<b>F</b> : 4	
			/		di/dt = 200A/ $\mu$ s, T <sub>J</sub> = 25 $^{\circ}$ C	Fig.4	

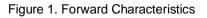
#### **Thermal Characteristics**

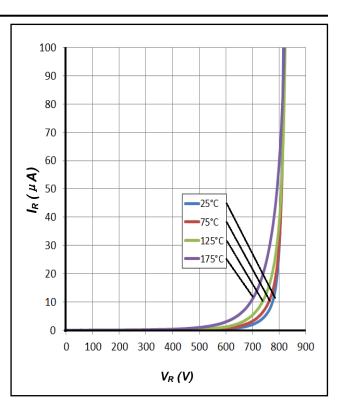
Symbol	Parameter	Тур.	Unit	Note
$R_{\theta JC}$	$R_{\theta JC}$ Thermal Resistance from Junction to Case		°C/W	Fig.6
R <sub>0JA</sub>	R <sub>0JA</sub> Thermal Resistance from Junction to Ambient		°C/W	
T <sub>sold</sub> Soldering Temperature		260	°C	

\*Per Leg, \*\*Per Device

### **Typical Performance (Per Leg)**







#### Figure 2. Reverse Characteristics

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### **Typical Performance (Per Leg)**

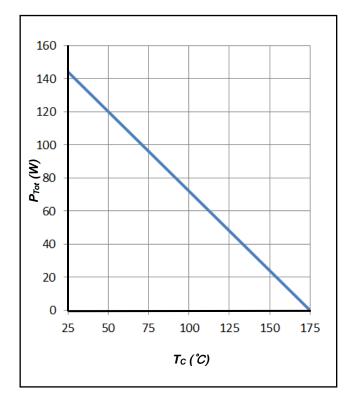
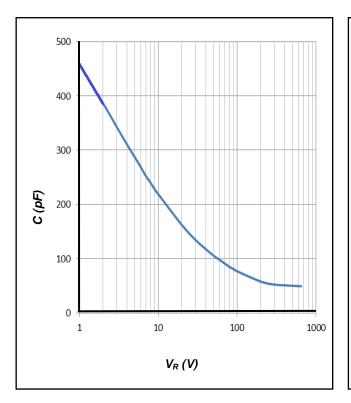
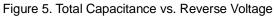


Figure 3. Power Derating





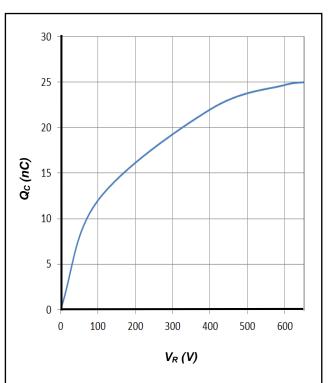
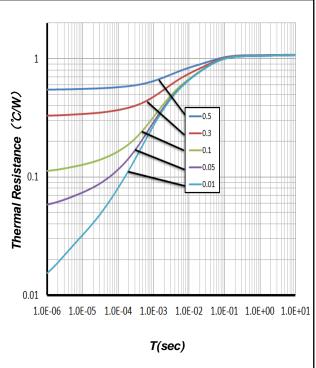
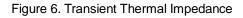


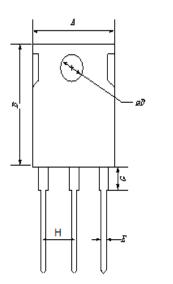
Figure 4. Total Capacitive Charge vs. Reverse Voltage

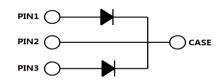




#### **Package Dimensions**

Package TO-247-3



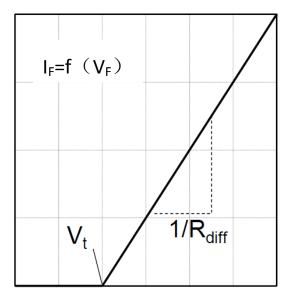


Symbol	Min. (mm)	Typ. (mm)	Max. (mm)
A	14.18	15.75	17.33
В	18.45	20.5	22.55
С	4.50	5.00	5.50
D	3.15	3.50	3.85
E	1.08	1.20	1.32
F	18.27	20.30	22.33
G	4.21	4.68	5.15
н	4.91	5.46	6.01

#### Simplified Diode Model (Per Leg)

#### Equivalent IV Curve for Model

R,



#### **Mathematical Equation**

 $V_F = V_t + I_F \times R_{diff}$ 

$$V_{t} = -0.0011 \times T_{j} + 0.98 \text{ [V]}$$
  
R<sub>diff</sub> = 7.7×10<sup>-7</sup>×T<sub>j</sub><sup>2</sup> + 8.9×10<sup>-5</sup>×T<sub>j</sub> + 0.039 [Ω]

Note:

$$\label{eq:time_time_time} \begin{split} Tj &= \text{Diode Junction Temperature In Degrees Celsius,} \\ valid from 25^{\circ}\text{C to } 175^{\circ}\text{C} \\ I_{\text{F}} &= \text{Forward Current} \\ \text{Less than } 20\text{A} \end{split}$$

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