

Features

- CRM(CQ) Super_Junction technology
- Much lower Ron*A performance for On-state efficiency
- Much lower FOM for fast switching efficiency

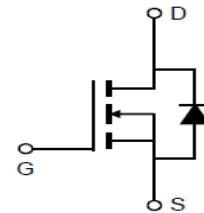
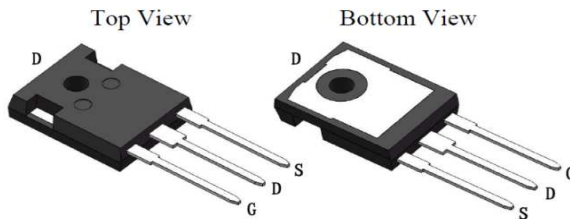
Applications

- LED/LCD/PDP TV and monitor Lighting
- Solar/Renewable/UPS-Micro Inverter System
- Charger
- Power Supply

Product Summary

V _{DS}	650V
R _{DS(on)_typ}	185mΩ
I _D	23A

100% Avalanche Tested


Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
CRJQ190N65GCF	-	TO-247-3L	Tube	N/A	N/A	30pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V _{DS}	650	V
Continuous drain current T _C = 25°C T _C = 100°C	I _D	23 14.8	A
Pulsed drain current (T _C = 25°C, t _p limited by T _{jmax})	I _{D pulse}	93	A
Avalanche energy, single pulse (L=60mH, R _g =30Ω)	E _{AS}	320	mJ
Gate-Source voltage	V _{GS}	±30	V
Power dissipation (T _C = 25°C)	P _{tot}	229	W
Operating junction and storage temperature	T _j , T _{stg}	-55...+150	°C

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	R_{thJC}	0.55	°C/W
Thermal resistance, junction – ambient. Max	R_{thJA}	45	

Electrical Characteristic (at $T_j = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	BV_{DSS}	650	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Gate threshold voltage	$V_{GS(th)}$	3		5	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Zero gate voltage drain current	I_{DSS}	-	-	5	μA	$V_{DS}=650V, V_{GS}=0V$ $T_C=25^\circ C$ $T_C=150^\circ C$
Gate-source leakage current	I_{GSS}	-		± 100	nA	$V_{GS}=\pm 30V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	185	210	mΩ	$V_{GS}=10V, I_D=10A,$ $T_C=25^\circ C$ $T_C=150^\circ C$
Transconductance	g_{fs}	-	14.5	-	S	$V_{DS}=20V, I_D=10A$

Dynamic Characteristic

Input Capacitance	C_{iss}	-	1427	-	pF	$V_{GS}=0V, V_{DS}=100V,$ $f=1MHz$
Output Capacitance	C_{oss}	-	67	-		
Reverse Transfer Capacitance	C_{rss}	-	21	-		
Gate Total Charge	Q_G	-	41	-	nC	$V_{GS}=10V, V_{DS}=480V,$ $I_D=10A, f=1MHz$
Gate-Source charge	Q_{gs}	-	12	-		
Gate-Drain charge	Q_{gd}	-	20	-		
Turn-on delay time	$t_{d(on)}$	-	42.5	-	ns	$T_j=25^\circ C, V_{GS}=10V,$ $I_D=10A, V_{DS}=400V,$ $R_g=25\Omega$
Rise time	t_r	-	58	-		
Turn-off delay time	$t_{d(off)}$	-	105	-		
Fall time	t_f	-	36	-		
Gate resistance	R_G	-	0.9	-	Ω	$V_{GS}=0V, V_{DS}=0V,$ $f=1MHz$

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	0.5	0.84	1	V	$V_{GS}=0V, I_{SD}=10A$
Body Diode Reverse Recovery Time	t_{rr}	-	108	-	ns	$I_{sd}=10A$ $dI/dt=100A/us, V_{ds}=100V$
Body Diode Reverse Recovery Charge	Q_{rr}	-	0.54	-	Uc	

Typical Performance Characteristics

Fig 1. Output Characteristics (Tj=25°C)

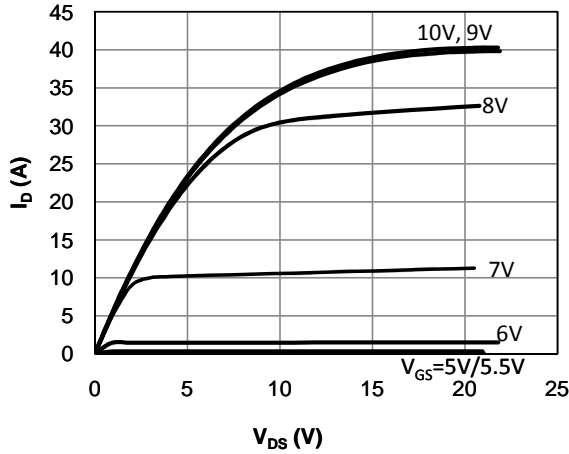


Fig 2. Output Characteristics (Tj=150°C)

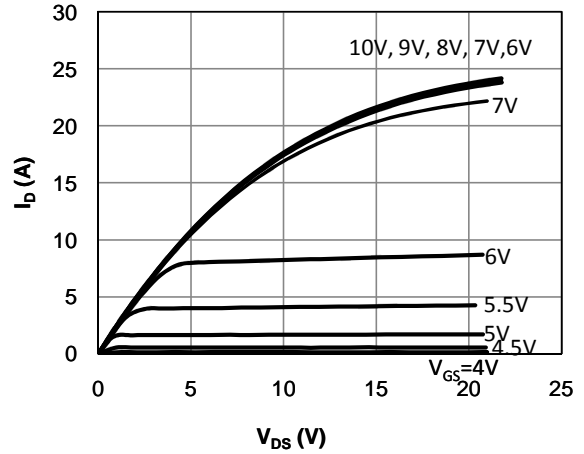


Fig 3: Transfer Characteristics

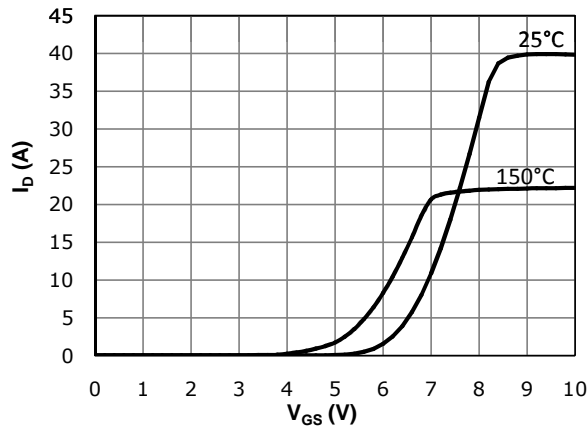


Fig 4: V_{TH} Vs T_j Temperature Characteristics

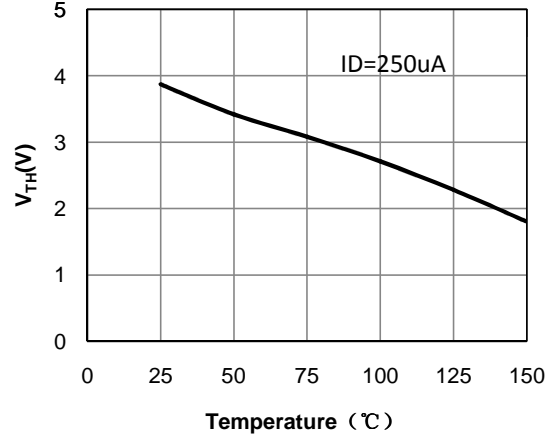


Fig 5: $R_{DS(on)}$ Vs I_{DS} Characteristics ($T_c = 25^\circ\text{C}$)

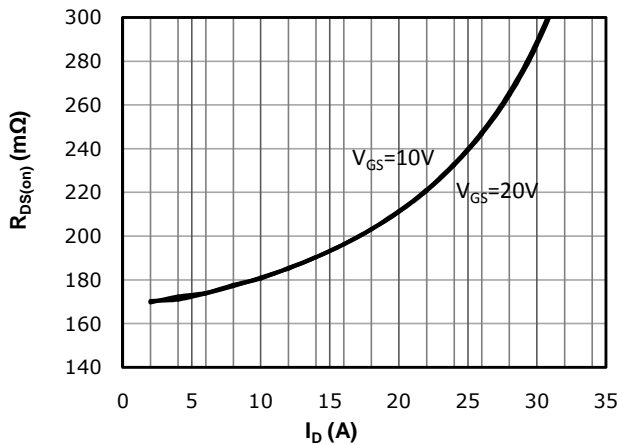


Fig 6: $R_{DS(on)}$ vs. Temperature

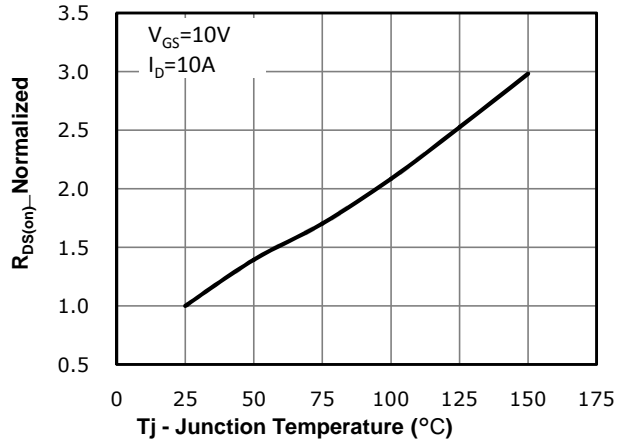


Fig 7: BVDSS vs. Temperature

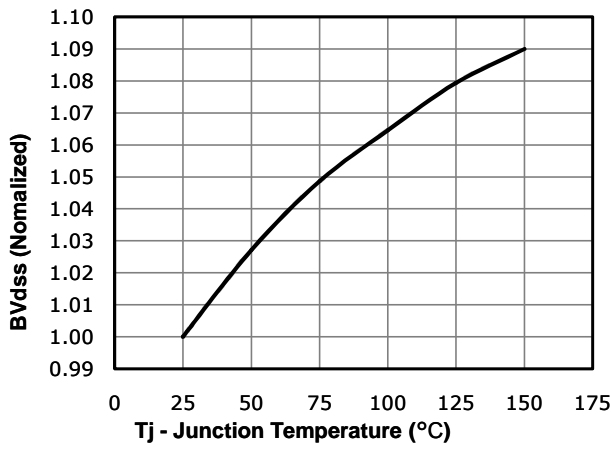


Fig 8: Rds(on) vs. Gate Voltage

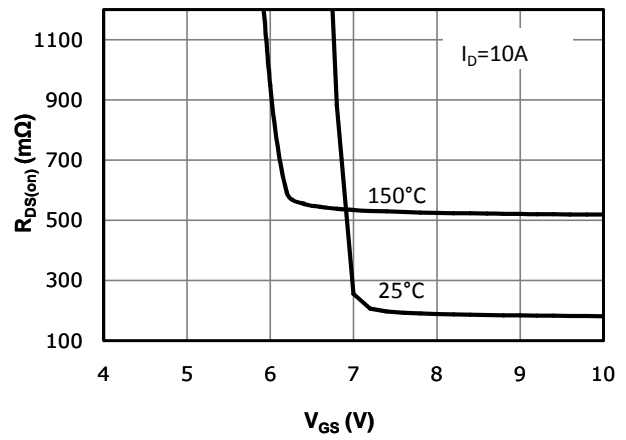


Fig 9: Body-diode Forward Characteristics

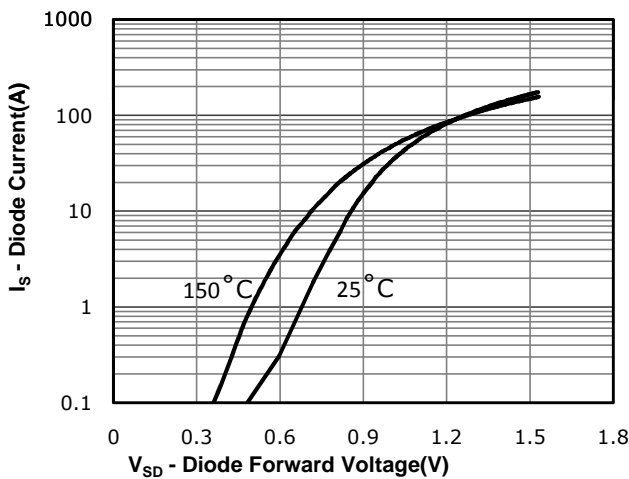


Fig 10: Gate Charge Characteristics

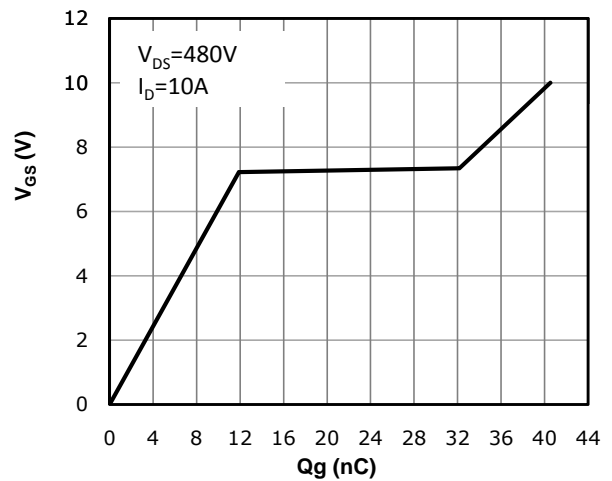


Fig 11: Capacitance Characteristics

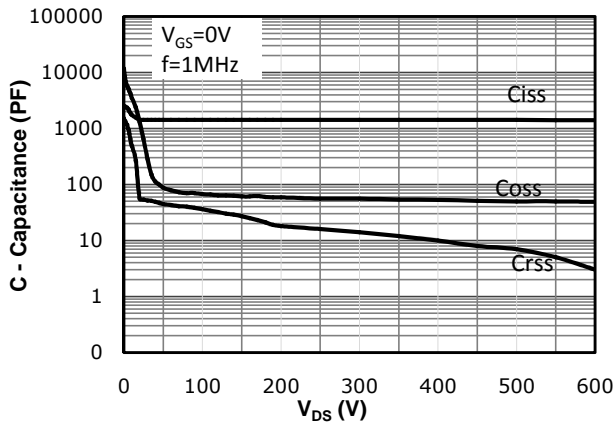
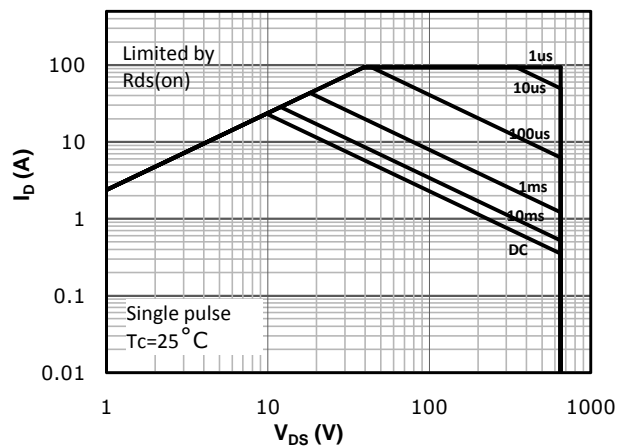
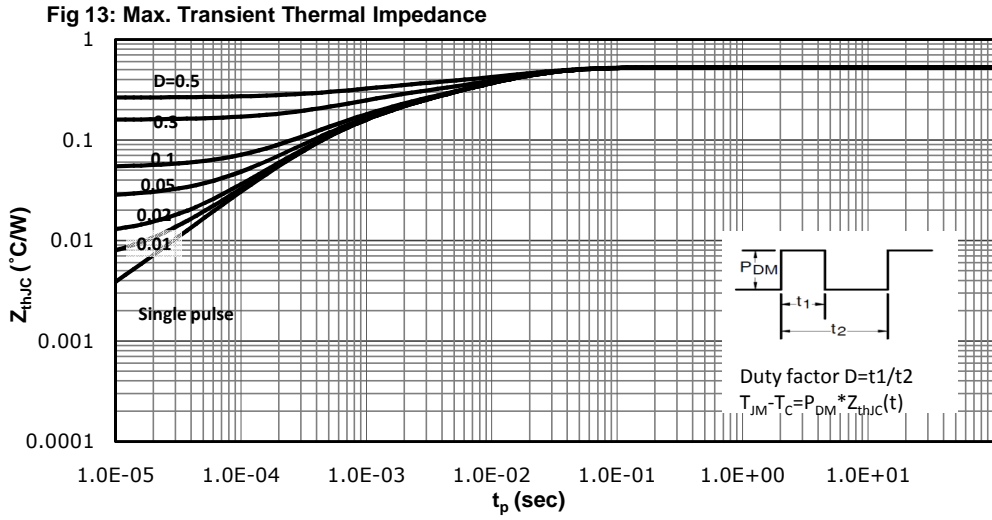


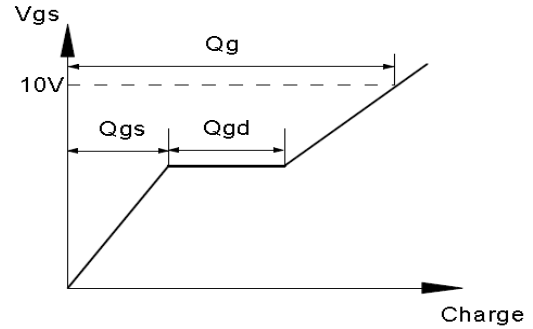
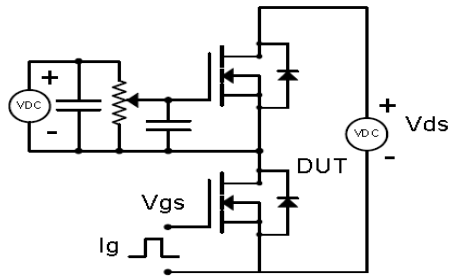
Fig 12: Safe Operating Area



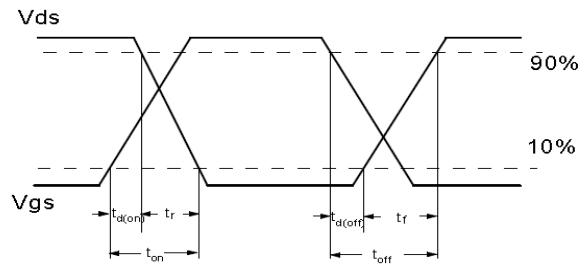
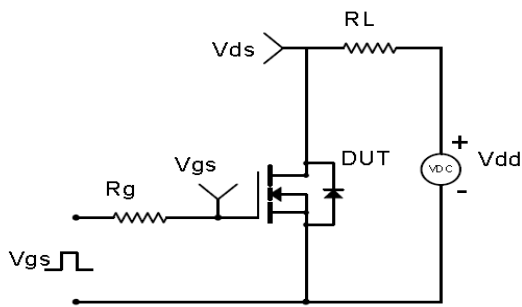


Test Circuit & Waveform

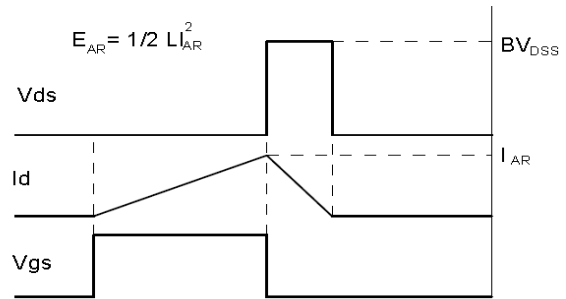
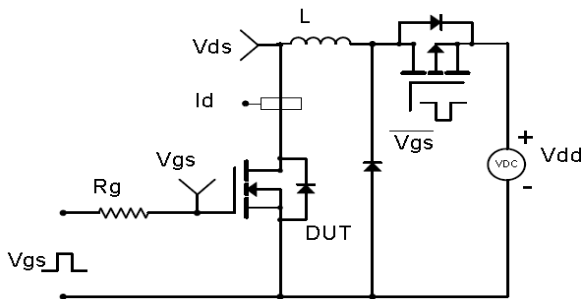
Gate Charge Test Circuit & Waveform



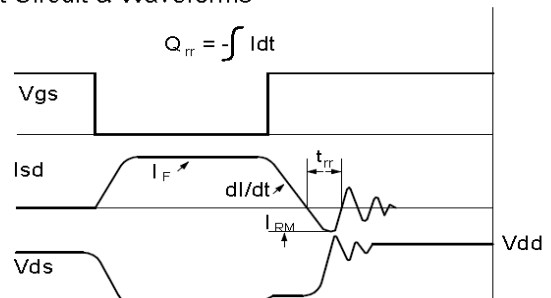
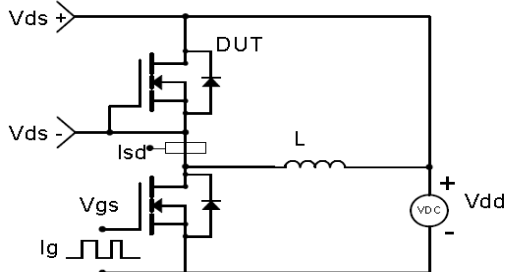
Resistive Switching Test Circuit & Waveforms



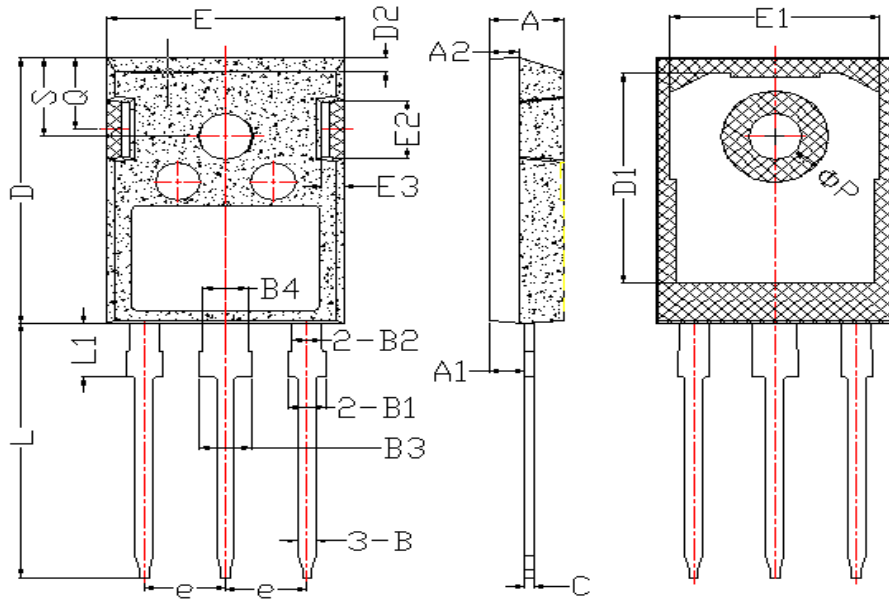
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outline: TO-247-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.60	5.20	0.181	0.205
A1	2.20	2.60	0.087	0.102
A2	1.80	2.20	0.071	0.087
B	0.90	1.40	0.035	0.055
B1	1.75	2.35	0.069	0.093
B2	1.75	2.15	0.069	0.085
B3	2.80	3.35	0.110	0.132
B4	2.80	3.15	0.110	0.124
C	0.50	0.70	0.020	0.028
D	20.60	21.30	0.811	0.839
D1	16.00	18.00	0.630	0.709
D2	0.90	1.40	0.035	0.055
E	15.50	16.10	0.610	0.634
E1	13.00	14.70	0.512	0.579
E2	3.80	5.30	0.150	0.209
E3	0.80	2.60	0.031	0.102
e	5.20	5.70	0.205	0.224
L	19.00	20.50	0.748	0.807
L1	3.90	4.60	0.154	0.181
ΦP	3.30	3.70	0.130	0.146
Q	5.20	6.00	0.205	0.236
S	5.80	6.60	0.228	0.260

Revision History

Revision	Date	Major changes
1.0	2020-8-5	Release of first version

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qualified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM(CQ) reserves the right to improve product design, function and reliability without notice.

单击下面可查看定价，库存，交付和生命周期等信息

[>>CRMICRO\(华润微\)](#)