



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

в	V _{DSS}	R _{DS(ON)} Max T _A = +25°0				
:	20V	$25m\Omega @ V_{GS} = 4.5V$	6.5A			

Features and Benefits

- Low On-Resistance
- Low-Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMG6968UQ is suitable for automotive applications requiring specific change control and is AEC-Q101 qualified, is PPAP capable, and is manufactured in IATF16949:2016 certified facilities.

Description and Applications

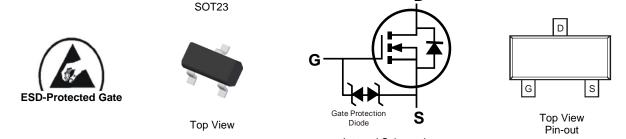
This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Power-Management Functions
- DC-DC Converters
- Motor Control

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ©3
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)

П



Ordering Information (Note 4)

	-		
	Part Number	Case	Packaging
	DMG6968UQ-7	SOT23	3000/Tape & Reel
Notes:	1. No purposely added lead. Fully EU Direct	tive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/	863/EU (RoHS 3) compliant.

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

 Lead-free.
 Lead-free.
 Constraints of Malogen- and Antimony-free, "Green "and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

21	N4	ΥM	

2N4 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

2010 0000 110												
Year	2019		2020	2021		2022	2023		2024	2025		2026
Code	G		Н	I		J	K		L	М		Ν
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Internal Schematic



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characte	eristic		Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 5)Steady State $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		I _D	6.5 5.2	A	
Pulsed Drain Current			I _{DM}	30	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.3	W
Thermal Resistance, Junction to Ambient @ $T_A = +25^{\circ}C$	R _{0JA}	157	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	O°

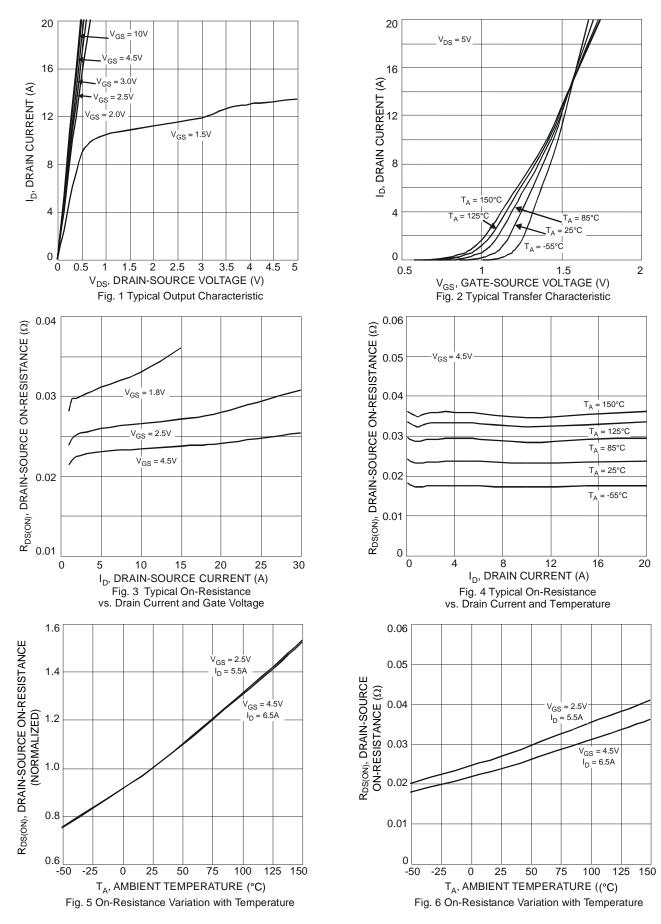
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)						•	•	
Drain-Source Breakdown Voltage		BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	$T_J = +25^{\circ}C$	I _{DSS}	-	_	1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage		IGSS	-	_	±10	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$	
Gate-Source Breakdown Voltage		BV _{GSS}	±12	_		V	$V_{DS} = 0V, I_G = \pm 250 \mu A$	
ON CHARACTERISTICS (Note 6)							÷	
Gate Threshold Voltage		V _{GS(TH)}	0.5	—	0.9	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
				21	25		V _{GS} = 4.5V, I _D = 6.5A	
Static Drain-Source On-Resistance	1	R _{DS(ON)}	—	23	29	mΩ	V _{GS} = 2.5V, I _D = 5.5A	
				28	36		V _{GS} = 1.8V, I _D = 3.5A	
Forward Transfer Admittance		Y _{fs}		8	_	S	$V_{DS} = 10V, I_D = 5A$	
DYNAMIC CHARACTERISTICS (Note 7)							•	
Input Capacitance		Ciss	_	151		pF		
Output Capacitance		Coss		91		pF	$V_{DS} = 10V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance		Crss		32	_	pF		
Total Gate Charge		Qg		8.5	_	nC		
Gate-Source Charge		Q _{gs}		1.6	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_D = 6.5A$	
Gate-Drain Charge		Q _{gd}		2.8	_	nC		
Turn-On Delay Time		t _{D(ON)}	_	54	—	ns		
Turn-On Rise Time		t _R	_	66	—	ns	V _{DD} = 10V, V _{GS} = 4.5V,	
Turn-Off Delay Time		t _{D(OFF)}		613		ns	$R_L = 10\Omega, R_G = 6\Omega, I_D = 1A$	
Turn-Off Fall Time		tF	_	205	_	ns]	

 Device mounted on 1" × 1" FR-4 PCB with high-coverage 2oz copper, single sided.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:

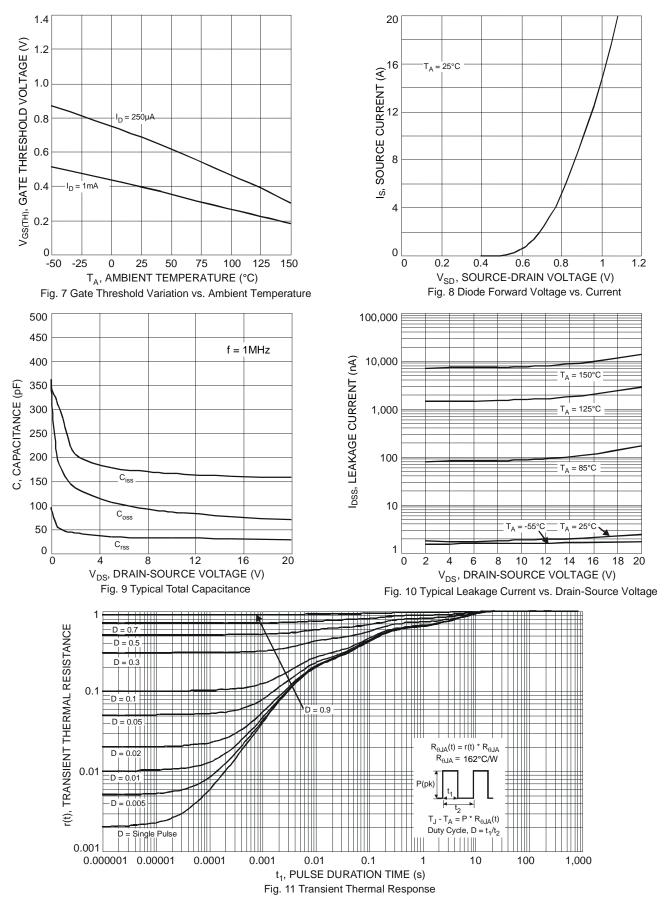


DMG6968UQ





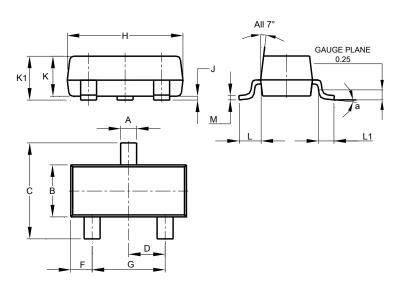
DMG6968UQ





Package Outline Dimensions

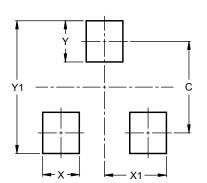
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT23								
Dim	Min	Max	Тур						
Α	0.37	0.51	0.40						
В	1.20	1.40	1.30						
С	2.30	2.50	2.40						
D	0.89	1.03	0.915						
F	0.45	0.60	0.535						
G	1.78	2.05	1.83						
н	2.80	3.00	2.90						
J	0.013	0.10	0.05						
K	0.890	1.00	0.975						
K1	0.903	1.10	1.025						
L	0.45	0.61	0.55						
L1	0.25	0.55	0.40						
М	0.085	0.150	0.110						
а	0°	8°							
All	All Dimensions in mm								

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9

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