



60V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	Package	I _{D max} T _A = +25°C	
60V	$38m\Omega$ @ $V_{GS} = 10V$	U-DFN2020-6	6.5A	
607	47mΩ @ V _{GS} = 4.5V	Type E	5.2A	

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

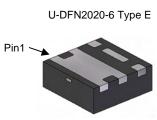
- General Purpose Interfacing Switch
- Power Management Functions

Features and Benefits

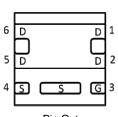
- 100% Unclamped Inductive Switch (UIS) test in production
- 0.6mm profile ideal for low profile applications
- PCB footprint of 4mm²
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

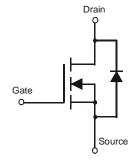
- Case: U-DFN2020-6 Type E
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.0065 grams (approximate)







Pin Out Bottom View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Marking	Reel size (inches)	Quantity per reel	
DMN6040SFDE-7	N8	7	3,000	
DMN6040SFDE-13	N8	13	10,000	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



N8 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011) M = Month (ex: 9 = September)

Date Code Key

Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Υ		Z		Α	E	3	С		D		E
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

DMN6040SFDE
Datasheet number: DS35792 Rev. 8 - 2



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	60	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note C) / 40/	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	lD	5.3 4.1	А
Continuous Drain Current (Note 6) V _{GS} = 10V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		I _D	6.5 5.1	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)		I _{DM}	30	Α	
Maximum Body Diode Continuous Current	Is	2.5	Α		
Avalanche Current (Note 7) L = 0.1mH	I _{AR}	14.2	Α		
Avalanche Energy (Note 7) L = 0.1mH	E _{AR}	10	mJ		

Thermal Characteristics

Characteristic	Symbol	Value	Units		
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	C	0.66	w	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	P_{D}	0.42	۷V	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	D	189	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{ heta JA}$	132	C/VV	
Total Power Dissipation (Note 6)	$T_A = +25$ °C	Pn	2.03	W	
Total Fower Dissipation (Note o)	$T_A = +70^{\circ}C$	FD	1.31		
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	-	61		
Themal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ heta JA}$	43	°C/W	
Thermal Resistance, Junction to Case (Note 6)	$R_{ heta JC}$	9.3			
Operating and Storage Temperature Range		$T_{J_{i}}T_{STG}$	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	100	nA	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(th)}$	1		3	٧	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	D		30	38	mΩ	$V_{GS} = 10V, I_D = 4.3A$
Static Dialii-Source Oil-Resistance	R _{DS} (ON)		35	47	1112.2	$V_{GS} = 4.5V, I_D = 4A$
Forward Transfer Admittance	Y _{fs}		4.5	_	S	$V_{DS} = 10V, I_D = 4.3A$
Diode Forward Voltage	V_{SD}		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}		1287	_		V 05V V 0V
Output Capacitance	Coss	_	57	_	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C_{rss}	_	44	_		1 = 1.0IVII IZ
Gate Resistance	Rg	_	1.2	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = 10V)	Q_g	_	22.4	_		
Total Gate Charge (V _{GS} = 4.5V)	Q_g	_	10.4	_	nC	
Gate-Source Charge	Q_{gs}	_	4.9	_	nc	$V_{DS} = 30V, I_{D} = 4.3A$
Gate-Drain Charge	Q_{gd}	_	3.0	_		
Turn-On Delay Time	t _{D(on)}	_	6.6	_		
Turn-On Rise Time	t _r	_	8.1	_	nS	$V_{GS} = 10V, V_{DD} = 30V, R_G = 6\Omega,$
Turn-Off Delay Time	t _{D(off)}	_	20.1	_		$I_D = 4.3A$
Turn-Off Fall Time	t _f	_	4.0	_		
Body Diode Reverse Recovery Time	t _{rr}		18	_	nS	I _S = 4.3A, dl/dt = 100A/μs
Body Diode Reverse Recovery Charge	Qrr		11.9	_	nC	$I_S = 4.3A$, $dI/dt = 100A/\mu s$

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

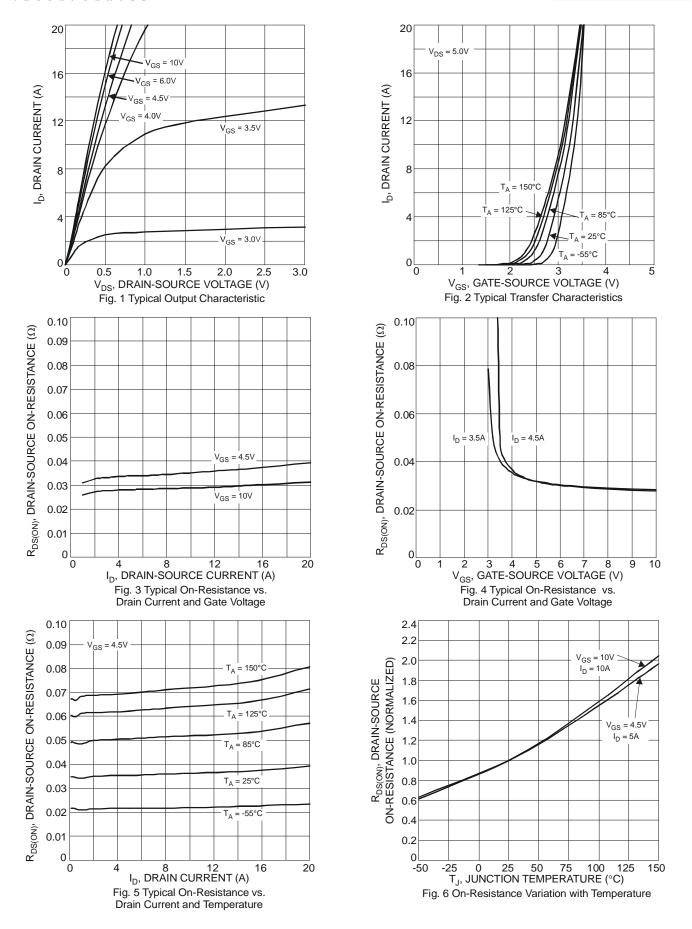
7. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep $T_J = +25$ °C.

8. Short duration pulse test used to minimize self-heating effect.

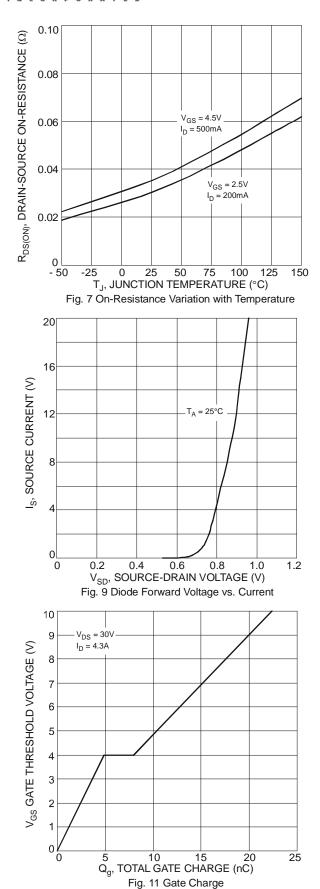
9. Guaranteed by design. Not subject to product testing.

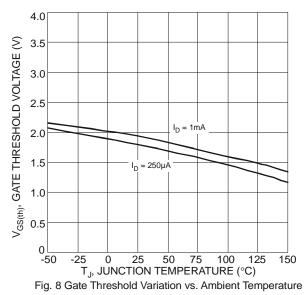
2 of 6 DMN6040SFDE August 2012 © Diodes Incorporated Datasheet number: DS35792 Rev. 8 - 2

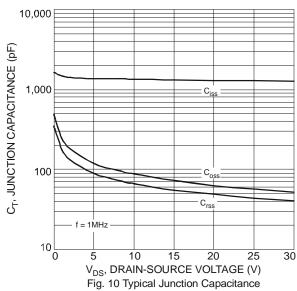


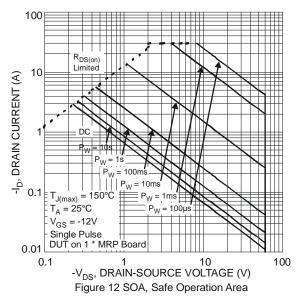




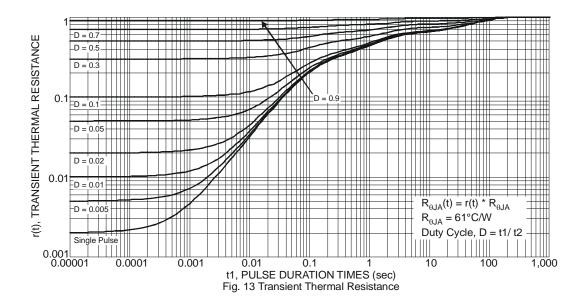






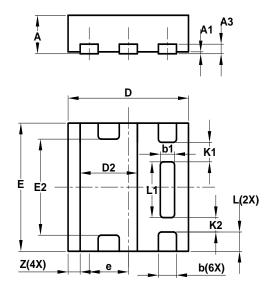






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

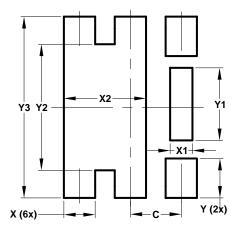


U-DFN2020-6 Type E							
Dim	Min	Max	Тур				
Α	0.57	0.63	0.60				
A1	0	0.05	0.03				
A3	_	_	0.15				
b	0.25	0.35	0.30				
b1	0.185	0.285	0.235				
D	1.95	2.05	2.00				
D2	0.85	1.05	0.95				
Е	1.95	2.05	2.00				
E2	1.40	1.60	1.50				
е	_	_	0.65				
L	0.25	0.35	0.30				
L1	0.82	0.92	0.87				
K1	_	_	0.305				
K2	_	_	0.225				
Z	_	_	0.20				
All	All Dimensions in mm						



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.650
Х	0.400
X1	0.285
X2	1.050
Υ	0.500
Y1	0.920
Y2	1.600
Y3	2.300

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DMN6040SFDE 6 of 6 Datasheet number: DS35792 Rev. 8 - 2

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