



DMTH4005SPS

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> T <sub>C</sub> = +25°C (Note 9)	
40V	$3.7 \text{m}\Omega @ \text{V}_{\text{GS}} = 10 \text{V}$	100A	

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>), yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- **Engine Management Systems**
- **Body Control Electronics**
- **DC-DC Converters**

## 40V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

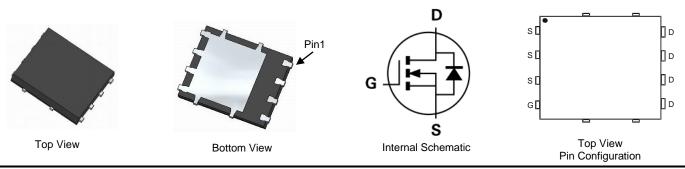
## **Features**

- Rated to +175°C Ideal For High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable And Robust End Application
- Low RDS(ON) Minimizes Power Losses
- Low Q<sub>a</sub> Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMTH4005SPSQ)

## **Mechanical Data**

- Case: PowerDI<sup>®</sup>5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)

#### PowerDI5060-8



## Ordering Information (Note 4)

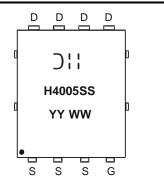
	Part Number	Case	Packaging		
DMTH4005SPS-13		PowerDI5060-8	2,500 / Tape & Reel		
Notes:	Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.				

EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimonv compounds.

For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## Marking Information



⊃¦¦ = Manufacturer's Marking H4005SS = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 17 = 2017) WW = Week (01 to 53)

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# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	40	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 5)	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	20.9 17.5	A
Continuous Drain Current (Notes 6 & 9)	T <sub>C</sub> = +25°C T <sub>C</sub> = +100°C	Ι <sub>D</sub>	100 100	A
Maximum Continuous Body Diode Forward Current (Note 6)		I <sub>S</sub>	100	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	320	A
Avalanche Current, L=0.6mH		I <sub>AS</sub>	21	A
Avalanche Energy, L=0.6mH		E <sub>AS</sub>	132.3	mJ

# **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	2.6	W
Thermal Resistance, Junction to Ambient (Note 5)		R <sub>0JA</sub>	57	°C/W
Total Power Dissipation (Note 6)	T <sub>C</sub> = +25°C	PD	150	W
Thermal Resistance, Junction to Case (Note 6)		R <sub>eJC</sub>	1	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +175	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

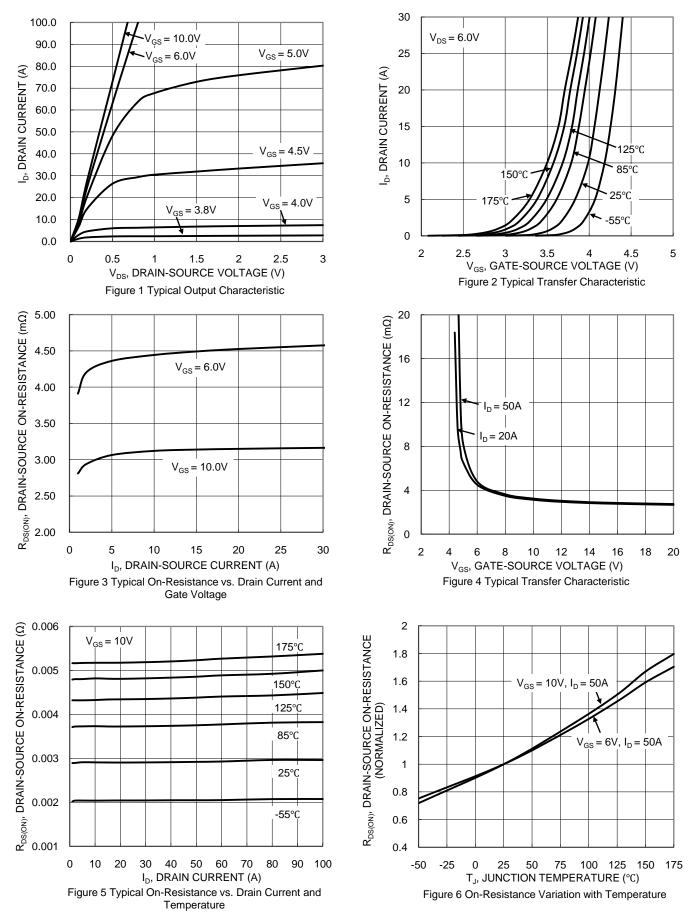
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	40			V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1	$\mu A \qquad V_{DS} = 32V, V_{GS} = 0V$		
Gate-Source Leakage	I <sub>GSS</sub>	_		±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2		4	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		2.9	3.7	mΩ	$V_{GS} = 10V, I_D = 50A$	
Diode Forward Voltage	V <sub>SD</sub>		0.88		V	$V_{GS} = 0V, I_{S} = 50A$	
DYNAMIC CHARACTERISTICS (Note 8)						·	
Input Capacitance	C <sub>iss</sub>		3062			$V_{DS} = 20V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	C <sub>oss</sub>		902.2		pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	179.2				
Gate Resistance	R <sub>g</sub>		0.67		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg		49.1			$V_{DD}$ = 20V, $I_D$ = 50A, $V_{GS}$ = 10V	
Gate-Source Charge	Q <sub>gs</sub>	_	10.3		nC		
Gate-Drain Charge	Q <sub>gd</sub>	_	13				
Turn-On Delay Time	t <sub>D(ON)</sub>		8.7			$V_{DD} = 20V, V_{GS} = 10V, \label{eq:VDD} I_D = 50A, R_G = 3\Omega$	
Turn-On Rise Time	t <sub>R</sub>		6.8		ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>		18.6				
Turn-Off Fall Time	t <sub>F</sub>		7.3				
Body Diode Reverse Recovery Time	t <sub>RR</sub>	I	31.8	—	ns	L = 500 di/dt = 1000/up	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	26.5	_	nC	I <sub>F</sub> = 50A, di/dt = 100A/μs	

Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
Thermal resistance from junction to soldering point (on the exposed drain pad).
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.
Package limited.

Notes:

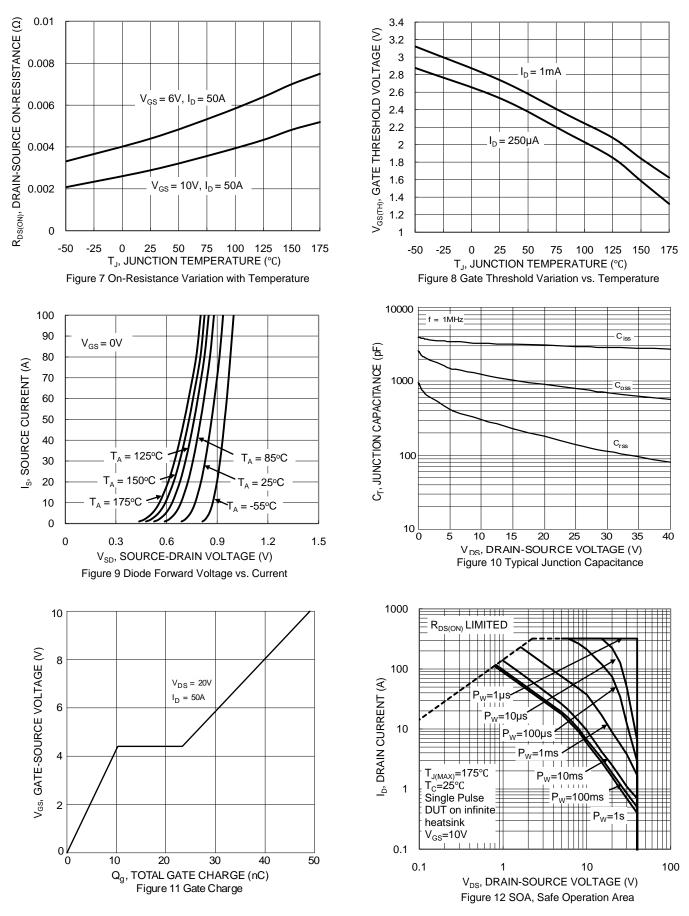


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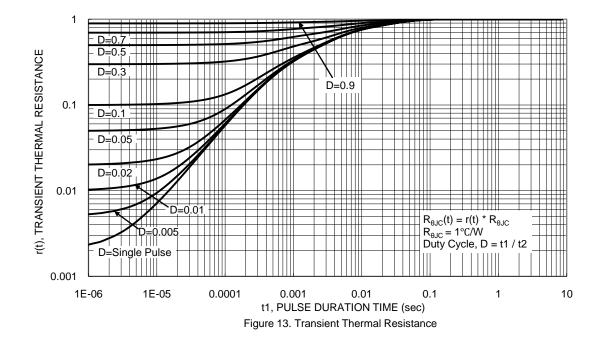




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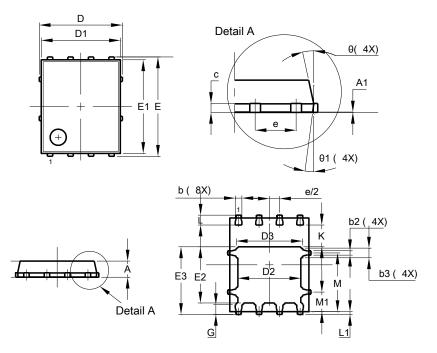




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8

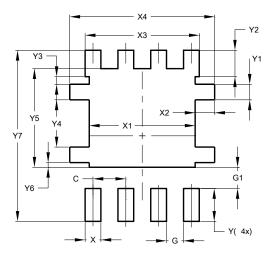


PowerDI5060-8							
			Тур				
Α	0.90	1.10	1.00				
A1	0.00	0.05	-				
b	0.33	0.51	0.41				
b2	0.200	0.350	0.273				
b3	0.40	0.80	0.60				
c	0.230	0.330	0.277				
D	ļ	5.15 BSC					
D1	4.70	5.10	4.90				
D2	3.70	4.10	3.90				
D3	3.90	4.30	4.10				
Е	(	6.15 BSC					
E1	5.60	6.00	5.80				
E2	3.28	3.68	3.48				
E3	3.99	4.39	4.19				
е	1.27 BSC						
G	0.51	0.71	0.61				
K	0.51	-	-				
L	0.51	0.71	0.61				
L1	0.100	0.200	0.175				
М	3.235	4.035 3.63					
M1	1.00	1.40	1.21				
θ	10º	12º	11º				
θ1	6°	8º	7°				
All Dimensions in mm							

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8



Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
Х	0.610			
X1	4.100			
X2	0.755			
X3	4.420			
X4	5.610			
Y	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
Y7	6.610			



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