



DMG6968U

N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
 - $25m\Omega @ V_{GS} = 4.5V$
 - 29mΩ @ V_{GS} = 2.5V
 - 36mΩ @ V_{GS} = 1.8V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
 - https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMG6968UQ)

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 3

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Top View

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- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)

ESD PROTECTED TO 2kV

Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
DMG6968U-7	Standard	SOT23	3000/Tape & Reel

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Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

Top View

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

Protectior Diode

Internal Schematic

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

2N4	¥

2N4 = Product Type Marking Code YM = Date Code Marking

- Y or \overline{Y} = Year (ex: G = 2019)
- M = Month (ex: 9 = September)

Year	2009		2019	2020	2021	2022	2023	2024	2025	2026	2027	2028
Code	W		G	Н	Ι	J	K	L	М	N	0	Р
Month	Jan	Feb	Mar	Apr	Mav	Jun	Jul	Aug	Sep	Oct	Nov	Dec
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Code	1	2	3	4	5	6	7	8	9	0	N	D

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Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characte	eristic		Symbol	Value	Unit
Drain-Source Voltage			Vdss	20	V
Gate-Source Voltage		V _{GSS}	±12	V	
Continuous Drain Current (Note 5)	T _A = +25°C T _A = +70°C	ID	6.5 5.2	А	
Pulsed Drain Current			Ідм	30	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.3	W
Thermal Resistance, Junction to Ambient @ T _A = +25°C	Reja	157	°C/W
Operating and Storage Temperature Range	Тј, Тѕтс	-55 to +150	0°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

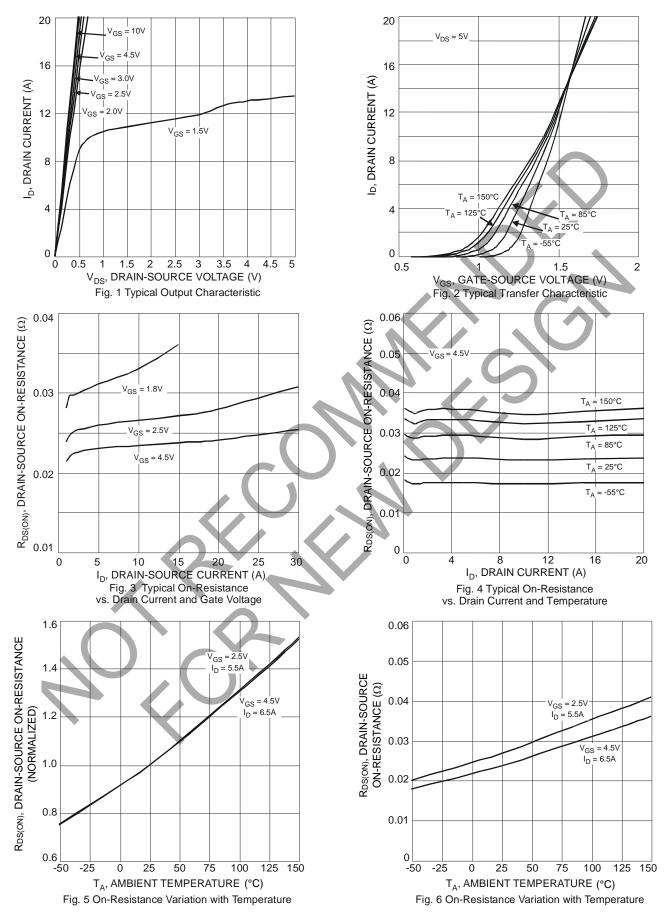
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 6)								
Drain-Source Breakdown Voltage	BVDSS	20			V	$V_{GS} = 0V, I_{D} = 250 \mu A$		
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	IDSS	T		1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$		
Gate-Source Leakage	lgss			±10	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$		
Gate-Source Breakdown Voltage	BVsgs	±12	-		V	$V_{DS} = 0V$, $I_G = \pm 250 \mu A$		
ON CHARACTERISTICS (Note 6)								
Gate Threshold Voltage	VGS(TH)	0.5		0.9	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$		
			21	25		$V_{GS} = 4.5V, I_D = 6.5A$		
Static Drain-Source On-Resistance	R _{DS(ON)}	_	23	29	mΩ	$V_{GS} = 2.5V, I_D = 5.5A$		
			28	36		$V_{GS} = 1.8V, I_D = 3.5A$		
Forward Transfer Admittance	Y _{fs}		8	_	S	VDS = 10V, ID = 5A		
DYNAMIC CHARACTERISTICS (Note 7)								
Input Capacitance	Ciss	_	151	_	pF			
Output Capacitance	Coss	_	91	_	pF	Vps = 10V, Vgs = 0V f = 1.0MHz		
Reverse Transfer Capacitance	Crss	_	32	_	pF			
Total Gate Charge	Qg		8.5		nC			
Gate-Source Charge	Qgs	_	1.6	_	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 6.5A		
Gate-Drain Charge	Q _{gd}	_	2.8	_	nC	1		
Turn-On Delay Time	tD(ON)		54		ns			
Turn-On Rise Time	t _R		66		ns	V _{DD} = 10V, V _{GS} = 4.5V,		
Turn-Off Delay Time	tD(OFF)	_	613		ns	$R_L = 10\Omega$, $R_G = 6\Omega$, $I_D = 1A$		
Turn-Off Fall Time	tF	_	205	_	ns			

5. Device mounted on FR-4 substrate PC board, 2oz. copper, with thermal vias to bottom layer 1 inch square copper plate. 6 Short duration pulse test used to minimize self-heating effect. Notes:

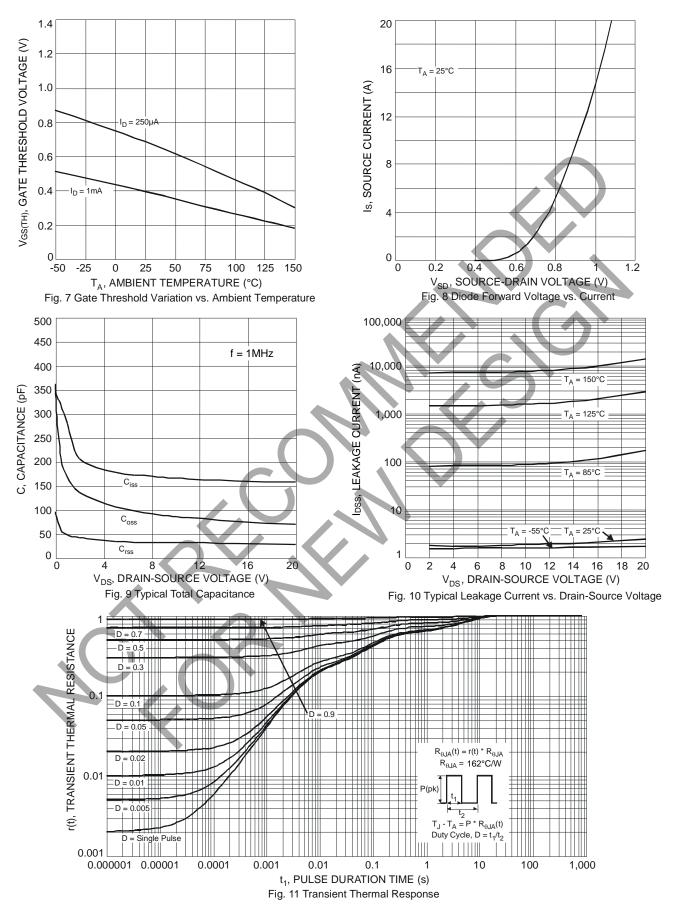
7. Guaranteed by design. Not subject to production testing.



DMG6968U



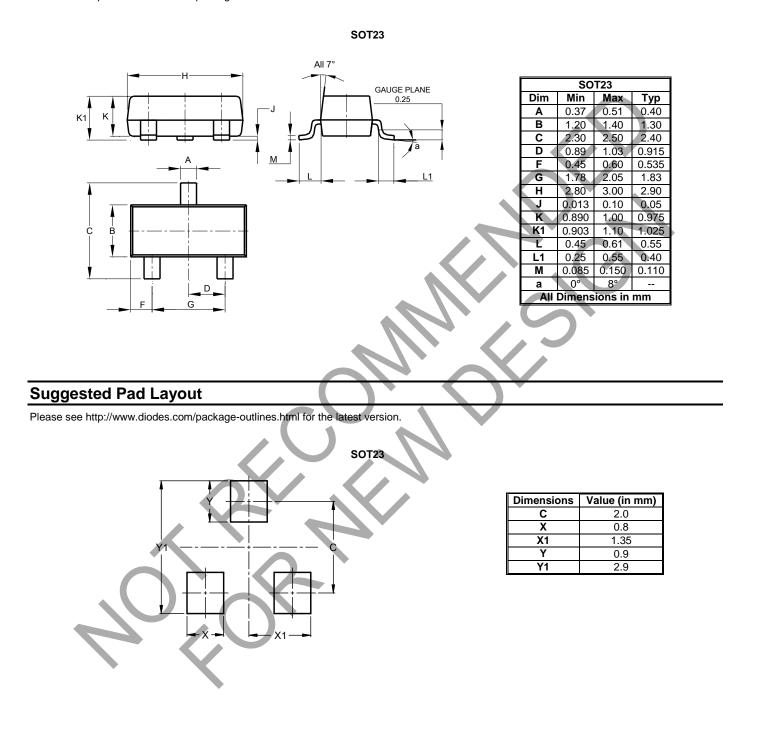






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





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