

NOT RECOMMENDED FOR NEW DESIGN USE DMP2040USS



DMP2066LSS

SINGLE P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	40mΩ @ V _{GS} = -4.5V	-6.5A
-20V	70mΩ @ V _{GS} = -2.5V	-5.0A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Description and Applications

- Backlighting
- **Power Management Functions**
- **DC-DC Converters**

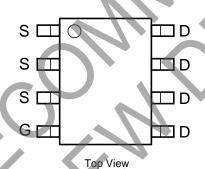
Features and Benefits

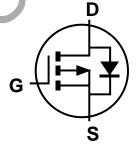
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.074g (Approximate)







Equivalent Circuit

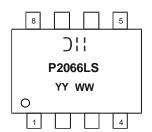
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2066LSS-13	SO-8	2500/Tape & Reel

Notes: 1. No purposely added lead, Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant,

- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>
 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



) | = Manufacturer's Marking P2066LS = Product Type Marking Code YYWW = Date Code Marking YY or \overline{YY} = Year (ex: 19 = 2019) WW = Week (01 to 53)



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Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±12	V
Drain Current (Note 5) Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			I _D	-6.5 -5.2	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-26	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_D	2.5	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	50	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

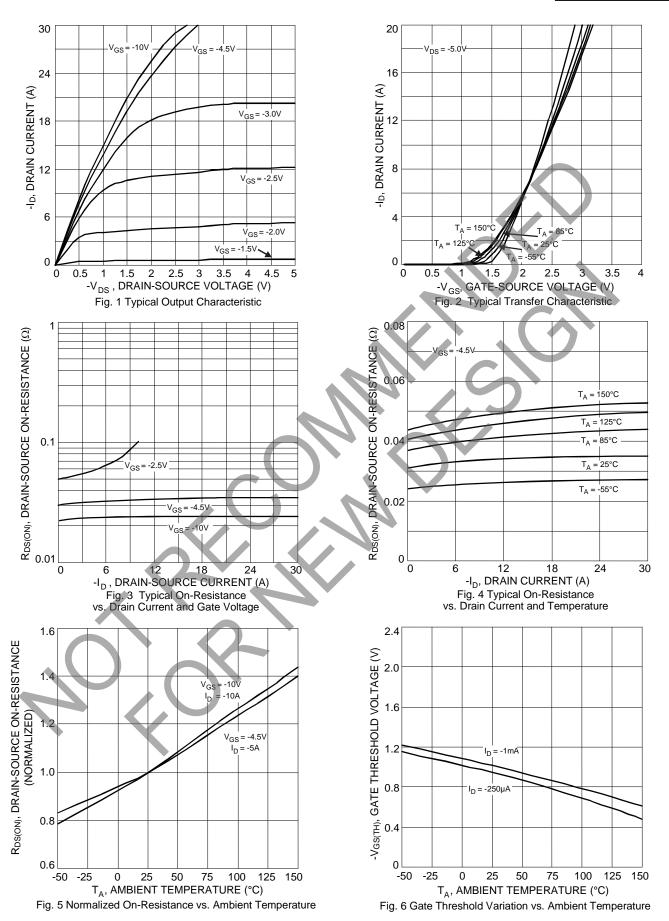
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20			>	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_		-1	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	# 1	-	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(TH)}	-0.6	_	-1.2	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance		1-4	-	40	mΩ	$V_{GS} = -4.5V$, $I_{D} = -5.8A$
Static Brain-Source On-Resistance	R _{DS(ON)}	—		70	11122	$V_{GS} = -2.5V$, $I_{D} = -3.8A$
Forward Transconductance	9 fs		9		S	$V_{DS} = -10V, I_{D} = -4.6A$
Diode Forward Voltage	V _{SD}	-0.5	-0.72	-1.4	V	$V_{GS} = 0V, I_{S} = -2.1A$
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C_{iss}		820	_	pF	
Output Capacitance	Coss		200	_	pF	$V_{DS} = -15V, V_{GS} = 0V$
Reverse Transfer Capacitance	C _{rss}		160	_	pF	
Gate Resistance	Rg	—	10.4	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$
Total Gate Charge	Q_g		14.4	_	101111	
Gate-Source Charge	Q_{gs}	_	2.6	_	nC	$V_{DS} = -10V, V_{GS} = -4.5V$ $I_{D} = -4.5A$
Gate-Drain Charge	Q_{gd}		2.7			ID = -4.5A
Turn-On Delay Time	t _{D(ON)}	_	13.7			
Turn-On Rise Time	t _R	_	14.0		no	$V_{DD} = -10V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t _{D(OFF)}	_	79.1	_	ns	$R_G = 6\Omega$, $R_L = 10\Omega$, $I_D = -1A$
Turn-Off Fall Time	t _F	_	35.5			

Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

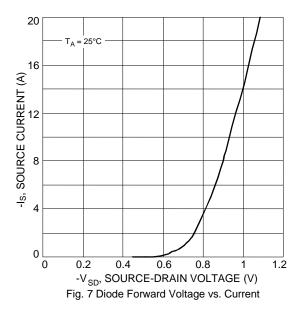
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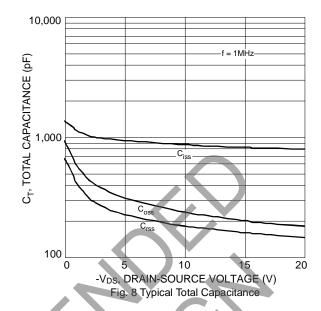
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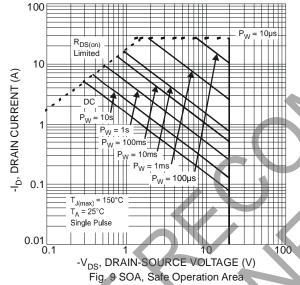










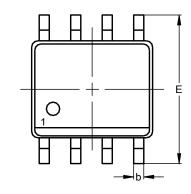


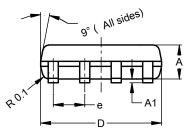
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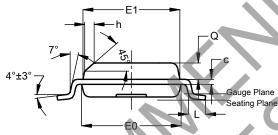
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





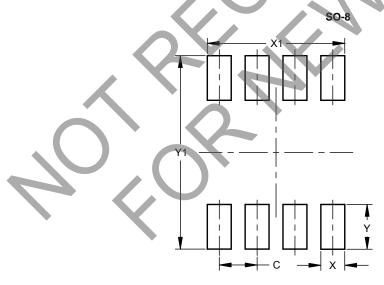




SO-8				
Dim	Min	Max	Тур	
Α	1.40	1.50	1.45	
A1	0.10	0.20	0.15	
b	0.30	0.50	0.40	
C	0.15	0.25	0.20	
D	4.85	4.95	4.90	
E	5.90	6.10	6.00	
E1	3.80	3.90	3.85	
E0	3.85	3.95	3.90	
е			1.27	
h	-		0.35	
L	0.62	0.82	0.72	
Ø	0.60	0.70	0.65	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)		
С	1.27		
Х	0.802		
X1	4.612		
Υ	1.505		
V1	6.50		



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