



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
	7.5mΩ @ V _{GS} = 10V	30A
60V	11.5mΩ @ V _{GS} = 4.5V	25A

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$, yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

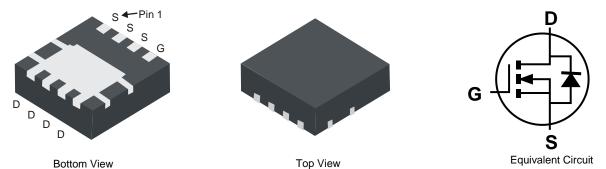
- Synchronous Rectifier
- Backlighting
- Power Management Functions
- DC-DC Converters

Features and Benefits

- Low R_{DS(ON)} Ensures On-State Losses are Minimized
- Excellent Q_{GD X} R_{DS(ON)} Product (FOM)
- Advanced Technology for DC-DC Converters
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the board area occupied by SO-8, enabling smaller end product
- 100% UIS (Avalanche) Rated
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMT6010LFG-7	PowerDI3333-8	2,000/Tape & Reel
DMT6010LFG-13	PowerDI3333-8	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Haloger- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



SG6 = Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 13 = 2013) WW = Week Code (01 ~ 53)

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage		V _{DSS}	60	V	
Gate-Source Voltage		V _{GSS}	±20	V	
	T _A = +25°C T _A = +70°C	ID	13 11	А	
Continuous Drain Current (Note 5) $V_{GS} = 10V$	T _C = +25°C T _C = +70°C	ID	30 24	A	
Maximum Continuous Body Diode Forward Current (Note	ls	3	А		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	80	А	
Avalanche Current, L=0.1mH		I _{AS}	20	А	
Avalanche Energy, L=0.1mH		E _{AS}	20	mJ	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Total Power Dissipation (Note 5)	T _A = +25°C	D-	2.2	W	
	$T_{\rm C} = +25^{\circ}{\rm C}$	PD	41		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Devi	55		
mermai Resistance, Junction to Ambient (Note 5)	t<10s	R _{0JA}	35	°C/W	
Thermal Resistance, Junction to Case (Note 5)	R _{0JC}	3			
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

		-				
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	IDSS	-	—	1	μA	$V_{DS} = 48V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}		_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(TH)}	0.8	—	2	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance			6	7.5	mΩ	$V_{GS} = 10V, I_D = 20A$
	R _{DS(ON)}	—	7.8	11.5		$V_{GS} = 4.5V, I_D = 20A$
Diode Forward Voltage	V _{SD}	—	0.9	1.2	V	$V_{GS} = 0V, I_{S} = 20A$
DYNAMIC CHARACTERISTICS (Note 7)						·
Input Capacitance	C _{ISS}		2,090	—		$V_{DS} = 30V, V_{GS} = 0V,$ f = 1.0MHz
Output Capacitance	C _{OSS}	_	746	—	pF	
Reverse Transfer Capacitance	C _{RSS}	_	38.5	—		
Gate resistance	R _G	—	0.59	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _G	_	19.3	—		V _{DS} = 30V, I _D = 20A
Total Gate Charge (V _{GS} = 10V)	Q _G	_	41.3	_		
Gate-Source Charge	Q _{GS}	_	6.0	_	nC	
Gate-Drain Charge	Q _{GD}	_	8.8	—		
Turn-On Delay Time	t _{D(ON)}	—	5.7	—		$V_{DD} = 30V, V_{GS} = 10V,$ $I_D = 20A, R_G = 3\Omega,$
Turn-On Rise Time	t _R	_	4.3	—		
Turn-Off Delay Time	t _{D(OFF)}	—	23.4	—	nS	
Turn-Off Fall Time	t _F	—	9.7	—	1	

Notes: 5. ReJA is determined with the device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate. ReJC is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design.

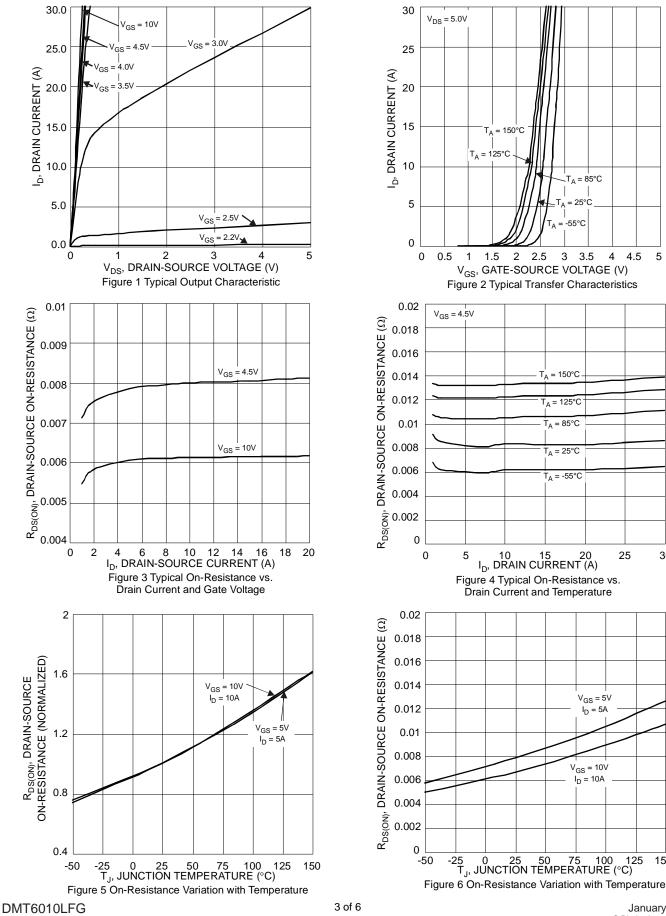
Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

DMT6010LFG

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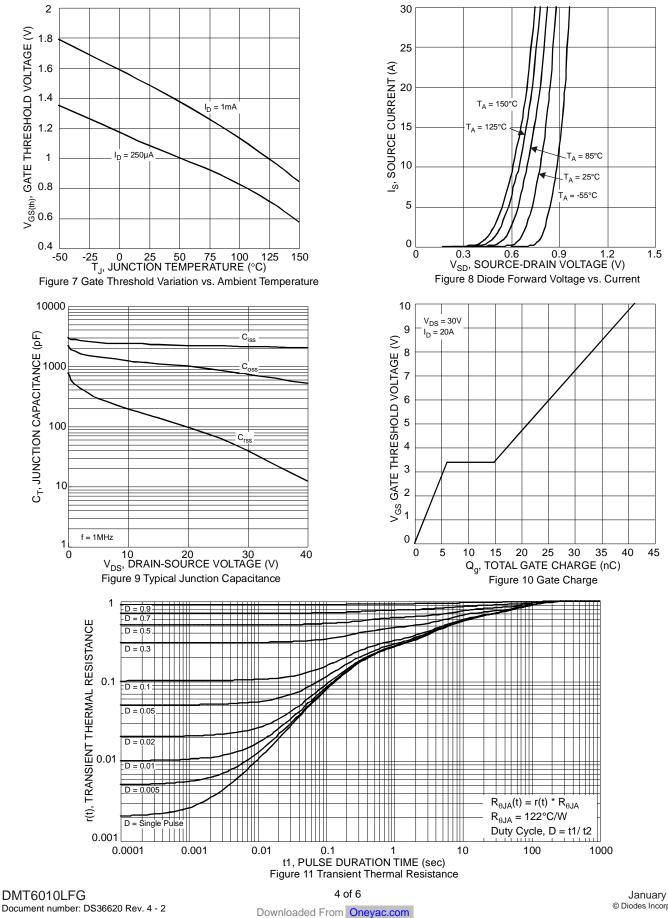


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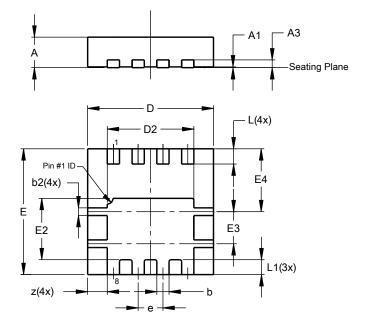






Package Outline Dimensions

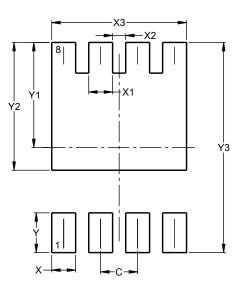
Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.



POWERDI3333-8					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05	0.02		
A3	-	-	0.203		
b	0.27	0.37	0.32		
b2	0.15	0.25	0.20		
D	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
ш	3.25	3.35	3.30		
E2	1.56	1.66	1.61		
E3	0.79	0.89	0.84		
E4	1.60	1.70	1.65		
e	-	-	0.65		
L	0.35	0.45	0.40		
L1	1	-	0.39		
z	_	-	0.515		
	All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/_files/datasheets/ap02001.pdf for the latest version.



PowerDI3333-8

PowerDI3333-8

Dimensions	Value (in mm)
С	0.650
Х	0.420
X1	0.420
X2	0.230
X3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700



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