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DESD1CAN2WQ

CAN BUS ESD PROTECTION DIODE

Product Summary

IPP (Max)	Ст (Тур)
3A	9.3pF
	IPP (Max) 3A

Features

•	150W Peak Power Dissipation per Line (8/20µs Waveform)
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- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±23kV, Contact ±23kV
- 2 Channels of ESD Protection
- Low Channel Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DESD1CAN2WQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

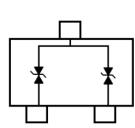
https://www.diodes.com/quality/product-definitions/

Description and Applications

This DESD1CAN2WQ is a next generation ESD and surge protection device packaged in a small footprint surface mount package. It is qualified to AEC-Q101, supported by a PPAP and is designed to protect two data lines of the Controller Area Network (CAN) in an automotive.

- CAN Bus Protection
- Industrial Control Network





Top View Internal Schematic

Ordering Information (Note 4)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DESD1CAN2WQ-7	Automotive	ЗK	7	8	3,000/Tape & Reel

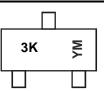
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



3K = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

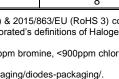
Date Code Key

Notes:

Year	2017		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	E		Н		J	К	L	М	Ν	0	Р	R
Month	lan	Feb	Mar	Apr	May	lun	hul	Διια	Son	Oct	Nov	Dec
Month Code	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

DESD1CAN2WQ

Document number: DS40205 Rev. 5 - 2



Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
 - Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (93)
- Weight: 0.006 grams (Approximate)



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	Ppp	150	W	8/20µs, per Figure 1
Peak Pulse Current	IPP	3	А	8/20µs, per Figure 1
ESD Protection – Contact Discharge	VESD_Contact	±23	kV	IEC 61000-4-2 Standard
ESD Protection – Air Discharge	V _{ESD_Air}	±23	kV	IEC 61000-4-2 Standard

Thermal Characteristics

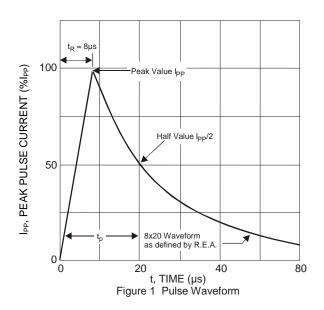
Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	Reja	420	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

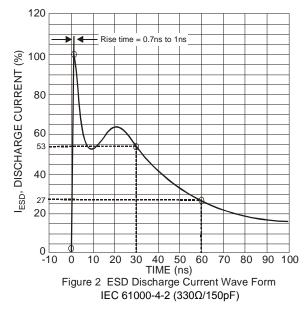
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Standoff Voltage	Vrwm	_	_	24	V	—
Channel Leakage Current (Note 6)	IRM	_	<1	50	nA	V _{RWM} = 24V
Clemping Voltage Desitive Transients		—	_	35		IPP = 1A, tP = 8/20µs, Figure 1
Clamping Voltage, Positive Transients	VcL	—	_	50	V	IPP = 3A, tP = 8/20µs, Figure 1
Breakdown Voltage	VBR	25.4	27.8	30.3	V	I _R = 1mA
Channel Input Capacitance	Ст	_	9.3	12	pF	$V_R = 0V, f = 1MHz$
Channel Input Capacitance	U	_	7.3	10	рг	
ABS Parasitic Capacitance Matching	Δ (C _T _Ch1-C _T _Ch2) / C _T Max	—	0.2	2.2	%	V _R = 5V, f = 250kHz
(Channel 1 – Channel 2)	∆ (C⊤_Ch1-C⊤ _Ch2)	_	0.02	0.22	pF	

Notes:

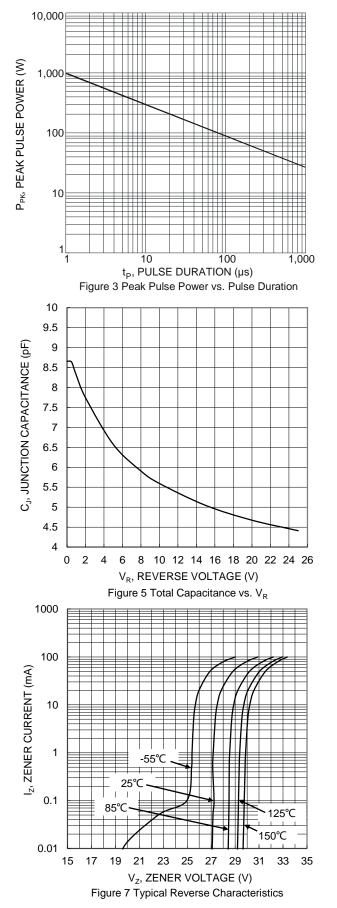
5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.
6. Short duration pulse test used to minimize self-heating effect.







DESD1CAN2WQ



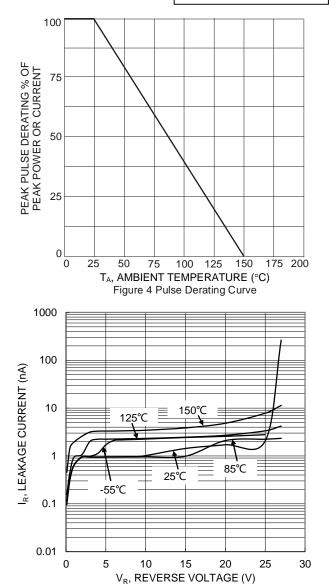


Figure 6 I_R vs. V_R Temperature Characteristic

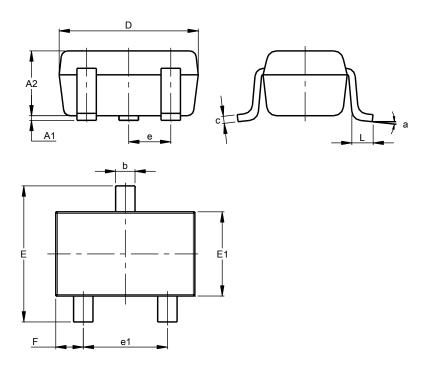
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

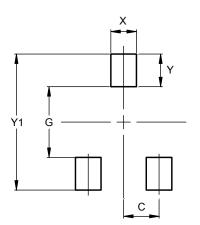


SOT323									
Dim									
Dim	win	Max	Тур						
A1	0.00	0.10	0.05						
A2	0.90	1.00	0.95						
b	0.25	0.40	0.30						
С	0.10	0.18	0.11						
D	1.80	2.20	2.15						
Е	2.00	2.20	2.10						
E1	1.15	1.35	1.30						
е	C).650 B	SC						
e1	1.20	1.40	1.30						
F	0.375	0.475	0.425						
L	0.25	0.40	0.30						
а	0°	8°							
All	Dimen	sions i	in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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