

### **QUADRUPLE 2-INPUT AND GATES**

### Description

The 74LVC08A provides four independent 2-input AND gates. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using IOFF. The IOFF circuitry disables the output preventing damaging current backflow when the device is powered down.

The gates perform the positive Boolean function:

$$Y = A \bullet B \text{ or } Y = \overline{\overline{A} + \overline{B}}$$

### Features

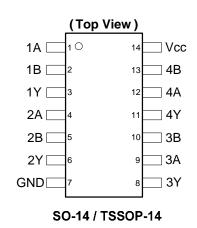
- Wide Supply Voltage Range from 1.65V to 5.5V
- Sinks 24mA at V<sub>CC</sub> = 3.3V
- CMOS low power consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs or outputs accept up to 5.5V
- Inputs can be driven by 3.3V or 5.5V allowing for voltage translation applications.
- ESD Protection Exceeds JESD 22
  - 200-V Machine Model (A115-A)
  - 2000-V Human Body Model (A114-A)
  - Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 250 mA per JESD 78, Class II
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### Notes: 1. No pur

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</li>

## **Pin Assignments**



### Applications

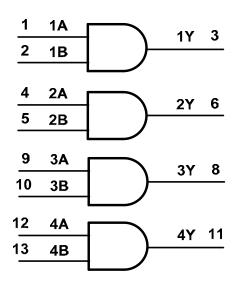
- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as:
  - PCs, networking, notebooks, ultrabooks, netbooks
  - Computer peripherals, hard drives, CD/DVD ROM
  - TV, DVD, DVR, set top box



## **Pin Descriptions**

Pin Number	Pin Name	Description	
1	1A	Data Input	
2	1B	Data Input	
3	1Y	Data Output	
4	2A	Data Input	
5	2B	Data Input	
6	2Y	Data Output	
7	GND	Ground	
8	3Y	Data Output	
9	ЗA	Data Input	
10	3B	Data Input	
11	4Y	Data Output	
12	4A	Data Input	
13	4B	Data Input	
14	V <sub>CC</sub>	Supply Voltage	

# Logic Diagram



## **Function Table**

Inp	Inputs		
Α	A B		
L	L	L	
L	н	L	
н	L	L	
Н	Н	Н	



Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage applied to output in high impedance or I <sub>OFF</sub> state	-0.5 to 6.5	V
Vo	Voltage applied to output in high or low state	-0.3 to V <sub>CC</sub> +0.5	V
lıк	Input Clamp Current VI < 0	-50	mA
I <sub>OK</sub>	Output Clamp Current V <sub>O</sub> < 0	-50	mA
lo	Continuous output current	±50	mA
I <sub>CC</sub> , I <sub>GND</sub>	Continuous current through V <sub>CC</sub> or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C
P <sub>TOT</sub>	Total Power Dissipation	500	mW

# Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Note: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

## Recommended Operating Conditions (Note 5) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage		1.65	5.5	V
VI	Input Voltage		0	5.5	V
<i>\</i> /	V <sub>O</sub> Output Voltage	Active Mode	0	V <sub>CC</sub>	V
VO		V <sub>CC</sub> = 0V; Power Down Mode	0	5.5	V
A ( / A ) /	Langel (magaziti a sila a su fall na fa	V <sub>CC</sub> = 1.65V to 2.7V		20	
Δt/ΔV	Input transition rise or fall rate	$V_{CC} = 2.7V$ to 5.5V		10	ns/V
T <sub>A</sub>	Operating free-air temperature		-40	+125	°C

Notes: 5. Unused inputs should be held at  $V_{\mbox{CC}}$  or Ground.



## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

		THE		$T_{A} = -40^{\circ}C$	C to +85°C	T <sub>A</sub> = -40°C	to +125°C		
Symbol	Parameter	Test Conditions	V <sub>cc</sub>	Min	Max	Min	Max	Unit	
			1.65V to 1.95V	0.65 X V <sub>CC</sub>		0.65 X V <sub>CC</sub>			
VIH	High-level		2.3V to 2.7V	1.7		1.6		V	
	Input Voltage		2.7V to 3.6V	2.0		2.0			
			1.65V to 1.95V		0.35 X V <sub>CC</sub>		0.35 X V <sub>CC</sub>		
VIL	Low-level		2.3V to 2.7V		0.7		0.7	V	
	input voltage		2.7V to 3.6V		0.8		0.8		
		I <sub>OH</sub> = -100μA	1.65V to 3.6V	V <sub>CC</sub> - 0.2		$V_{CC} - 0.3$			
		$I_{OH} = -4mA$	1.65V	1.2					
N/	High Level	I <sub>OH</sub> = -8mA	2.3V	1.9				V	
V <sub>OH</sub>	Output Voltage	1. 10m 4	2.7V	2.2		2.05		V	
	voltage	I <sub>OH</sub> = -12mA	3.0V	2.3		2.1			
		I <sub>OH</sub> = -24mA	3.0V	2.2		2.0			
		I <sub>OH</sub> = 100μA	1.65V to 3.6V		0.2		0.3		
		$I_{OH} = 4mA$	1.65V		0.45		0.6		
M	High-level	I <sub>OH</sub> = 8mA	2.3V		0.70		0.85	V	
V <sub>OL</sub>	Output Voltage	1 10 m 4	2.7V		0.40		0.6	V	
	voltage	I <sub>OH</sub> = 12mA	3.0V		0.55		0.6		
		I <sub>OH</sub> =-24mA	3.0V		0.55		0.6		
lı	Input Current	V <sub>I</sub> =GND to 5.5V	3.6V		± 5		± 20	μA	
I <sub>OFF</sub>	Power Down Leakage Current	$V_1$ or $V_0 =$ 0V to 3.6V	0		10		20	μA	
Icc	Supply Current	$V_{I} = GND \text{ or } V_{CC}$ $I_{O}=0$	3.6V		10		40	μA	
ΔI <sub>CC</sub>	Additional Supply Current	One input at V <sub>CC</sub> – 0.6V Other at V <sub>CC</sub> or Gnd.	2.7V to 3.6V		500		5000	μA	



# **Switching Characteristics**

Symbol	Deremeter	Test	V	T,	<sub>A</sub> = +25°C	;	-40°C to	⊳ +85°C	-40°C to	+125°C	Unit
Symbol	Parameter	Conditions	V <sub>cc</sub>	Min	Тур	Max	Min	Max	Min	Max	Unit
			1.65V to1.95V	1.0	5.0	9.3	1.0	9.8	1.0	11.3	
			2.3V to 2.7V	1.0	2.9	6.4	1.0	6.9	1.0	9.0	
t <sub>PD</sub>			Figure i	2.7V	1.0	3.0	4.6	1.0	4.8	1.0	6.0
	10 1		3V to 3.6V	1.0	2.6	3.9	1.0	4.1	1.0	5.5	
t <sub>SK(0)</sub>	Output Skew Time		3V to 3.6V					1.0		1.5	ns

## **Operating Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

	Parameter	Test Conditions	V <sub>cc</sub> = 1.8V Typ	V <sub>CC</sub> = 2.5V Typ	V <sub>cc</sub> = 3.3V Typ	Unit
C <sub>pd</sub>	Power dissipation capacitance per gate	f = 10 MHz	7.0	7.5	8.0	pF
Cı	Input Capacitance	$V_i = V_{CC} - or$ GND	4	4	4	pF

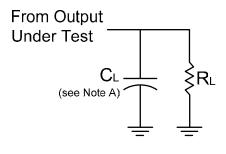
# Package Characteristics

Symbol	Parameter	Test Conditions	V <sub>cc</sub>	Min	Тур	Max	Unit
0	Thermal Resistance	SO-14			TBD		°C/W
$\theta_{JA}$	Junction-to-Ambient TSSOP-14 (Note 6)				159	C/VV	
θ	Thermal Resistance	SO-14	() (a (a (a))		TBD		°C/W
$\theta_{\rm JC}$	Junction-to-Case	TSSOP-14	(Note 6)		25		C/VV

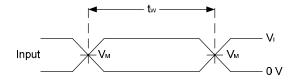
Note: 6. Test condition for SO-14 and TSSOP-14: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



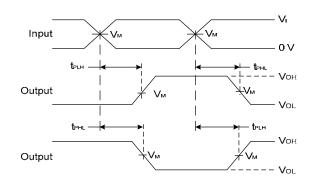
### **Parameter Measuement Information**



Vcc	Inputs		Ver	C	D.
VCC	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	C∟	R∟
1.8V±0.15V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	30pF	1ΚΩ
2.5V±0.2V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	30pF	500Ω
2.7V	2.7V	≤2.5ns	1.5V	50pF	500Ω
3.3V±0.3V	2.7V	≤2.5ns	1.5V	50pF	500Ω



Voltage Waveform Pulse Duration



#### Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Notes: A. Includes test lead and test apparatus capacitance.

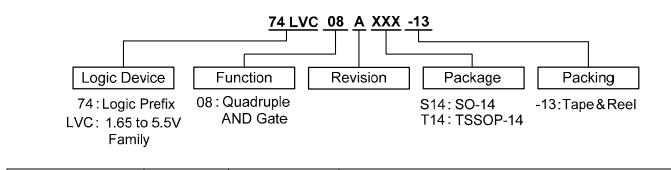
- B. All pulses are supplied at pulse repetition rate ≤ 10 MHz
- C. Inputs are measured separately one transition per measurement

D.  $t_{\text{PLH}}$  and  $t_{\text{PHL}}$  are the same as  $t_{\text{PD}}$ 

### Figure 1. Load Circuit and Voltage Waveforms



## **Ordering Information**

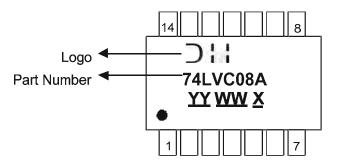


	Device	Package Packaging		13" Tape	and Reel
	Device	Code	(Note 7)	Quantity	Part Number Suffix
Pb,	74LVC08AS14-13	S14	SO-14	2500/Tape & Reel	-13
Pb,	74LVC08AT14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Notes: 7. The taping orientation and tape details can be found at http://www.diodes.com/datasheets/ap02007.pdf

## **Marking Information**

### (1) SO-14, TSSOP-14



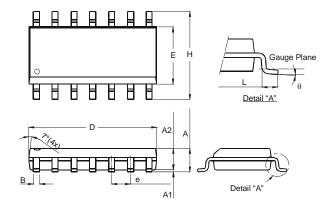
<u>YY</u> : Year : 08, 09,10~ <u>WW</u> : Week : 01~52; 52 represents 52 and 53 week <u>X</u> : Internal Code

Part Number	Package
74LVC08AS14	SO-14
74LVC08AT14	TSSOP-14



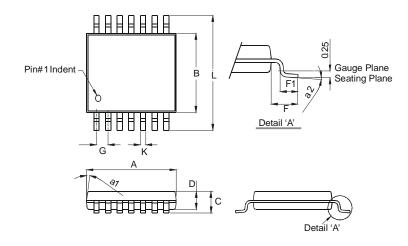
## Package Outline Dimensions (All dimensions in mm.)

### Package Type: SO-14



	SO-14					
Dim	Min	Max				
Α	1.47	1.73				
A1	0.10	0.25				
A2	1.45	Тур				
В	0.33	0.51				
D	8.53	8.74				
Е	3.80	3.99				
е	1.27	Тур				
Н	5.80	6.20				
L	0.38	1.27				
θ	0°	8°				
All Di	All Dimensions in mm					

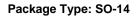
### Package Type: TSSOP-14

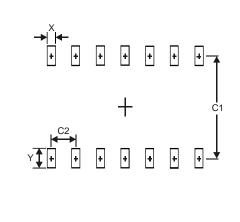


TSSOP-14		
Dim	Min	Max
a1	7° (4X)	
a2	0°	8°
Α	4.9	5.10
В	4.30	4.50
С		1.2
D	0.8	1.05
F	1.00 Тур	
F1	0.45	0.75
G	0.65 Typ	
К	0.19	0.30
L	6.40 Typ	
All Dimensions in mm		



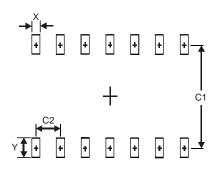
## **Suggested Pad Layout**





Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65



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