



100V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _A = +25°C
-100V	350mΩ @ V _{GS} = -10V	-1.6A
-1007	450mΩ @ V _{GS} = -6.0V	-1.4A

Description

This MOSFET is designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- DC-DC Converters
- Power Management Functions
- Uninterrupted Power Supply

Features and Benefits

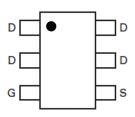
- Fast Switching Speed
- Low Gate Drive
- Low Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The ZXMP10A17E6Q is suitable for automotive applications requiring specific change control and is AEC-Q101 qualified, is PPAP capable, and is manufactured in IATF16949:2016 certified facilities.

Mechanical Data

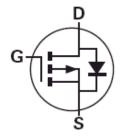
- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208 (a)
- Weight: 0.018 grams (Approximate)



Top View



Pin Out - Top View



Equivalent Circuit

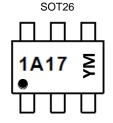
Ordering Information (Note 4)

Part Number	Qualification	Case	Packaging
ZXMP10A17E6QTA	Automotive	SOT26	3,000/Tape & Reel
ZXMP10A17E6QTAR	Automotive	SOT26	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



1A17 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

Year	2015	 2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	С	 Ð	Η	I	J	K	L	М	Ν	0

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

	Characteristic		Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-100	V
Gate-Source Voltage			V _{GSS}	±20	V
		(Note 6)		-1.6	
Continuous Drain Current	$V_{GS} = 10V$	$T_A = +70^{\circ}C \text{ (Note 6)}$	I _D	-1.3	Α
		(Note 5)		-1.3	
Pulsed Drain Current	V _{GS} = 10V	(Note 7)	I _{DM}	-7.7	Α
Continuous Source Current (Body Diode) (No		(Note 6)	Is	-2.1	А
Pulsed Source Current (Bod	y Diode)	(Note 7)	I _{SM}	-7.7	А

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Power Dissipation	(Note 5)		1.1 8.8	W	
Linear Derating Factor	(Note 6)	P _D	1.7 13.7	mW/°C	
Thermal Desistance, Junction to Ambient	(Note 5)	D	113	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)		$R_{\theta JA}$	73	*C/VV	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C		

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

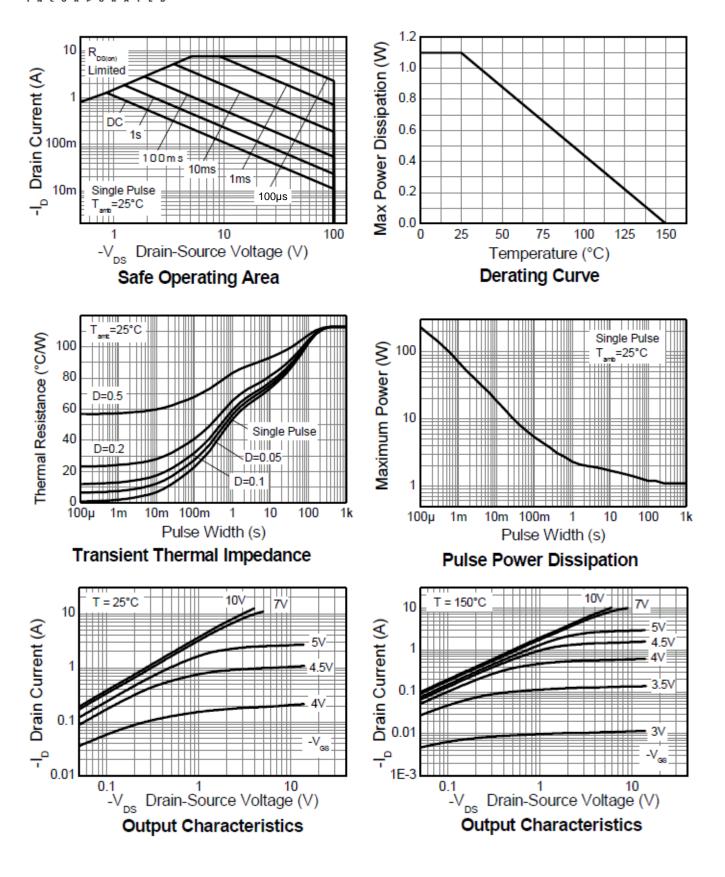
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS			•	•	•		
Drain-Source Breakdown Voltage	BV _{DSS}	-100	_	_	V	$I_D = -250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-0.5	μA	V _{DS} = -100V, V	_{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{D}$	os = 0V
ON CHARACTERISTICS			•	•	•		
Gate Threshold Voltage	V _{GS(TH)}	-2.0	_	-4.0	V	$I_D = -250 \mu A, V_D$	$v_{S} = V_{GS}$
Static Drain-Source On-Resistance (Note 8)	В			0.350	Ω	$V_{GS} = -10V, I_D = -10V$	= -1.4A
Static Dialif-Source Off-Resistance (Note 6)	R _{DS(ON)}			0.450	1 12	$V_{GS} = -6V$, $I_D =$	-1.2A
Forward Transconductance (Notes 8 & 9)	g fs	_	2.8	_	S	$V_{DS} = -15V, I_{D} = -15V$	= -1.4A
Diode Forward Voltage (Note 8)	V _{SD}	_	-0.85	-0.95	V	I _S = -1.7A, V _{GS} = 0V	
Reverse Recovery Time (Note 9)	t _{RR}	_	33	_	ns	1 4 5 4 3 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	
Reverse Recovery Charge (Note 9)	Q _{RR}	_	48	_	nC	$I_S = -1.5A$, di/dt = 100A/ μ s	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	_	424	_	pF	., 50,/,)/	0) /
Output Capacitance	Coss	_	36.6	_	pF	$V_{DS} = -50V, V_{G}$ f = 1MHz	S = 0V
Reverse Transfer Capacitance	Crss	_	29.8	_	pF	1 - 1101112	
Total Gate Charge (Note 10)	Qg	_	7.1	_	nC	$V_{GS} = -6V$	
Total Gate Charge (Note 10)	Qg	_	10.7	_	nC		V _{DS} = -50V
Gate-Source Charge (Note 10)	Q _{gs}	_	1.7	_	nC	$V_{GS} = -10V$	$I_D = -1.4A$
Gate-Drain Charge (Note 10)	Q _{gd}	_	3.8	_	nC		
Turn-On Delay Time (Note 10)	t _{D(ON)}	_	3	_	ns		
Turn-On Rise Time (Note 10)	t _R	_	3.5	_	ns	V _{DD} = -50V, V _{GS} = -10V	
Turn-Off Delay Time (Note 10)	t _{D(OFF)}	_	13.4	_	ns	$I_D = -1A$, $R_G \cong 6$	3.0Ω
Turn-Off Fall Time (Note 10)	t _F	_	7.2	_	ns		

Notes:

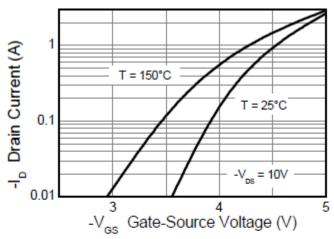
- 5. For a device surface mounted on 25mm x 25mm FR-4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 6. Same as Note 5, except the device is measured at $t \le 5$ sec.
- 7. Same as Note 5, except the device is pulsed with D = 0.05 and pulse width $10\mu s$. The pulse current is limited by the maximum junction temperature.
- 8. Measured under pulsed conditions. Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$.
- 9. For design aid only, not subject to production testing.10. Switching characteristics are independent of operating junction temperatures.

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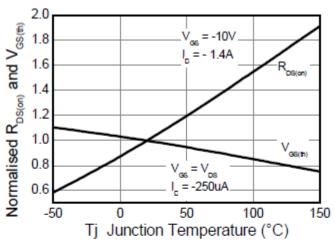




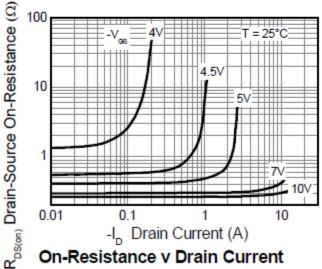




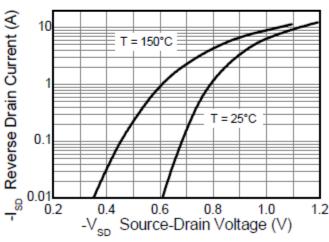
Typical Transfer Characteristics



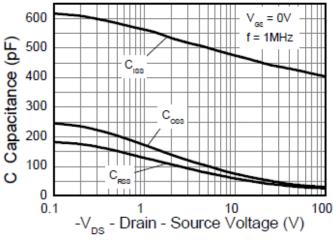
Normalised Curves v Temperature



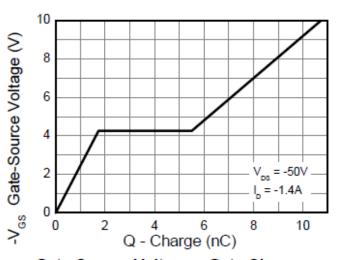
On-Resistance v Drain Current



Source-Drain Diode Forward Voltage



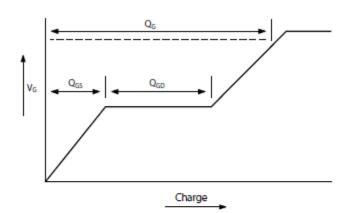
Capacitance v Drain-Source Voltage



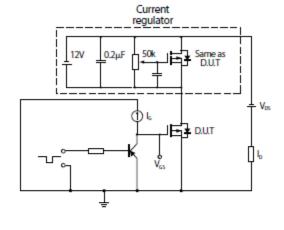
Gate-Source Voltage v Gate Charge



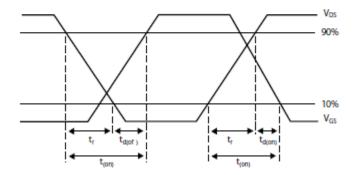
Test Circuits



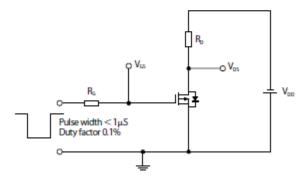
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms



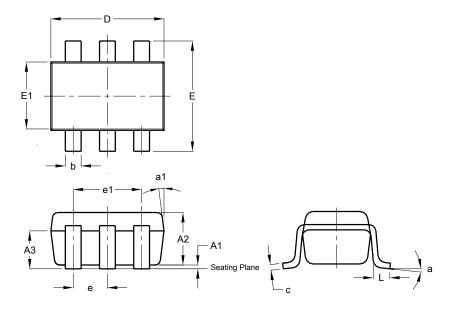
Switching time test circuit



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26

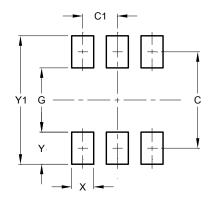


	SOT26							
Dim	Min	Max	Тур					
A1	0.013	0.10	0.05					
A2	1.00	1.30	1.10					
A3	0.70	0.80	0.75					
b	0.35	0.50	0.38					
С	0.10	0.20	0.15					
D	2.90	3.10	3.00					
е	-	-	0.95					
e1	-	1	1.90					
Е	2.70	3.00	2.80					
E1	1.50	1.70	1.60					
L	0.35	0.55	0.40					
а	-	-	8°					
a1	-	-	7°					
All	Dimen	sions	in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26



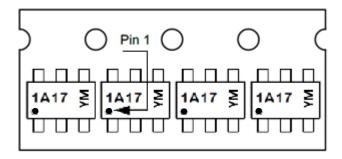
Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Y	0.80
V1	2.20



Tape and Reel Information

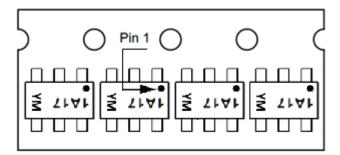
Please see https://www.diodes.com/assets/Packaging-Support-Docs/Ap02007.pdf for the latest version.

ZXMP10A17E6QTA



ZXMP10A17E6QTAR

Rotate 180 degree of Pin 1 orientation in the carrier tape.





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