



1 CHANNEL HIGH SURGE TVS DIODE

Product Summary

V _{BR} (Min)	IPP (Max)	Ст (Тур)
23.5V	28A	200pF

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Applications

- Cellular Handsets
- Portable Electronics
- · Computers and Peripheral

Features

- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV. Contact ±30kV
- One Channels of ESD Protection
- Low Channel Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: U-DFN1610-2
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.003 grams (Approximate)



Device Schematic

Ordering Information (Note 4)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
D22V0H1U2LP1610-7	Standard	MN2	7	8	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

MN2 YM

MN2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

Year	2019	9	2020		2021	20	22	2023		2024	2	2025
Code	G		Н		I		J	K		L		М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current	lpp	28	Α	8/20µs (Note 7)
Peak Pulse Power Dissipation	Ppp	1000	W	8/20µs (Note 7)
ESD Protection – Contact Discharge	Vesd_contact	±30	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	Vesd_air	±30	kV	Standard IEC 61000-4-2

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	300	mW
Thermal Resistance, Junction to Ambient (T _A = +25°C)	Reja	417	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Standoff Voltage	V_{RWM}		_	22	V	_
Channel Leakage Current (Note 6)	lκ	_	_	200	nA	V _R = 22V
Reverse Breakdown Voltage	V_{BR}	23.5	_	_	V	$I_R = 1mA$
	Vc	ı	27	29	V	$I_{PP} = 1A, t_P = 8/20 \mu s$
Clamping Voltage, Positive Transients (Note 7)			30	32	V	$I_{PP} = 10A$, $t_P = 8/20 \mu s$
		_	35	37	V	$I_{PP} = 28A$, $t_P = 8/20\mu s$
Channel Input Capacitance (Note 8)	Ст	_	200	_	pF	$V_R = 0V$, $f = 1MHz$, Any I/O to GND
Dynamic Resistance	RDYN	_	0.05	_	Ω	TLP, IPP = 10A, tP = 100ns

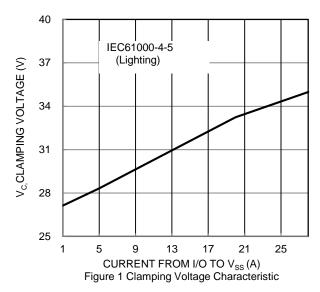
Notes:

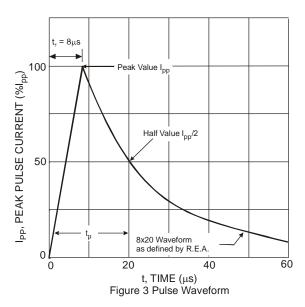
- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.

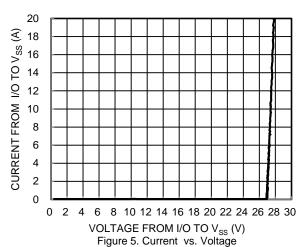
 6. Short duration pulse test used to minimize self-heating effect.
- 7. Clamping voltage value is based on an $8x20\mu s$ peak pulse current (IPP) waveform.
- 8. Measured from any I/O to GND.

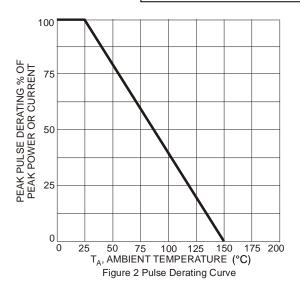
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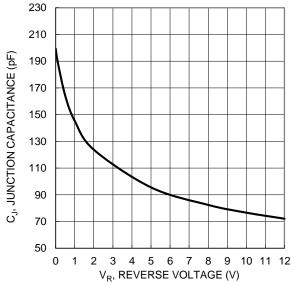


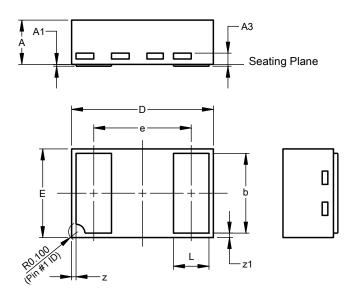
Figure 4: Typical Junction Capacitance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN1610-2 (Type B)

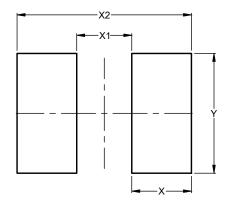


U-DFN1610-2 (Type B)							
Dim	Min	Min Max Typ					
Α	0.45	0.55	0.50				
A1	0.00	0.05	0.015				
A3	-	-	0.127				
b	0.85	0.95	0.90				
D	1.55	1.65	1.60				
E	0.95	1.05	1.00				
е	-	-	1.10				
L	0.35	0.45	0.40				
Z	0.050 REF						
z1	0.050 REF						
All C	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN1610-2 (Type B)



Dimensions	Value (in mm)
Х	0.650
X1	0.600
X2	1.900
Υ	1.300



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