

#### **Features**

- **Epitaxial Planar Die Construction**
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Complementary NPN Type Available (2DD2678)
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

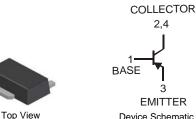


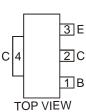
2DB1713

LOW V<sub>CE(SAT)</sub> PNP SURFACE MOUNT TRANSISTOR

### **Mechanical Data**

- Case: SOT89-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.072 grams (approximate)





Pin Out Configuration

**Device Schematic** 

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2,4

#### **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-15	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-12	V
Emitter-Base Voltage	V <sub>EBO</sub>	-6	V
Peak Pulse Current	I <sub>СМ</sub>	-6	A
Continuous Collector Current	lc	-3	A

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ T <sub>A</sub> = 25°C	PD	0.9	W
Thermal Resistance, Junction to Ambient Air (Note 3) @ $T_A = 25^{\circ}C$	$R_{\theta JA}$	139	°C/W
Power Dissipation (Note 4) @ T <sub>A</sub> = 25°C	PD	2	W
Thermal Resistance, Junction to Ambient Air (Note 4) @ $T_A = 25^{\circ}C$	$R_{\theta JA}$	62.5	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

### Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Conditions
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	-15	_		V	$I_{\rm C} = -10 \mu A$ , $I_{\rm E} = 0$
Collector-Emitter Breakdown Voltage (Note 5)	V <sub>(BR)CEO</sub>	-12	_	_	V	$I_{\rm C} = -1 {\rm mA},  I_{\rm B} = 0$
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	-6	_	_	V	$I_{\rm E} = -10\mu A, I_{\rm C} = 0$
Collector Cut-Off Current	I <sub>CBO</sub>	_	_	-0.1	μΑ	$V_{CB} = -15V, I_E = 0$
Emitter Cut-Off Current	I <sub>EBO</sub>	_	_	-0.1	μΑ	$V_{EB} = -6V, I_{C} = 0$
ON CHARACTERISTICS (Note 5)						
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	-120	-250	mV	I <sub>C</sub> = -1.5A, I <sub>B</sub> = -30mA
DC Current Gain	h <sub>FE</sub>	270	_	680	_	$V_{CE} = -2V, I_{C} = -500 \text{mA}$
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C <sub>obo</sub>	—	40	_	pF	$V_{CB} = -10V, I_E = 0,$ f = 1MHz
Current Gain-Bandwidth Product	f <sub>T</sub>	—	180	_	MHz	$V_{CE} = -2V, I_C = -100mA, f = 100MHz$

Notes: No purposefully added lead. 1.

2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.

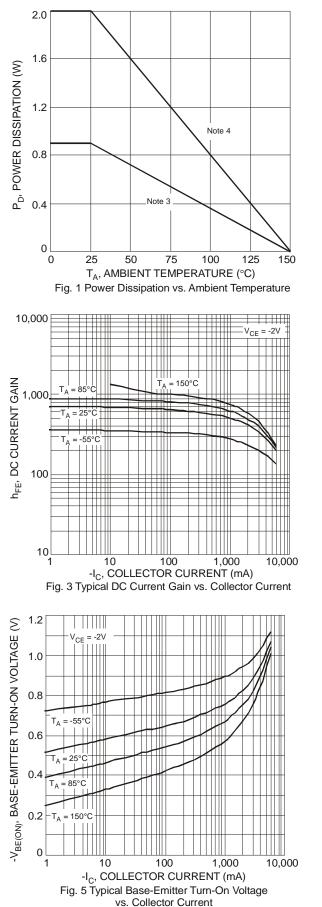
Device mounted on FR-4 PCB with minimum recommended pad layout. 3.

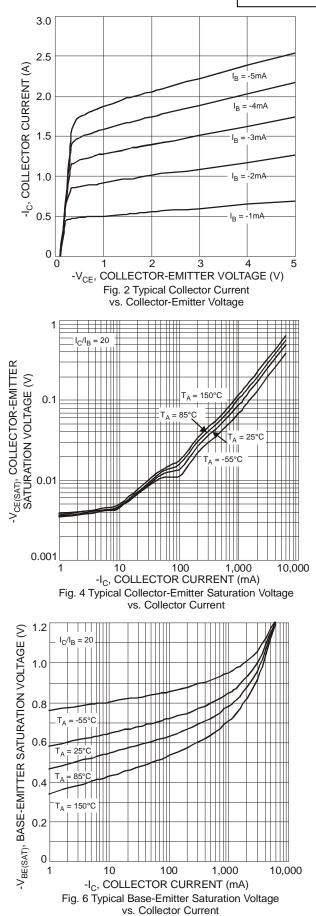
Device mounted on FR-4 PCB with 1 inch<sup>2</sup> copper pad layout. 4.

5. Measured under pulsed conditions. Pulse width =  $300\mu$ s. Duty cycle  $\leq 2\%$ .

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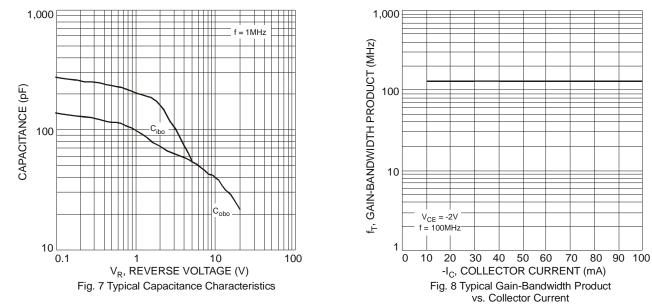








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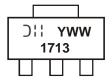


# Ordering Information (Note 6)

Part Number	Case	Packaging
2DB1713-13	SOT89-3L	2500/Tape & Reel

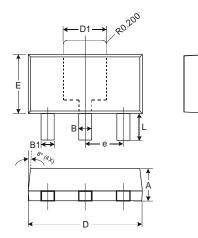
Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

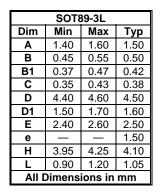
## **Marking Information**



1713 = Product Type Marking Code YWW = Date Code Marking Y = Last digit of year (ex: 8 = 2008) WW = Week code 01 - 52

# **Package Outline Dimensions**



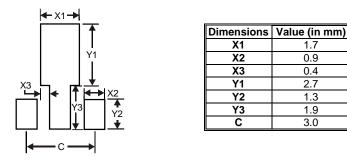


2DB1713 Document number: DS31634 Rev. 2 - 2



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## **Suggested Pad Layout**



1.7

0.9

0.4

2.7

1.3 1.9

3.0

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