

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVDSS	Rds(on) max	ID MAX TA = +25°C
30V	30mΩ @ V _{GS} = 10V	5.3A
301	40mΩ @ V _{GS} = 4.5V	4.6A

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- · ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

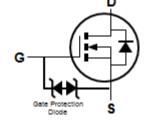
- Load Switch
- DC-DC Converters
- Power Management Functions

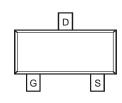
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.009 grams (Approximate)









Top View

Equivalent Circuit

Top View

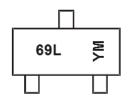
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3069L-7	SOT23	3,000/Tape & Reel
DMN3069L-13	SOT23	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



69L = Product Type Marking Code Y or \overline{Y} = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Date Code Rey												
Year	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	F	G	Н	I	J	K	L	М	N	0	Р	R
	1		ı	ı	ı	ı				I -		_
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	30	V		
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	I _D	5.3 4.3	А		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	30	Α		
Maximum Body Diode Forward Current (Note 6)			Is	1.35	Α

Thermal Characteristics

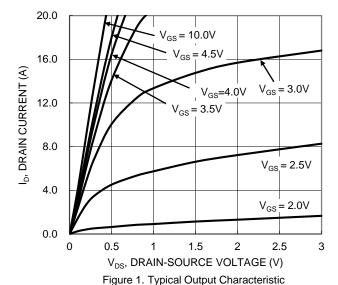
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	158.8	°C/W
Total Power Dissipation (Note 6)		P _D	1.3	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	97.6	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	V _{DS} = 24V, V _{GS} = 0V
Gate-Body Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)			•		•	
Gate Threshold Voltage	V _{GS(TH)}	1.0	_	1.8	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	RDS(ON)	_	20 25	30 40	mΩ	V _{GS} = 10V, I _D = 4.0A V _{GS} = 4.5V, I _D = 3.5A
Source-Drain Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	309	_	pF	
Output Capacitance	Coss	_	56	_	pF	V _{DS} = 15V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	47	_	pF	-1 - 1.0WH12
Gate Resistance	Rg	_	4.6	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = 10V)	Qg	_	8.1	_	nC	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	4.3	_	nC	\/ 45\/ I- 40
Gate-Source Charge	Qgs	_	0.5	_	nC	$V_{DS} = 15V, I_{D} = 4A$
Gate-Drain Charge	Q _{gd}	_	2.2	_	nC]
Turn-On Delay Time	td(ON)	_	1.6	_	ns	
Turn-On Rise Time	t _R	_	2.3	_	ns	V _{DD} = 15V, V _{GS} = 10V,
Turn-Off Delay Time	tD(OFF)	_	10.8	_	ns	$R_L = 15\Omega$, $R_g = 6\Omega$
Turn-Off Fall Time	t _F	_	5.2	_	ns	

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:





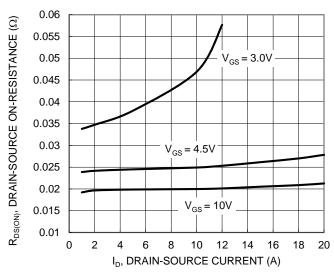


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

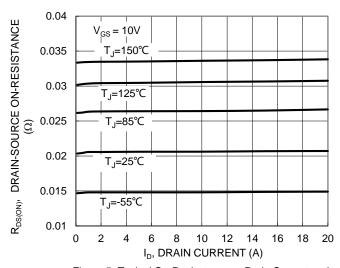


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

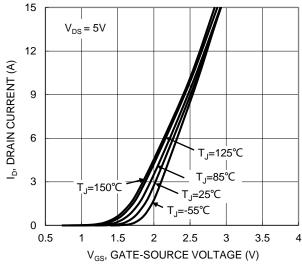


Figure 2. Typical Transfer Characteristic

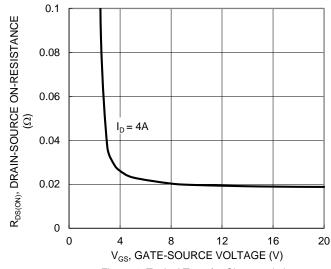


Figure 4. Typical Transfer Characteristic

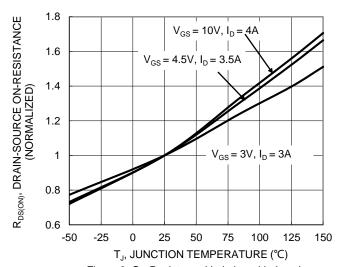


Figure 6. On-Resistance Variation with Junction Temperature



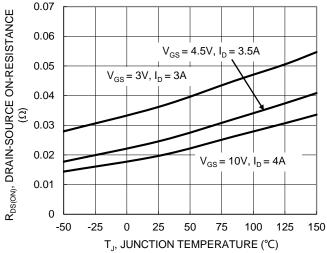


Figure 7. On-Resistance Variation with Junction Temperature

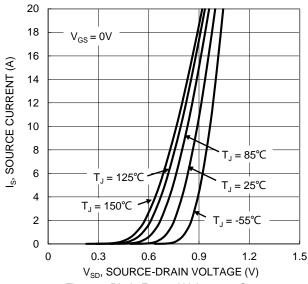


Figure 9. Diode Forward Voltage vs. Current

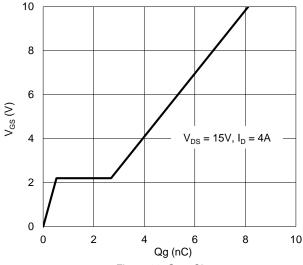


Figure 11. Gate Charge

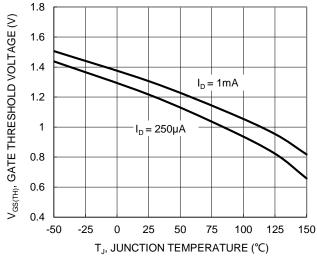
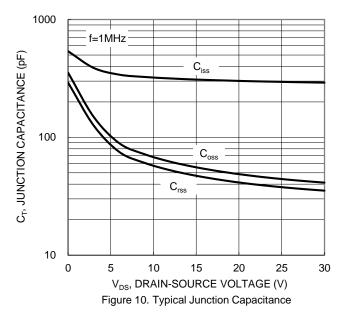
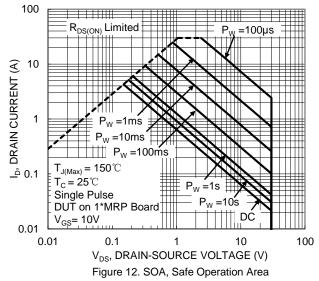


Figure 8. Gate Threshold Variation vs. Junction Temperature







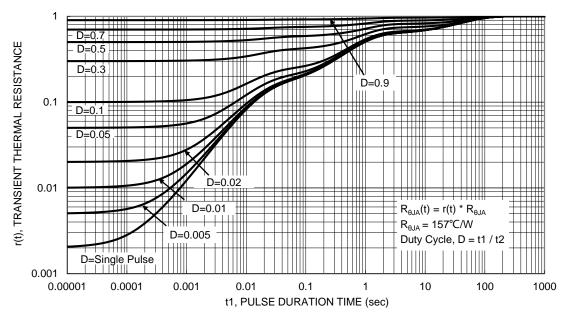


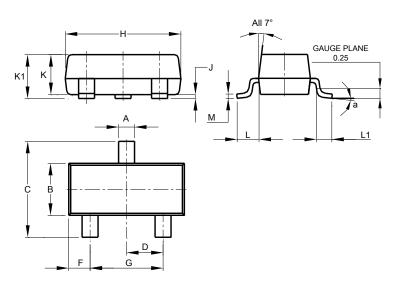
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

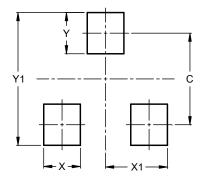


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
X	0.8
X1	1.35
Υ	0.9
Y1	2.9



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