



#### P-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> max	l <sub>D</sub> max T <sub>A</sub> = +25°C
-20V	54mΩ @ V <sub>GS</sub> = -4.5V	-2.5A
-200	90mΩ @ V <sub>GS</sub> = -1.8V	-1.8A

### **Description and Applications**

This MOSFET has been designed to minimize the on-state resistance  $(R_{DS(ON)})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- Power Management Functions
- DC-DC Converters

### **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 3kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

#### **Mechanical Data**

- Case: X2-DFN2015-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 @4
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (approximate)

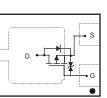




Top View



Bottom View



Internal Schematic

### Ordering Information (Note 4 & 5)

Part Number	Compliance	Case	Packaging
DMP2069UFY4Q-7	Automotive	X2-DFN2015-3	3,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product\_grade\_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**

Notes:

•	29P
	YM

29P = Marking Code YM = Date Code Marking Y = Year (ex: W = 2009) M = Month (ex: 9 = September)

Date Code Key												
Year	200	9	2010		2011	20	)12	2013		2014	1	2015
Code	W		Х		Y		Z	А		В		С
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# Maximum Ratings @TA = 25°C unless otherwise specified

Characte	eristic		Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	-20	V
Gate-Source Voltage		V <sub>GSS</sub>	±8	V	
Continuous Drain Current (Note 6)Steady State $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		ID	-2.5 -2.2	А	
Pulsed Drain Current (Note 7)		I <sub>DM</sub>	-12	А	

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	PD	0.53	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C	R <sub>0JA</sub>	231	°C/W
Operating and Storage Temperature Range	T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	C°

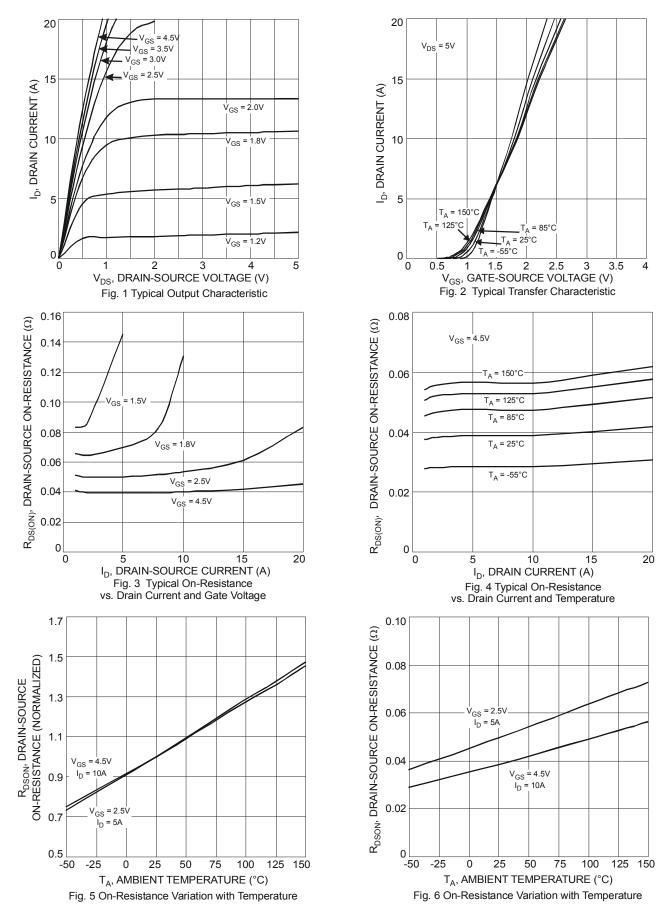
## Electrical Characteristics @T<sub>A</sub> = +25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 8)								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20		_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA		
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	I <sub>DSS</sub>	_		-1.0	μA	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V		
Gate-Source Leakage	I <sub>GSS</sub>	_		±10	μA	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V		
ON CHARACTERISTICS (Note 8)						_		
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.3	-0.55	-1.0	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$		
			36	54		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2.5A		
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	46	69	mΩ	V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -2.2A		
			60	90		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -2.0A		
Forward Transfer Admittance	Y <sub>fs</sub>	_	8		S	$V_{DS} = -5V, I_D = -2.5A$		
DYNAMIC CHARACTERISTICS (Note 9)								
Input Capacitance	C <sub>iss</sub>	_	214		pF			
Output Capacitance	Coss	_	104		pF	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f = 1.0MHz		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	25	_	pF			
Gate Resistnace	Rg	_	250	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$		
SWITCHING CHARACTERISTICS (Note 9)								
Total Gate Charge	Qg	_	9.1		nC			
Gate-Source Charge	Qgs	_	1.5	_	nC	$V_{GS}$ = -4.5V, $V_{DS}$ = -10V, $I_D$ = -4A		
Gate-Drain Charge	Q <sub>gd</sub>	_	1.7	_	nC			
Turn-On Delay Time	t <sub>D(on)</sub>	_	80.4	160	ns			
Turn-On Rise Time	tr	_	155.1	210	ns	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -4.5V,		
Turn-Off Delay Time		_	688.1	1376	ns	$R_D$ = 2.5Ω, $R_G$ = 3.0Ω		
Turn-Off Fall Time	t <sub>D(off)</sub> t <sub>f</sub>	_	423.8	848	ns	7		

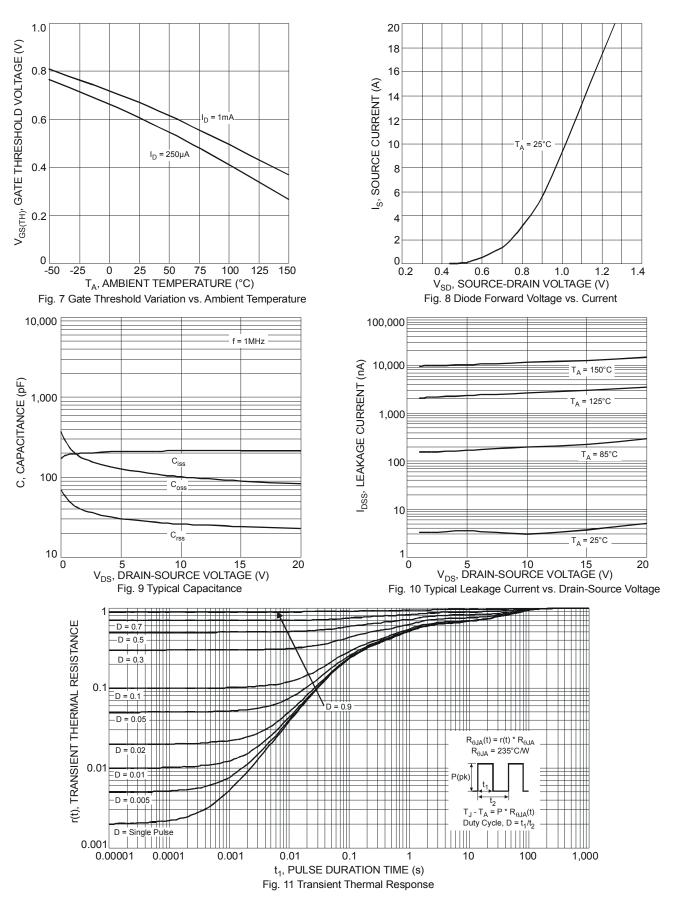
 Device mounted on FR-4 PCB with minimum recommended pad layout.
Repetitive rating, pulse width limited by junction temperature.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing. Notes:



## DMP2069UFY4Q



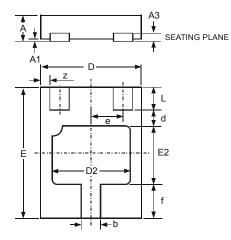






## **Package Outline Dimensions**

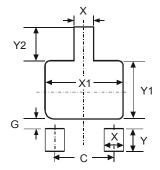
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



X2-DFN2015-3						
Dim	Min	Max	Тур			
Α	-	0.40	-			
A1	0	0.05	0.02			
A3	-	-	0.13			
b	0.20	0.30	0.25			
d	1	-	0.30			
D	1.45	1.575	1.50			
D2	1.00	1.20	1.10			
е	-	-	0.50			
Е	1.95	1.95 2.075 2.00				
E2	0.70	0.90	0.80			
f	-	-	0.60			
L	0.25	0.35	0.30			
z	_	_	0.125			
All D	imens	ions ir	n mm			

## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
С	1.00			
G	0.15			
Х	0.31			
X1	1.30			
Y	0.50			
Y1	1.00			
Y2	0.65			



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