



60V INPUT, 5V 15mA REGULATOR TRANSISTOR IN SOT23F

Description

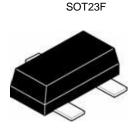
This regulator transistor is designed to meet the stringent requirements of automotive applications.

The ZXTR2105FFQ monolithically integrates a transistor, zener diode and resistor to function as a linear regulator. The device regulates with a 5V nominal output at 15mA. It is designed for use in highvoltage applications where standard linear regulators cannot be used. This function is fully integrated into a SOT23F package, minimizing PCB area and reducing the number of components when compared with a multi-chip discrete solution.

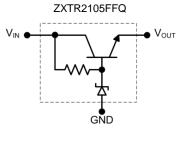
Applications

Supply voltage regulation for:

- 12V to 5V Rails
- 24V to 5V Rails
- Other Customized Input Rails



Top View



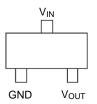
Internal Device Schematic

Features

- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 7V to 60V (For Regulated Output Voltage)
- Output Voltage = 5V ± 5%
- Fully Integrated into a SOT23F Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: SOT23F
- Case Material: Molded Plastic "Green" Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (Approximate)



Pin Name	Pin Function
VIN	Input Supply
GND	Power Ground
V _{OUT}	Voltage Output

Top View Pin-Out

Ordering Information (Note 5)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel	
ZXTR2105FFQ-7	Automotive	2T1	7	8	3,000	
Notes: 1 No purposely added lead Eully ELI Directive 2002/95/EC (RoHS), 2011/65/ELI (RoHS 2) & 2015/863/ELI (RoHS 3) compliant						

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.

5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





2T1 = Product Type Marking Code



Absolute Maximum Ratings (Voltage relative to GND, @T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	VIN	-0.3 to 60	V
Continuous Input and Output Current	IIN, IOUT	320	mA
Peak Pulsed Input and Output Current	IIM, IOM	2	А
Maximum Voltage Applied to V _{OUT}	Vout(max)	Smaller of V _{IN} +5V or 10V	V

Maximum Current at V_{IN} = 12V (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Continuous Output Current	(Note 8)	Іоит	89	mA
Buland Output Current	(Note 9)	I _{OM}	2,000	~^
Pulsed Output Current	(Note 10)		890	mA

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Dower Dissinction	(Note 6)		1.3	w
Power Dissipation	(Note 7)	– P _D –	1	vv
Thermal Desistance Junction to Ambient	(Note 6)	P	95	
Thermal Resistance, Junction to Ambient	(Note 7)	R _{0JA}	126	°C/W
Thermal Resistance, Junction to Lead	(Note 11)	R _{θJL}	59	°C/vv
Thermal Resistance, Junction to Case	(Note 11)	R _{θJC}	38	
Maximum Operating Junction and Storage Temperature Range		T _J , T _{STG}	-65 to +150	٥C

ESD Ratings (Note 12)

Characteristics	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	ЗA
Electrostatic Discharge – Charge Device Model	ESD CDM	1,000	V	IV

Notes: 6. For a device mounted with the VIN lead on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in steady-state.

7. Same as Note 6, except mounted on 15mm x 15mm 1oz copper.

8. Same as Note 6, whilst operating at VIN=12V. Refer to Safe Operating Area for other Input Voltages.

9. Same as Note 6, except measured with a single pulse width = 100 μ s and V_{IN}=12V.

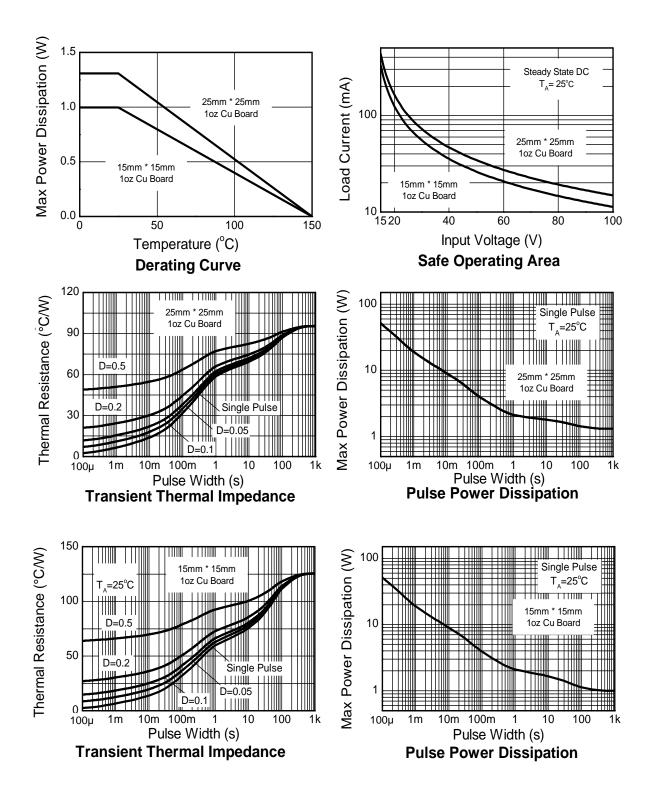
10. Same as Note 6, except measured with a single pulse width = 10ms and V_{IN} =12V.

11. $R_{\theta JL}$ = Thermal resistance from junction to solder-point (at the end of the VIN lead). $R_{\theta JC}$ = Thermal resistance from junction to the top of case.

12. Refer to JEDEC specification JESD22-A114 and JESD22-C101.



Thermal Characteristics and Derating Information





Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Output Voltage (Note 13)	V _{OUT}	4.75	5.0	5.25	V	$V_{IN} = 12V, I_{OUT} = 15mA$
			33	220		$V_{IN} = 10V$ to 15V, $I_{OUT} = 15mA$
Line Regulation (Notes 13 & 14)	ΔV_{OUT}		400	700	mV	$V_{IN} = 7V$ to 60V, $I_{OUT} = 15mA$
		_	145	400		$V_{IN} = 10V$ to 60V, $I_{OUT} = 15mA$
Temperature Coefficient	$\Delta V_{OUT} / \Delta T$	_	3.52	—	mV/°C	$\label{eq:TJ} \begin{split} T_J &= -40^\circ C \text{ to } +150^\circ C \\ V_{IN} &= 12 V, \ I_{OUT} &= 15 \text{mA} \end{split}$
Load Regulation (Notes 13 & 15)	ΔV_{OUT}	-	-20 -166	-130 -300	mV	I_{OUT} = 10mA to 20mA, V_{IN} = 12V I_{OUT} = 0.1mA to 50mA, V_{IN} = 12V
Minimum Value of Input Voltage Required to Maintain Line Regulation	V _{IN(MIN)}	7	—	—	V	_
Quiescent Current	ΙQ		450 4,000	800 6,700	μA	V _{IN} = 12V, I _{OUT} = 10μA V _{IN} = 60V, I _{OUT} = 10μA
Power Supply Rejection Ratio	ΔVIN / ΔVout	_	46	_	dB	$C_{OUT} = 100$ nF, $I_{OUT} = 15$ mA, $V_{OUT} = 5$ V, $V_{IN} = 7$ V to 60V, f = 100Hz

Notes: 13. Measured Under Pulsed Conditions; Pulse Width ≤ 300µs. Duty cycle ≤ 2%.

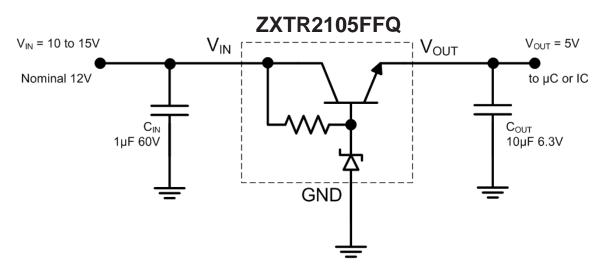
14. Line Regulation $\Delta V_{OUT} = V_{OUT}(@V_{IN} = 15V) - V_{OUT}(@V_{IN} = 10V)$

 $\Delta V_{OUT} = V_{OUT} (@V_{IN} = 60V) - V_{OUT} (@V_{IN} = 7V)$

 $\Delta V_{OUT} = V_{OUT} (@V_{IN} = 60V) - V_{OUT} (@V_{IN} = 10V)$ 15. Load Regulation $\Delta V_{OUT} = V_{OUT} (@I_{OUT} = 20mA) - V_{OUT} (@I_{OUT} = 10mA)$

 $\Delta V_{OUT} = V_{OUT} (@I_{OUT} = 50 \text{mA}) - V_{OUT} (@I_{OUT} = 0.1 \text{mA})$

Typical Application Circuit



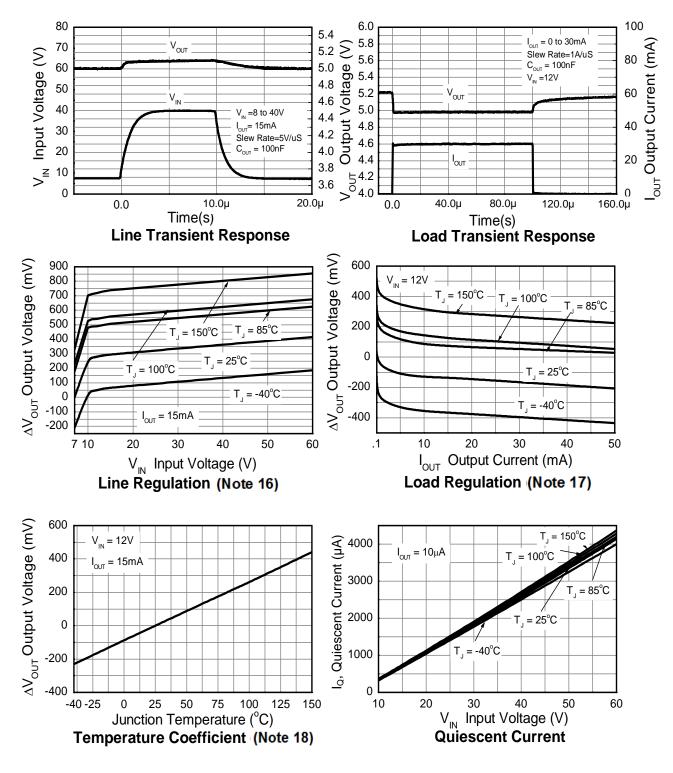
Example of a 5V regulated supply from a nominal 12V for powering a Controller IC.

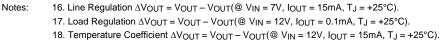
Pin Functions

Pin Name	Pin Function	Notes	
VIN	Input Supply	Input voltage can vary from -0.3V to 60V with respect to GND; for V _{OUT} regulated then 7V \leq V _{IN} \leq 60V. It is recommended to connect a 1µF capacitor to GND.	
GND	Power Ground	This pin should be tied to the system ground.	
Vout	Voltage Output	Outputs a regulated 5V when $7V \le V_{IN} \le 60V$. When $V_{IN} < 7V$, then V_{OUT} maximum = $V_{IN} - 1V$. The pin can be pulled high to a maximum of +10V with respect to GND, or +5V with respect to V_{IN} , whichever is lower. It is recommended to connect a 10μ F capacitor to GND and a minimum of 10μ A to be drawn from V_{OUT} to maintain regulation.	



Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

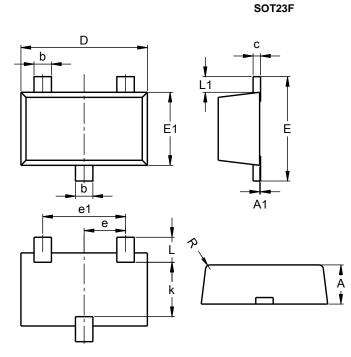






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

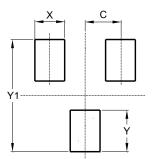


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SOT23F						
Dim	Min	Max	Тур			
Α	0.80	1.00	0.90			
A1	0.00	0.10	0.01			
b	0.35	0.50	0.44			
c	0.10	0.20	0.16			
D	2.80	3.00	2.90			
e	0.95 REF					
e1		1.90 RE	F			
ш	2.30	2.50	2.40			
E1	1.50	1.70	1.65			
k	1.20	-	-			
1	0.30	0.65	0.50			
L1	0.30	0.50	0.40			
R	0.05	0.15	-			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23F



Dimensions	Value (in mm)
С	0.95
Х	0.80
Y	1.110
Y1	3.000



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