



60V DUAL N-CHANNEL SELF PROTECTED ENHANCEMENT MODE INTELLIFET® MOSFET

Product Summary

Continuos Drain Source Voltage 60V On-State Resistance $100 m\Omega$ Nominal Load Current (V_{IN} = 5V) 2.8A Clamping Energy 210mJ

Description

The ZXMS6006DT8Q is a dual self protected low side MOSFET with logic level input. It integrates over-temperature, over-current, overvoltage (active clamp) and ESD protected logic level functionality. The ZXMS6006DT8Q is ideal as a general purpose switch driven from 3.3V or 5V microcontrollers in harsh environments where standard MOSFETs are not rugged enough.

- Lamp Driver
- Motor Driver
- Relay Driver
- Solenoid Driver

Features and Benefits

- Compact High Power Dissipation Package
- Low Input Current
- Logic Level Input (3.3V and 5V)
- Short Circuit Protection with Auto Restart
- Over Voltage Protection (Active Clamp)
- Thermal Shutdown with Auto Restart
- **Over-Current Protection**
- Input Protection (ESD)
- High Continuous Current Rating
- Lead-Free Finish; RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP Capable**

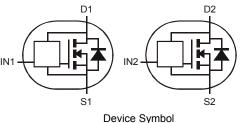
Mechanical Data

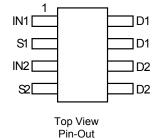
- Case: SM-8
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish
- Weight: 0.117 grams (approximate)



SM-8







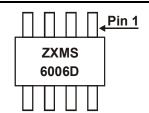
Ordering Information (Note 4)

Droduct	Moulsing	Deal size (inches)	Towns: alth. (mams)	Overetite and mod
Product	Marking	Reel Size (Inches)	i ape width (mm)	Quantity per reel
ZXMS6006DT8QTA	ZXMS6006D	7	12	1.000

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information



Top View

ZXMS6006D = Product Type Marking Code

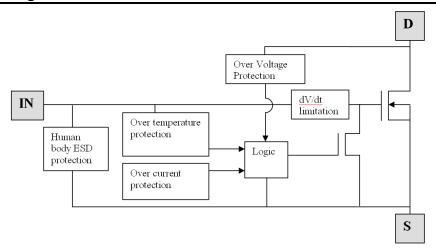
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Functional Block Diagram



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Continuous Drain-Source Voltage	V _{DS}	60	V
Drain-Source Voltage For Short Circuit Protection	V _{DS(SC)}	16	V
Continuous Input Voltage	V _{IN}	-0.5 to +6	V
Continuous Input Current @-0.2V \leq V _{IN} \leq 6V Continuous Input Current @V _{IN} $<$ -0.2V or V _{IN} $>$ 6V	I _{IN}	No limit I _{IN} ≤2	mA
Pulsed Drain Current @V _{IN} = 3.3V (Note 7)	I _{DM}	11	А
Pulsed Drain Current @V _{IN} = 5V (Note 7)	I _{DM}	13	Α
Continuous Source Current (Body Diode) (Note 5)	Is	2	Α
Pulsed Source Current (Body Diode)	I _{SM}	12	Α
Unclamped Single Pulse Inductive Energy, $T_J = +25^{\circ}C$, $I_D = 0.5A$, $V_{DD} = 24V$	Eas	210	mJ
Electrostatic Discharge (Human Body Model)	V _{ESD}	4000	V
Charged Device Model	V _{CDM}	1000	V

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Power Dissipation at T _A = +25°C (Notes 5 & 8) Linear Derating Factor	PD	1.16 9.28	W mW/°C
Power Dissipation at T _A = +25°C (Notes 5 & 9) Linear Derating Factor	P _D	1.67 13.3	W mW/°C
Power Dissipation at T _A = +25°C (Notes 6 & 8) Linear Derating Factor	P _D	2.13 17	W mW/°C
Thermal Resistance, Junction to Ambient (Notes 5 & 8)	R _{θJA}	108	°C/W
Thermal Resistance, Junction to Ambient (Notes 5 & 9)	R _{θJA}	75	°C/W
Thermal Resistance, Junction to Case (Notes 6 & 8)	R _{θJC}	58.7	°C/W
Thermal Resistance, Junction to Case (Note 10)	R _{θJC}	26.5	°C/W
Operating Temperature Range	TJ	-40 to +150	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

Notes:

- 5. For a dual device surface mounted on a 25mm x 25mm single sided 1oz weight copper split down the middle on 1.6mm FR4 board, in still air conditions
- 6. For a dual device surface mounted on FR4 PCB measured at t≤ 10sec
- 7. Repetitive rating25mm x 25mm FR4 PCB, D = 0.02, Pulse width = 300µs pulse width limited by junction temperature. Refer to transient thermal impedance graph.
- 8. For a dual device with one active die.
- 9. For a dual device with 2 active die running at equal power.
- 10. Thermal resistance from junction to the mounting surface of the drain pin.

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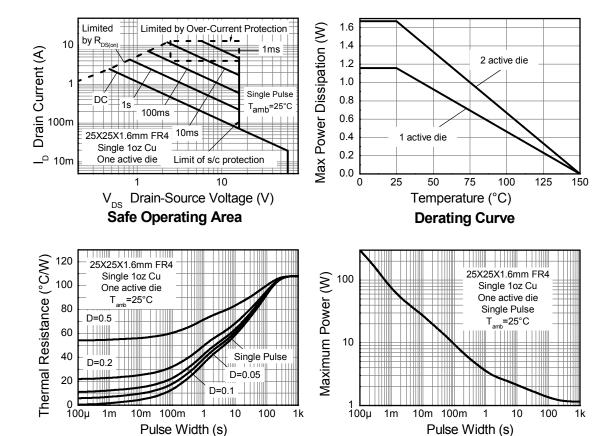


Recommended Operating Conditions

The ZXMS6006DT8Q is optimized for use with µC operating from 3.3V and 5V supplies.

Characteristic	Symbol	Min	Max	Unit
Input Voltage Range	V _{IN}	0	5.5	V
Ambient Temperature Range	T _A	-40	+125	°C
High Level Input Voltage for MOSFET to be on	V _{IH}	3	5.5	V
Low Level Input Voltage for MOSFET to be off	V _{IL}	0	0.7	V
Peripheral Supply Voltage (voltage to which load is referred)	V_P	0	16	V

Thermal Characteristics



Transient Thermal Impedance

Pulse Power Dissipation





Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Static Characteristics	Static Characteristics					•
Drain-Source Clamp Voltage	V _{DS(AZ)}	60	65	70	V	I _D = 10mA
Off State Drain Current	I _{DSS}	_	_	1	μΑ	V _{DS} = 12V, V _{IN} = 0V
Oli State Dialii Culient		_	_	2		V _{DS} = 36V, V _{IN} = 0V
Input Threshold Voltage	V _{IN(th)}	0.7	1	1.5	V	$V_{DS} = V_{GS}$, $I_D = 1mA$
Input Current		_	60	100	μА	V _{IN} = +3V
Imput Current	I _{IN}	_	120	200		V _{IN} = +5V
Input Current while Over Temperature Active	-	_	_	400	μΑ	V _{IN} = +5V
Static Drain-Source On-State Resistance	В	_	85	125	mΩ	V _{IN} = +3V, I _D = 1A
Static Dialii-Source On-State Resistance	R _{DS(on)}	_	75	100		V _{IN} = +5V, I _D = 1A
Continuous Drain Current (Notes 5 & 9)	I _D	2.0	_	_	A	V _{IN} = 3V; T _A = +25°C
Continuous Diam Current (Notes 5 & 9)		2.2	_	_		V _{IN} = 5V; T _A = +25°C
Continuous Drain Current (Notes 5 & 8)		2.6	_	_		$V_{IN} = 3V; T_A = +25^{\circ}C$
Continuous Diain Current (Notes 5 & 6)		2.8	_	_		$V_{IN} = 5V; T_A = +25^{\circ}C$
Current Limit (Note 11)	I _{D(LIM)}	4	8	_	A	V _{IN} = +3V
Current Limit (Note 11)		6	13	_		V _{IN} = +5V
Dynamic Characteristics						
Turn On Delay Time	t _{d(on)}	_	8.6	_	μs	
Rise Time	t _r	_	18	_	μs	V _{DD} = 12V. I _D = 1A. V _{GS} = 5V
Turn Off Delay Time	t _{d(off)}	_	34	_	μs	VDD - 12V, ID - 1A, VGS - 5V
Fall Time	f _f	_	15	_	μs	
Over-Temperature Protection						
Thermal Overload Trip Temperature (Note 12)	T_{JT}	+150	+175	_	°C	_
Thermal Hysteresis (Note 12)	f _f	_	+10	_	°C	_

Notes: 11. The drain current is restricted only when the device is in saturation (see graph 'typical output characteristic'). This allows the device to be used in the fully on state without interference from the current limit. The device is fully protected at all drain currents, as the low power dissipation generated outside saturation makes current limit unprecessary.

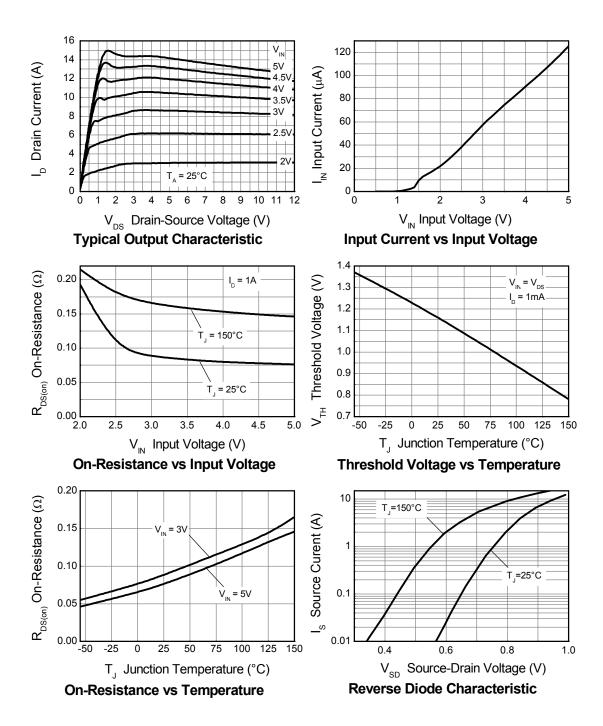
saturation makes current limit unnecessary.

12. Over-temperature protection is designed to prevent device destruction under fault conditions. Fault conditions are considered as "outside" normal operating range, so this part is not designed to withstand over-temperature for extended periods..





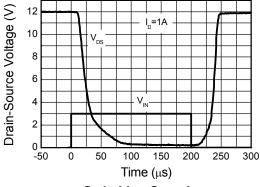
Typical Characteristics

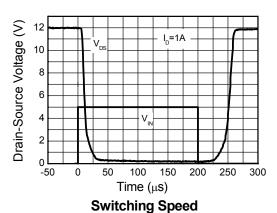




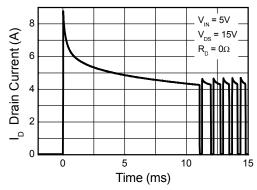


Typical Characteristics (cont.)





Switching Speed



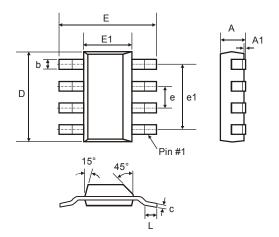
Typical Short Circuit Protection





Package Outline Dimensions

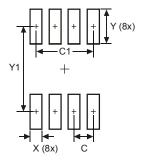
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SM-8					
Dim	Min	Max	Тур		
Α	-	1.7	-		
A1	0.02	0.1	_		
b	_	0.7	_		
С	0.24	0.32	_		
D	6.3	6.7	_		
е	_	_	1.53		
e1	_	_	4.59		
Е	6.7	7.3	_		
E1	3.3	3.7	-		
L	0.9	_	-		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	1.52		
C1	4.6		
Х	0.95		
Y	2.80		
Y1	6.80		





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