



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON) max}	Package	I _D T _A = +25°C
-25V	27mΩ @ $V_{GS} = -4.5V$	U-DFN2020-6	-6.7A
-25V	40mΩ @ V _{GS} = -1.8V	Type E	-5.4A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

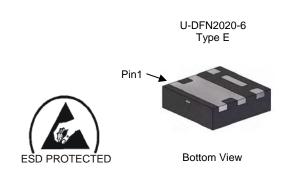
- Load Switching
- Battery Management Application
- Power Management Functions

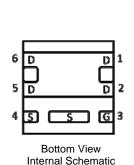
Features

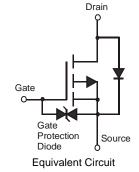
- Low R_{DS(ON)} Ensures on State Losses are Minimized
- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- ESD Protected Gate
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: U-DFN2020-6 Type E
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Weight: 0.001 grams (approximate)







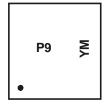
Ordering Information (Note 4)

Part Number	Case	Packaging		
DMP2039UFDE-7	U-DFN2020-6 Type E	3,000/Tape & Reel		

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



P9 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011) M = Month (ex: 9 = September)

Date Code Key

Year	201	1	2012		2013	20	14	2015		2016		2017
Code	Υ		Z		Α	[3	С		D		E
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units			
Drain-Source Voltage	V _{DSS}	-25	V			
Gate-Source Voltage			V _{GSS}	±8	V	
Continuous Durin Courset (Nato 5) V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-6.7 -5.3	А	
Continuous Drain Current (Note 5) V _{GS} = -4.5V	t<5s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-8.3 -6.6	А	
Continuous Drain Current (Note 5) V 4 9V	$T_A = +25$ °C $T_A = +70$ °C	I _D	-5.4 -4.3	А		
Continuous Drain Current (Note 5) V _{GS} = -1.8V	t<5s	$T_A = +25$ °C $T_A = +70$ °C	I _D	-6.6 -5.2	А	
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	-60	Α			
Continuous Source-Drain Diode Current	Is	-2.0	Α			

Thermal Characteristics

Characteristic		Symbol	Value	Units	
Total Bawar Discination (Note 5)	$T_A = +25$ °C	р	0.8	W	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	$T_A = +70^{\circ}C$		VV	
Thermal Begintenes, Junction to Ambient (Note 5)	Steady state	D.	160	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<5s	$R_{\theta JA}$	104	C/VV	
Total Bawar Discination (Note 6)	$T_A = +25$ °C	D-	2.0	W	
Total Power Dissipation (Note 6)	$T_A = +70$ °C	P _D	2.9		
Thermal Begintenes, Junction to Ambient (Note 6)	Steady state	D.	63	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<5s	$R_{\theta JA}$	42	*C/VV	
Thermal Resistance, Junction to Case (Note 6)	Steady state	$R_{ heta JC}$	10.8	°C/W	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BV _{DSS}	-25	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$		
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -25V, V_{GS} = 0V$		
Gate-Source Leakage	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 8.0 V, V_{DS} = 0 V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	V _{GS(th)}	-0.4	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$		
		_	20	27		$V_{GS} = -4.5V, I_D = -6.4A$		
Static Drain-Source On-Resistance	D	_	24	34	0	$V_{GS} = -2.5V, I_D = -4.8A$		
Static Drain-Source On-Resistance	R _{DS (ON)}	_	28	40	mΩ	$V_{GS} = -1.8V, I_D = -2.5A$		
		_	33	70		$V_{GS} = -1.5V, I_D = -1.5A$		
Forward Transfer Admittance	Y _{fs}	_	16	_	S	$V_{DS} = -5V, I_{D} = -4A$		
Diode Forward Voltage	V _{SD}	_	-0.7	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$		
DYNAMIC CHARACTERISTICS (Note 8)			•	•	•			
Input Capacitance	Ciss	_	2530	_	pF	45)/)/ 0)/		
Output Capacitance	Coss	_	203	_	pF	$V_{DS} = -15V, V_{GS} = 0V$ -f = 1.0MHz		
Reverse Transfer Capacitance	Crss	_	177	_	pF	1 = 1.0lvin2		
Gate Resistance	R_g	_	9.1	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$		
Total Gate Charge (V _{GS} = -4.5V)	Q_g	_	28.2	_	nC			
Total Gate Charge (V _{GS} = -8V	Qq	_	48.7	_	nC	1, 45, 40,		
Gate-Source Charge	Q _{gs}	_	3.2	_	nC	$V_{DS} = -15V, I_{D} = -4.0A$		
Gate-Drain Charge	Q_{qd}	_	5.0	_	nC	1		
Turn-On Delay Time	t _{D(on)}	_	15.1	_	ns			
Turn-On Rise Time	t _r	_	23.5	_	ns	$V_{DD} = -15V, V_{GS} = -4.5V, R_{G} = 1\Omega,$		
Turn-Off Delay Time	t _{D(off)}	_	137.6	_	ns	I _D = -4.0A		
Turn-Off Fall Time	t _f	_	80.5	_	ns	1		

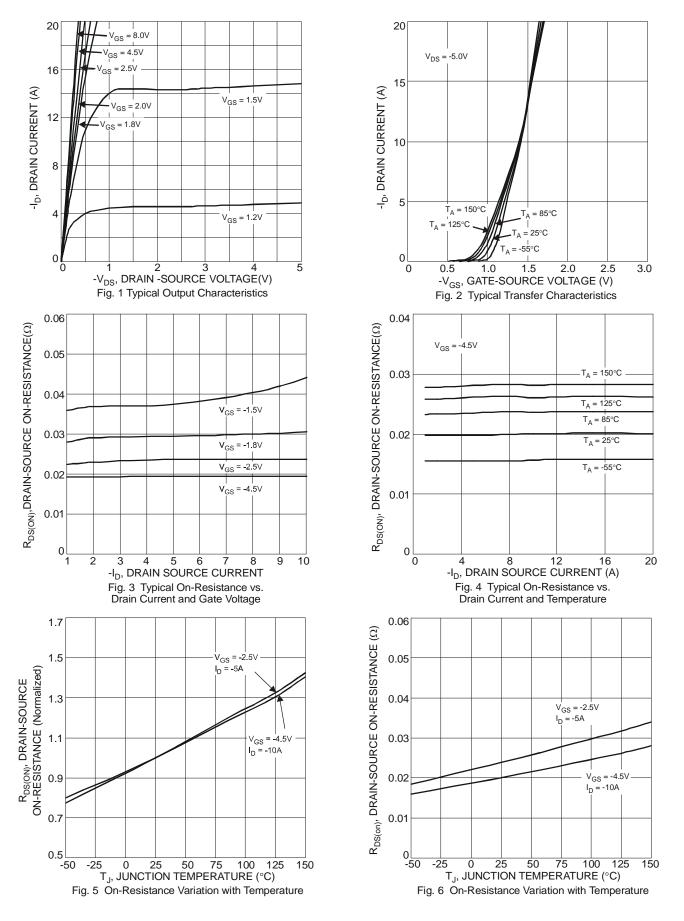
Notes:

2 of 6 DMP2039UFDE Document number: DS35420 Rev. 5 - 2

Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate
 Short duration pulse test used to minimize self-heating effect

^{8.} Guaranteed by design. Not subject to production testing.







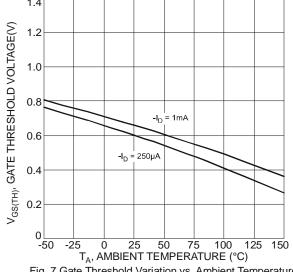
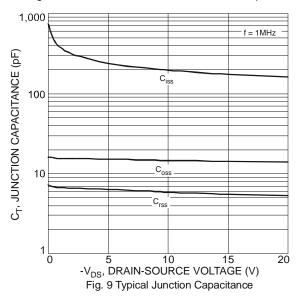
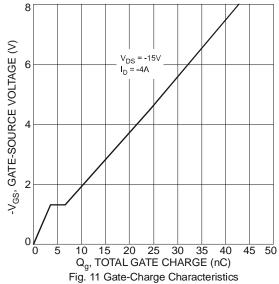
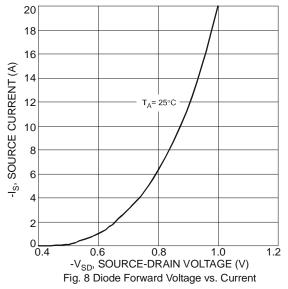


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







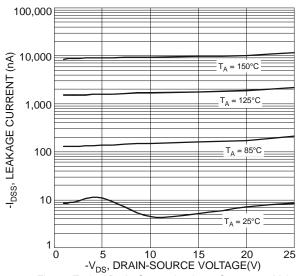
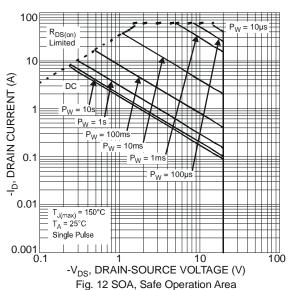
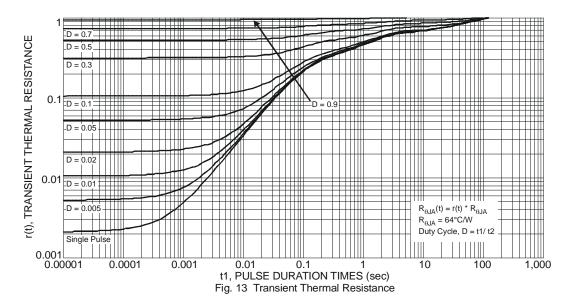


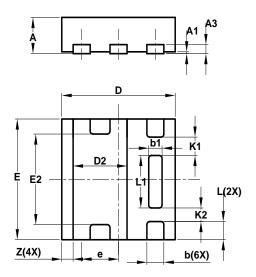
Fig. 10 Typical Drain-Source Leakage Current vs. Voltage





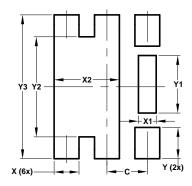


Package Outline Dimensions



U-DFN2020-6							
Type E							
Dim	Min Max Typ						
Α	0.57	0.63	0.60				
A1	0	0.05	0.03				
A3	_	_	0.15				
b	0.25	0.35	0.30				
b1	0.185	0.285	0.235				
D	1.95	2.05	2.00				
D2	0.85	1.05	0.95				
Е	1.95	2.05	2.00				
E2	1.40	1.60	1.50				
е	_	_	0.65				
L	0.25	0.35	0.30				
L1	0.82	0.92	0.87				
K1	_	_	0.305				
K2	_	_	0.225				
Z	_	_	0.20				
All Dimensions in mm							

Suggested Pad Layout



Dimensions	Value			
Dilliensions	(in mm)			
С	0.650			
Х	0.400			
X1	0.285			
X2	1.050			
Y	0.500			
Y1	0.920			
Y2	1.600			
Y3	2.300			



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DMP2039UFDE
Document number: DS35420 Rev. 5 - 2

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