



D1213A-02WL

2 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Features

- IEC 61000-4-2 (ESD): Air ±15kV, Contact ±8kV
- 2 Channels of ESD protection
- Low Channel Input Capacitance
- Typically Used at High Speed Ports such as USB 2.0, IEEE1394, Serial ATA, DVI, HDMI, PCI
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Weight: 0.006 grams (Approximate)

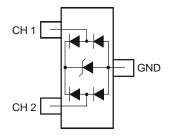
PRODUCT

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SOT323

Top View



Device Schematic

Ordering Information (Note 4)

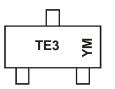
Part Number	Case	Packaging
D1213A-02WL-7	SOT323	3,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



TE3 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Z = 2012) M = Month (ex: 9 = September)

Date Code Key

Notes:

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Year	2011	1	2012		2013	20	14	2015		2016	2	2017
Code	Y		Z		А	E	3	С		D		E
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current (Note 7)	IPP	5	А	8/20µs, Per Fig. 2
ESD Protection – Contact Discharge	V _{ESD_Contact}	±8	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V _{ESD_Air}	±15	kV	Standard IEC 61000-4-2

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	PD	200	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	625	°C/W
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	T _{STG}	-65 to +150	°C

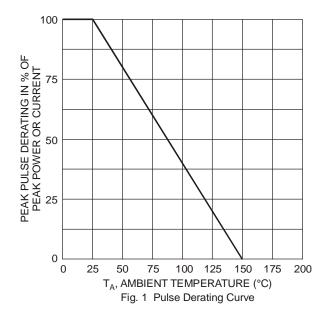
Electrical Characteristics @T_A = 25°C unless otherwise specified

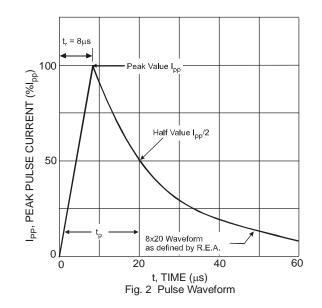
Characteristic (Note 7)	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse working voltage	VRWM	-	-	3.3	V	-
Reverse current (Note 6)	I _R	-	0.1	1.0	μA	$V_R = V_{RWM} = 3.3V$
Reverse breakdown voltage	VBR	6.0	7.5	9.0	V	$I_R = 1mA$
Forward voltage	VF	0.6	0.8	0.95	V	$I_F = 8mA$
Reverse clamping voltage, Positive Transients	V _{CL1}	-	10.0	-	V	$I_{PP} = 1A, t_p = 8/20 \mu s$
Reverse clamping voltage, Negative Transients	V _{CL2}	-	-1.7	-	V	I _{PP} = -1A, t _p = 8/20µs
Dynamic resistance	R _{DYN}	-	0.9	-	Ω	$I_R = 1A, t_p = 8/20 \mu s$
Capacitance	CT	-	0.85	1.2	pF	V _R = 1.65V, f = 1MHz

Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.

6. Short duration pulse test used to minimize self-heating effect.

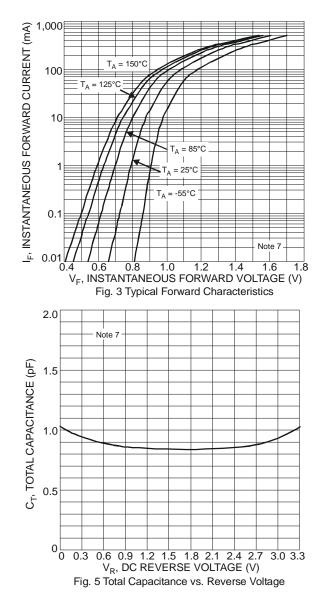
7. Measured between any channel and GND

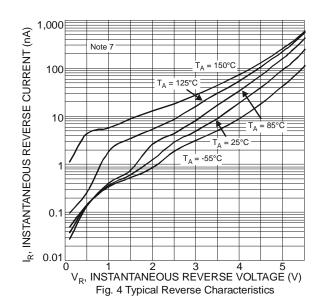




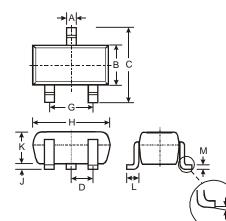


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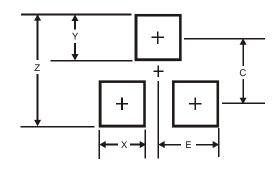
Package Outline Dimensions



	SOT323						
Dim	Min	Max	Тур				
Α	0.25	0.40	0.30				
В	1.15	1.35	1.30				
С	2.00	2.20	2.10				
D	-	-	0.65				
G	1.20	1.40	1.30				
Н	1.80	2.20	2.15				
J	0.0	0.10	0.05				
K	0.90	1.00	1.00				
L	0.25	0.40	0.30				
М	0.10	0.18	0.11				
α	0°	8°	-				
All	All Dimensions in mm						



Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.8
Х	0.7
Y	0.9
С	1.9
E	1.0

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