DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _{D MAX} T _A = +25°C
-20V	$90m\Omega$ @ $V_{GS} = -4.5V$	-3.2A
-20V	$137m\Omega @ V_{GS} = -2.5V$	-2.6A

Description

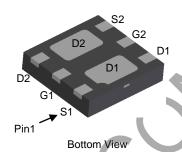
This MOSFET is designed to minimize on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Load Switch
- **Power Management Functions**
- Portable Power Adaptors

U-DFN2020-6 (Type B)



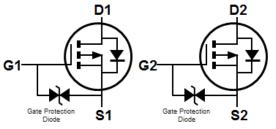


Features

- Low On-Resistance
- Low Input Capacitance
- Low Profile, 0.6mm Max Height
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: U-DFN2020-6 (Type B)
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe; Solderable per MIL-STD-202, Method 208 @4
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (Approximate)



Q1_P-CHANNEL MOSFET

Q2 P-CHANNEL MOSFET

Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2060UFDB -7	U-DFN2020-6 (Type B)	3,000/Tape & Reel
DMP2060UFDB -13	U-DFN2020-6 (Type B)	10,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. Notes

- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>
 4. For packaging details, go to our website athttps://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

U-DFN2020-6 (Type B)



FD = Product Type Marking Code YM = Date Code Marking Y = Year (ex: E = 2017)M = Month (ex: 9 = September)

Data Cada Kay

Date Code Rey												
Year	201	4	2015		2016	20	17	2018		2019	2	2020
Code	В		С		D			F		G		Н
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

DMP2060UFDB Document number: DS37422 Rev. 3 - 3

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DMP2060UFDB

Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	-20	V	
Gate-Source Voltage	V _{GSS}	±12	V	
Continuous Drain Current (Note 5) $V_{GS} = -4.5V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$		I _D	-3.2 -2.5	А
Maximum Continuous Body Diode Forward Current (Note 5)	Is	-1.5	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	-18	Α	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	Steady State	D-	1.4	W
Total Fower Dissipation (Note 5)	t < 5s	P _D	2.2	VV
Thermal Desigtance, Junction to Ambient (Note 5)	Steady State		92	
Thermal Resistance, Junction to Ambient (Note 5)	t < 5s	R_{θ} JA	55	°C/W
Thermal Resistance, Junction to Case (Note 5)		$R_{ heta JC}$	30	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics P-CHANNEL (@TA = +25°C, unless otherwise specified.)

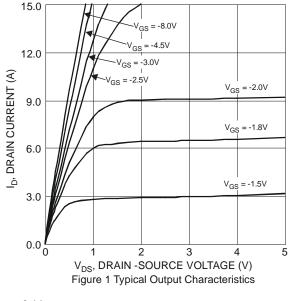
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20			V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	1		-1.0	μA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	1	_	±10	μΑ	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(TH)}	-0.35		-1.4	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance			59	90	mΩ	$V_{GS} = -4.5V$, $I_D = -2.9A$
Static Drain-Source On-Resistance	R _{DS(ON)}		76	137	11122	V _{GS} = -2.5V, I _D = -2.3A
Diode Forward Voltage	V _{SD}	_	-0.65	-1.2	V	V _{GS} = 0V, I _S = -3.0A
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C _{iss}	_	881		pF	.,
Output Capacitance	Coss	_	84		pF	$V_{DS} = -10V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	67	_	pF	1 = 1.000112
Gate Resistance	Rg	_	14.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = -4.5V)		_	11	_	nC	
Total Gate Charge (V _{GS} = -8V)	Qg	_	18	_	nC	101/ 1 0.74
Gate-Source Charge	Q _{gs}	_	1.5	_	nC	$V_{DS} = -10V, I_{D} = -3.7A$
Gate-Drain Charge	Q _{gd}	_	2.3	_	nC]
Turn-On Delay Time	t _{D(ON)}	_	5.0	_	ns	
Turn-On Rise Time	t _R	_	9.5	_	ns	$V_{DD} = -10V, V_{GS} = -4.5V,$
Turn-Off Delay Time	t _{D(OFF)}	_	29.7	_	ns	$R_L = 3.3\Omega$, $R_g = 1\Omega$
Turn-Off Fall Time	t _F	_	20.4	_	ns	1
Body Diode Reverse Recovery Time	t _{RR}	_	23.6	_	ns	$I_S = -3.0A$, $dI/dt = 100A/\mu s$
Body Diode Reverse Recovery Charge	Q _{RR}	_	11.4		nC	I _S = -3.0A, dI/dt = 100A/µs

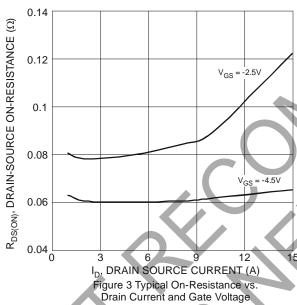
Notes: 5. Device mounted on on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.

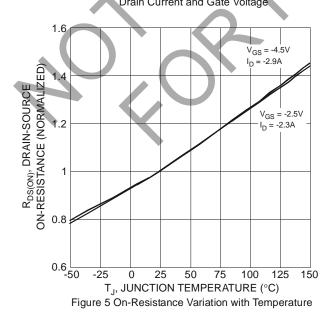
6. Short duration pulse test used to minimize self-heating effect

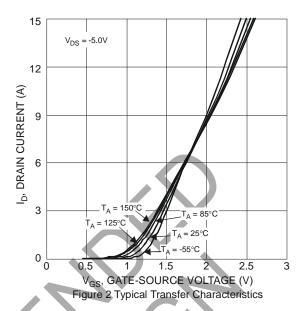
7. Guaranteed by design. Not subject to product testing.

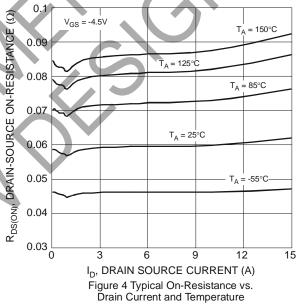


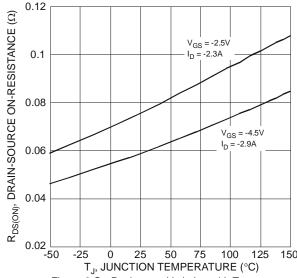














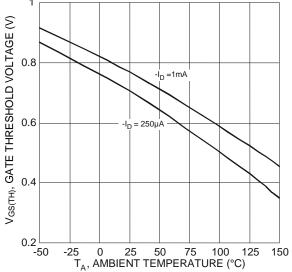
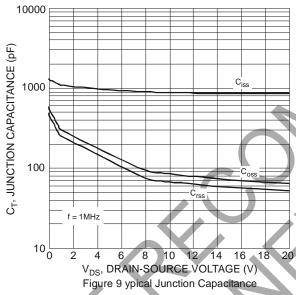
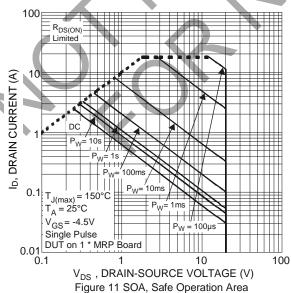
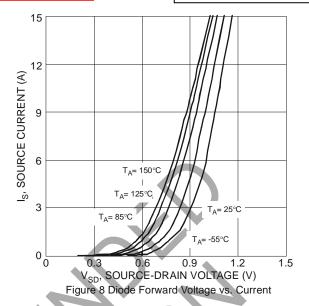
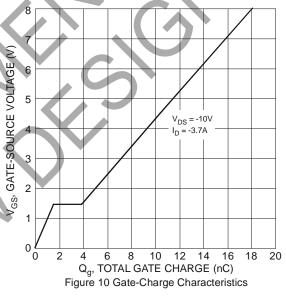


Figure 7 Gate Threshold Variation vs. Ambient Temperature

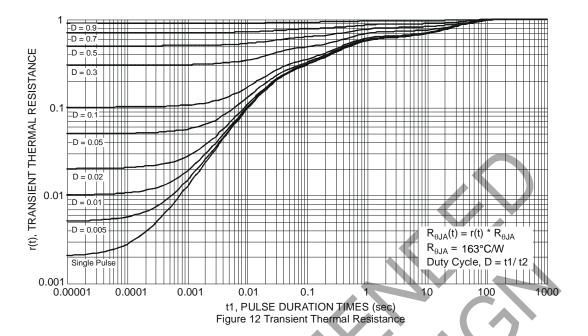








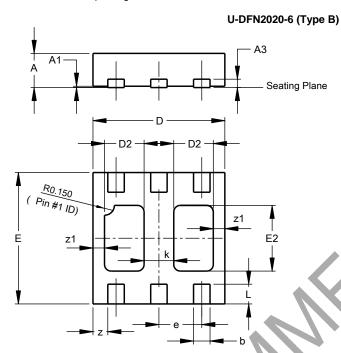






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

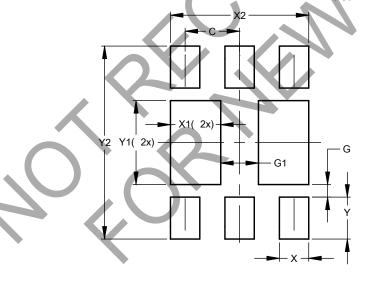


U-DFN2020-6							
(Type B)							
Dim	Min Max Typ						
Α	0.545	0.605	0.575				
A1	0.00	0.05	0.02				
A3		-	0.13				
b	0.20	0.30	0.25				
D	1.95	2.075	2.00				
D2	0.50	0.70	0.60				
е	_	-	0.65				
Е	1.95	2.075	2.00				
E2	0.90	1.10	1.00				
k	į		0.45				
	0.25	0.35	0.30				
Z	1	-	0.225				
z1	-	-	0.175				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type B)



Dimensions	Value
Dillielisions	(in mm)
С	0.650
G	0.150
G1	0.450
X	0.350
X1	0.600
X2	1.650
Y	0.500
Y1	1.000
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