



DMN3033LSN

### N-CHANNEL ENHANCEMENT MODE MOSFET

## **Features**

- Low Gate Charge
  - Low R<sub>DS(ON)</sub>:
  - 30mΩ @V<sub>GS</sub> = 10V
  - 40mΩ @V<sub>GS</sub> = 4.5V
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

## **Mechanical Data**

- Case: SC59
- Case Material Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (c3)
- Terminal Connections: See Diagram
- Weight: 0.014 grams (Approximate)

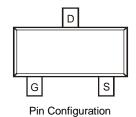




Top View



Equivalent Circuit



### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3033LSN-7	SC59	3000/Tape & Reel

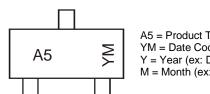
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



A5 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Key

Year	2007	-	•	2016	2017	20	18	2019	2020	20	21	2022
Code	U	~	-	D	E	F	-	G	Н		I	J
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	30	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 5) $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		6 5	А
Pulsed Drain Current (Note 6)	I <sub>DM</sub>	24	A
Body-Diode Continuous Current (Note 5)	ls	2.25	А

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	1.4	W
Thermal Resistance, Junction to Ambient (Note 5) t ≤10s	$R_{ heta}$ JA	90	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	۵°

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
STATIC PARAMETERS							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	—		V	$I_D = 250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current $T_J = +25$ $T_J = +55$	IDCC	_	_	1 5	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Body Leakage Current	I <sub>GSS</sub>	_	_	±100	nA	$V_{DS} = 0V, V_{GS} = \pm 20V$	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.0	_	2.1	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	
Static Drain-Source On-Resistance (Note 7)	R <sub>DS(ON)</sub>	_	25 36	30 40	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 5A	
Forward Transconductance (Note 7)	<b>g</b> fs	_	5	—	S	$V_{DS} = 10V, I_D = 8A$	
Diode Forward Voltage (Note 7)	V <sub>SD</sub>	_	0.7	1.1	V	I <sub>S</sub> = 2.25A, V <sub>GS</sub> = 0V	
DYNAMIC PARAMETERS (Note 8)						·	
Total Gate Charge	Qg	_	10.5		nC	$V_{GS} = 5V, V_{DS} = 15V, I_D = 6A$	
Gate-Source Charge	Qgs	_	3.8	—	nC	$V_{GS} = 10V, V_{DS} = 15V, I_D = 6A$	
Gate-Drain Charge	Q <sub>gd</sub>	_	2.9	—	nC	$V_{GS} = 10V, V_{DS} = 15V, I_D = 6A$	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	11	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	7	—	ns	$V_{DD} = 15V, V_{GS} = 10V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	63	_	ns	$R_D = 1.8\Omega, R_G = 6\Omega$	
Turn-Off Fall Time	tF	_	30	_	ns	]	
Input Capacitance	C <sub>iss</sub>	_	755	_	pF		
Output Capacitance	Coss	_	136	_	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	108		pF		

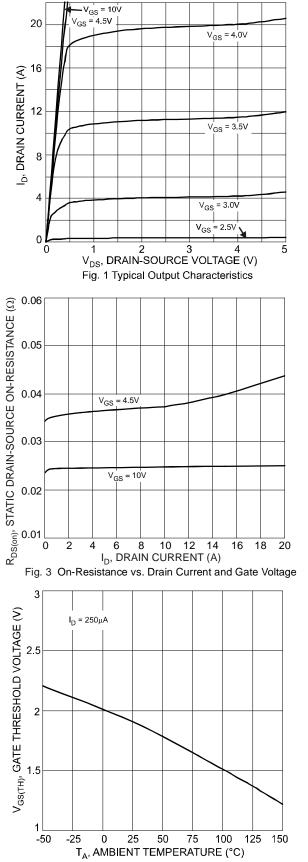
Notes: 5. Device mounted on 1"x1", FR-4 PC board with 2 oz. Copper and test pulse width t  $\leq$ 10s.

6. Repetitive Rating, pulse width limited by junction temperature.

7. Test pulse width t = 300ms.

8. Guaranteed by design. Not subject to production testing.





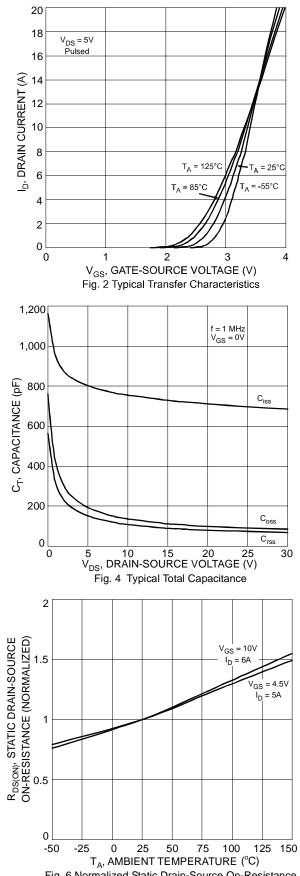
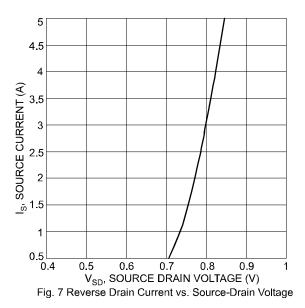


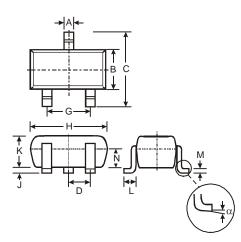
Fig. 6 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature





## **Package Outline Dimensions**

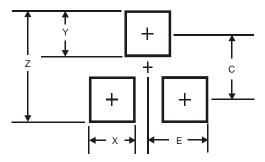
Please see http://www.diodes.com/package-outlines.html for the latest version.



8050							
SC59							
Dim	Min	Max	Тур				
Α	0.35	0.50	0.38				
В	1.50	1.70	1.60				
С	2.70	3.00	2.80				
D	-	-	0.95				
G	-	-	1.90				
Н	2.90	3.10	3.00				
J	0.013	0.10	0.05				
Κ	1.00	1.30	1.10				
L	0.35	0.55	0.40				
М	0.10	0.20	0.15				
Ν	0.70	0.80	0.75				
α	0°	8°	-				
All	All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
Z	3.4
Х	0.8
Y	1.0
С	2.4
E	1.35



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