



DMN3025LFDF

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
	20.5mΩ @ V <sub>GS</sub> = 10V	8.3A
30V	30mΩ @ V <sub>GS</sub> = 4.5V	7.4A

## **Description and Applications**

This new generation MOSFET has been designed to minimize the onstate resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions

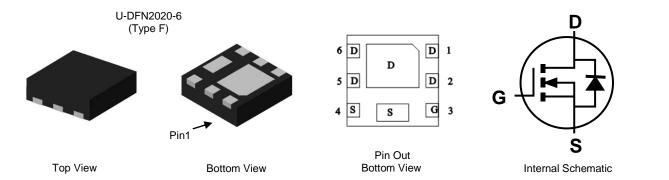
#### **30V N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Features and Benefits**

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm<sup>2</sup>
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: U-DFN2020-6 (Type F)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 <sup>(4)</sup>
- Weight: 0.0065 grams (Approximate)



#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3025LFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel
DMN3025LFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.htmlfor more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**



S6 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016) M = Month (ex: 9 = September)

#### Date Code Key

Notes:

Year	2016		2017	2018		2019	2020		2021	2022		2023
Code	D		E	F		G	H			J		K
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units		
Drain-Source Voltage		V <sub>DSS</sub>	30	V		
Gate-Source Voltage		V <sub>GSS</sub>	±20	V		
	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	8.3 6.6	А	
Continuous Drain Current (Note 6) $V_{GS} = 10V$	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	۱ <sub>D</sub>	9.9 7.9	А	
Maximum Continuous Body Diode Forward Currer	nt (Note 6)		ls	3	A	
Pulsed Drain Current (10µs pulse, duty cycle = 1%	5)	I <sub>DM</sub>	40	A		
Avalanche Current (L = 0.1mH) (Note 7)		I <sub>AS</sub>	15	A		
Avalanche Energy (L = 0.1mH) (Note 7)			E <sub>AS</sub>	11	mJ	

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Total Dawar Dissinction (Nata 5)	T <sub>A</sub> = +25°C	D	0.66	W	
Total Power Dissipation (Note 5)	T <sub>A</sub> = +70°C	PD	0.42		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Р	173	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{ extsf{ heta}JA}$	133	C/VV	
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	D-	2.1	W	
Total Fower Dissipation (Note 0)	$T_A = +70^{\circ}C$	PD	1.3	vv	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Р	62	°C/W	
Thermal Resistance, Junction to Ambient (Note 0)	t<10s	$R_{ extsf{ heta}JA}$	43	C/VV	
Thermal Resistance, Junction to Case (Note 6)	Steady State	$R_{\theta JC}$	9.4	°C/W	
Operating and Storage Temperature Range		T <sub>J.</sub> T <sub>STG</sub>	-55 to +150	°C	

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

		1				
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)			-			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	-	-	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	-	1	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.0	-	2.0	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$
Static Drain-Source On-Resistance	Passa	-	-	20.5	mΩ	$V_{GS} = 10V, I_D = 7A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	-	-	30	1112.2	$V_{GS} = 4.5V, I_D = 7A$
Diode Forward Voltage	V <sub>SD</sub>	-	0.70	1.0	V	$V_{GS} = 0V, I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	-	641	-		
Output Capacitance	C <sub>oss</sub>	-	66	-	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	-	50	-		1 = 1.00012
Gate Resistance	Rg	-	2.2	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	-	6	-		
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	-	13.2	-	nC	
Gate-Source Charge	Q <sub>gs</sub>	-	1.7	-	no	$V_{DS} = 15V, I_D = 10A$
Gate-Drain Charge	Q <sub>gd</sub>	-	2.2	-		
Turn-On Delay Time	t <sub>D(ON)</sub>	-	3.3	-		
Turn-On Rise Time	t <sub>R</sub>	-	4.4	-		$V_{DD} = 15V, V_{GS} = 10V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	-	22.3	-	ns	$R_G = 6\Omega$ , $I_D = 1A$
Turn-Off Fall Time	t <sub>F</sub>	-	5.3	-		
Reverse Recovery Time	t <sub>RR</sub>	-	11.4	-	ns	I <sub>F</sub> = 11A, di/dt = 100A/µs
Reverse Recovery Charge	Q <sub>RR</sub>	-	8.2	-	nC	I <sub>F</sub> = 11A, di/dt = 100A/µs

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

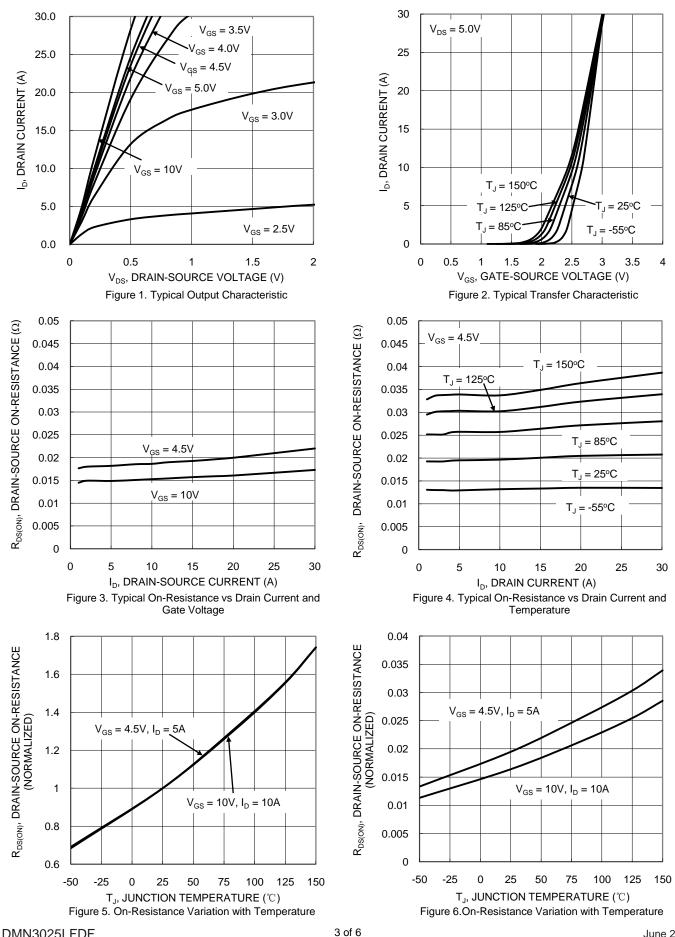
7.  $I_{AS}$  and  $E_{AS}$  rating are based on low frequency and duty cycles to keep  $T_J = +25^{\circ}C$ .

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.



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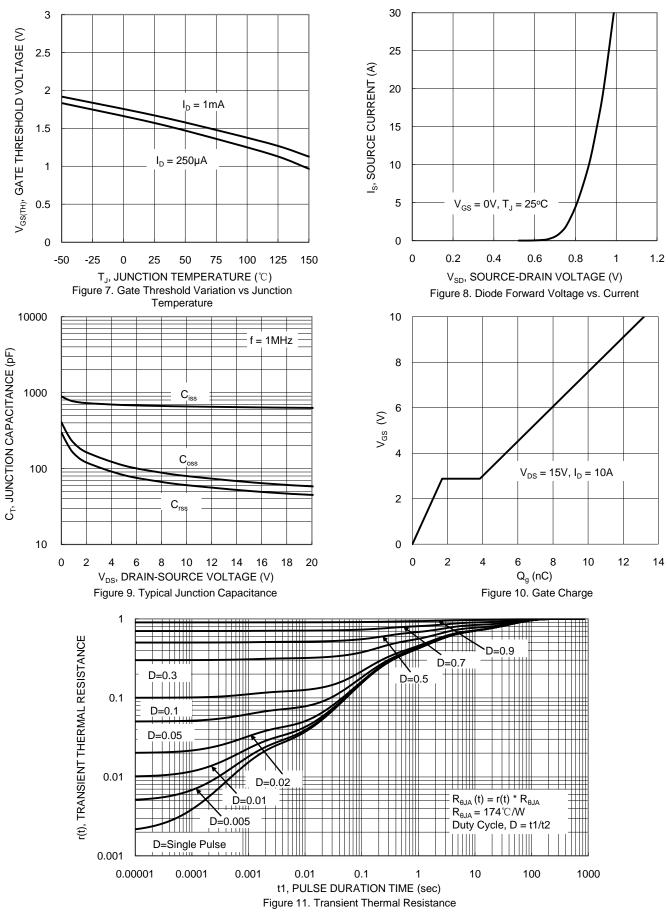


DMN3025LFDF Datasheet number: DS37737 Rev. 1 - 2

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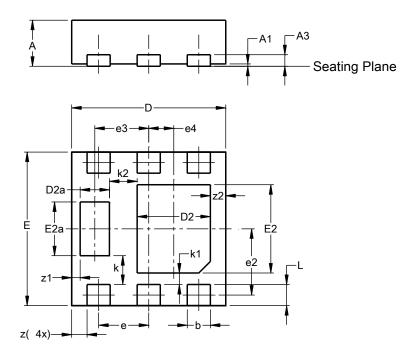




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN2020-6 (Type F)

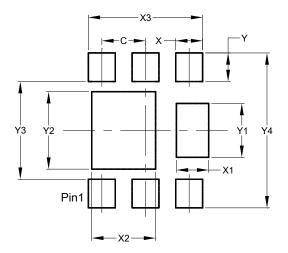


U-DFN2020-6								
(Type F)								
Dim	Min	Max	Тур					
Α	0.57	0.63	0.60					
A1	0.00	0.05	0.03					
A3	-	-	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D2a	0.33	0.43	0.38					
Е	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E2a	0.65 0.75 0.70							
е	0.65 BSC							
e2	C	).863 BS	SC					
e3		0.70 BS	С					
e4	C	).325 BS	SC					
k	0.37 BSC							
k1	0.15 BSC							
k2	0.36 BSC							
L	0.225 0.325 0.275							
z	0.20 BSC							
z1	C	).110 BS	SC					
z2	0.20 BSC							
All C	Dimens	ions in	mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN2020-6 (Type F)



Dimensions	Value		
Dimensions	(in mm)		
С	0.650		
Х	0.400		
X1	0.480		
X2	0.950		
X3	1.700		
Y	0.425		
Y1	0.800		
Y2	1.150		
Y3	1.450		
Y4	2.300		



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