

Product Summary (@T_A = +25°C)

V _{RRM} (V)	I _O (A)	V _{FMAX} (V)	Ι _{RMAX} (μΑ)
800	1	1.1	10

Features and Benefits

- Ideally Suited for Automated Assembly
 Exposed Heatsink on Device Underside Provides Excellent Thermal Performance
- Glass Passivated Die Construction
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Package: PowerDI123
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.01 grams (Approximate)



Ordering Information (Note 4)

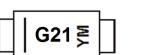
Part Number	Marking Code	Paakaga	Packing		
Fait Nulliber	Marking Code	Package	Qty.	Carrier	
S1KP1M-7	G21	PowerDI123	3000	Tape & Reel	

EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



G21 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: K = 2023)

M = Month (ex: 3 = March)

Date Code Key

Notes:

Date Obde Rey												
Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	К	L	М	Ν	0	Р	R	S	Т	U	V
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
	Jan	. 0.0		7.0	-	oun	-	, tug	000	-		-
Code	1	2	3	4	5	6	7	8	9	0	N	D

Description and Applications

Packaged in the compact thermally efficient PowerDI[®]123 package, the S1KP1M provides high surge capacity and high efficiency. It is ideally suited to be used in:

- AC-DC adaptors/chargers
- DC-DC converters
- Power supplies



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} Vrwm Vr	800	V
RMS Reverse Voltage	VR(RMS)	560	V
Average Rectified Output Current (See Figure 4)	lo	1.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	IFSM	25	A

Thermal Characteristics

Characteristic	Symbol	Typical	Maximum	Unit
Thermal Resistance, Junction to Ambient Air (Note 5)	Reja	134	—	°C/W
Thermal Resistance, Junction to Soldering Point (Note 6)	R _{0JS}	—	6	°C/W
Operating and Storage Temperature Range	TJ, TSTG	_	-55 to +150	°C

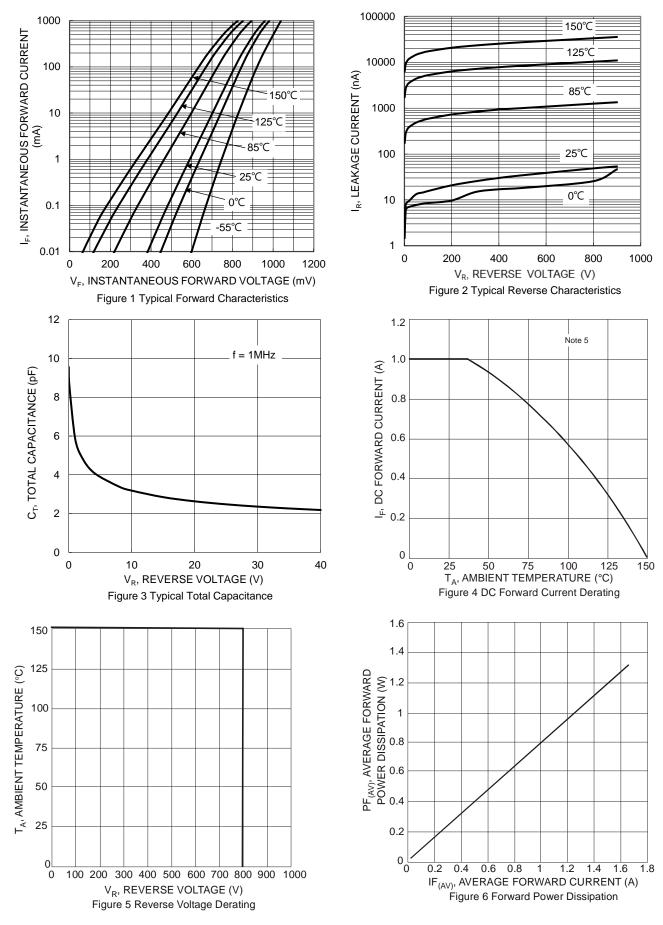
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Breakdown Voltage (Note 7)	V(BR)R	800	—	—	V	$I_R = 10 \mu A$
		_	0.64	_		$I_F = 1.0 \text{mA}, T_J = 0^{\circ}\text{C}$
		_	0.60	_		IF = 1.0mA, TJ = +25°C
Forward Voltage Drop	VF	—	0.46		V	IF = 1.0mA, TJ = +85°C
		—	0.96	1.1		IF = 1.0A, TJ = +25°C
		—	0.85	1.0		I _F = 1.0A, T _J = +125°C
Reverse Leakage Current (Note 7)		_	_	10	μA	V _R = 800V, T _J = +25°C
Reverse Leakage Current (Note 7)	IR	—	—	150	μΑ	V _R = 800V, T _J = +125°C
Reverse Recovery Time	trr	_	1.5	—	μs	IF = 0.5A, IR = 1A, IRR = 0.25A
Total Capacitance	Ст	_	4	_	pF	$V_R = 4.0 V_{DC}$, f = 1MHz

5. Device mounted on 1inch x 1inch, FR-4 PCB; 2oz Cu pad layout as shown on Diodes Incorporated's suggested pad layout document. $T_A = +25^{\circ}$ C. 6. Theoretical R_{0JS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction. 7. Short duration test pulse used to minimize self-heating effect. Notes:



S1KP1M



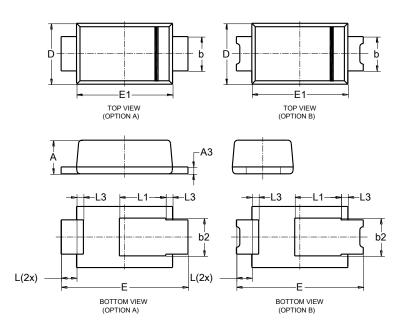
3 of 5 Downloaded From Oneyac.com



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123

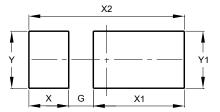


P								
	PowerDI123							
Dim	Min	Max	Тур					
Α	0.93	1.00	0.98					
A3	0.15	0.25	0.20					
b	0.85	1.25	1.00					
b2	1.025	1.125	1.10					
D	1.63	1.93	1.78					
E	3.50	3.90	3.70					
E1	2.60	3.00	2.80					
L	0.40	0.50	0.45					
L1	1.25	1.40	1.35					
L3	0.125	0.275	0.20					
All I	All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI123



Dimensions	Value (in mm)
G	0.65
X	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50



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