

30V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DS}	R _{DS(ON) max}	Package	I _{D max} T _A = +25°C	
201/	72mΩ @ V _{GS} = -10V	SOT-23	-3.9A	
-30V	85mΩ @ V _{GS} = -4.5V	501-23	-3.6A	

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

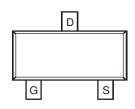
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic.
 - UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Lead-Free Plating (Matte Tin Finish Annealed over Alloy 42 Leadframe).
- Terminals: Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)

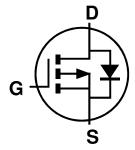
SOT23



Top View



Top View Pin Configuration



Equivalent Circuit

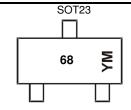
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP3068L-7	SOT23	3,000/Tape & Reel
DMP3068L-13	SOT23	10,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. Notes:

- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information



68 = Product Type Marking Code YM = Date Code Marking $Y \text{ or } \overline{Y} = \text{Year (ex: B} = 2014)$ M or \overline{M} = Month (ex: 9 = September)

Date Code Key

Year	2014	4	2015		2016	20	17	2018		2019	- 2	2020
Code	В		С		D	[F		G		Н
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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Maximum Ratings (@ $T_A = +25$ °C unless otherwise specified.)

Characterist	tic	Symbol	Value	Units	
Drain-Source Voltage		V_{DSS}	-30	V	
Gate-Source Voltage		V _{GSS}	±12	V	
Dunin Coursent (Nata C) Van 10V	Steady State	T _A = +25 °C T _A = +70 °C	I _D	-3.3 -2.6	А
Drain Current (Note 6) Vgs= -10V	t<10s	T _A = +25 °C T _A = +70 °C	I _D	-3.9 -3.2	А
Pulsed Drain Current (Pulse width ≤10µS, Du	ty Cycle ≤1%)	I _{DM}	-18	Α	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_{D}	0.7	W
Thermal Resistance, Junction to Ambient (Note 5) Steady State t<10s		$R_{ hetaJA}$	182 133	°C/W
Total Power Dissipation (Note 6)		P _D	1.2	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State t<10s	$R_{ hetaJA}$	103 75	°C/W
Operating and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to +150	℃

Electrical Characteristics (@TA = +25 °C unless otherwise specified.)

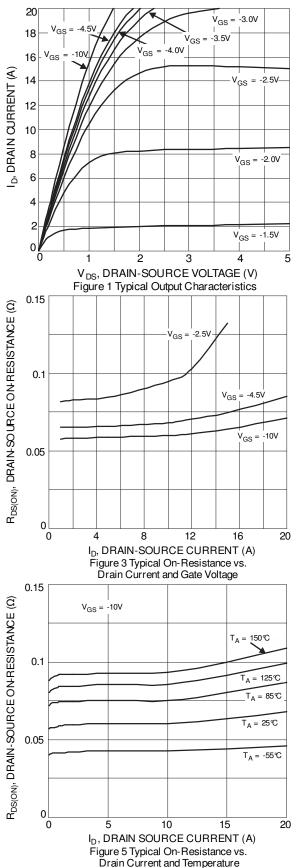
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV_{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		_	-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(th)}$	-0.5		-1.3	٧	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
			57	72		$V_{GS} = -10V$, $I_D = -4.2A$	
Static Drain-Source On-Resistance	Dag (a)		64	85	mΩ	$V_{GS} = -4.5V, I_D = -4.0A$	
Static Diani-Source On-Hesistance	R _{DS (ON)}	_	80	120	11122	$V_{GS} = -2.5V, I_D = -2.0A$	
			107	165		$V_{GS} = -1.8V, I_D = -1.0A$	
Diode Forward Voltage	V_{SD}	_	_	-1.2	V	$V_{GS} = 0V, I_{S} = -1.0A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}		708		pF		
Output Capacitance	Coss		57		рF	$V_{DS} = -15V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Reverse Transfer Capacitance	Crss		47	_	pF		
Gate Resistance	Rg		14	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Q_{G}	_	7.3	_	nC	$V_{DS} = -15V, I_{D} = -4A$	
Total Gate Charge (V _{GS} = -10V)	Q_{G}		15.9	_			
Gate-Source Charge	Q_{GS}	_	1.2	_	nC	$V_{DS} = -15V, I_{D} = -4A$	
Gate-Drain Charge	Q_{GD}		1.7	_			
Turn-On Delay Time	t _{d(on)}		3.5	_			
Rise Time	t _r	_	15.8	_	ns	$V_{DS} = -15V, V_{GS} = -10V,$	
Turn-Off Delay Time	t _{d(off)}		70.3	_	115	$I_D = -4A$, $R_G = 6.0\Omega$	
Fall Time	t _f		33.9	_			

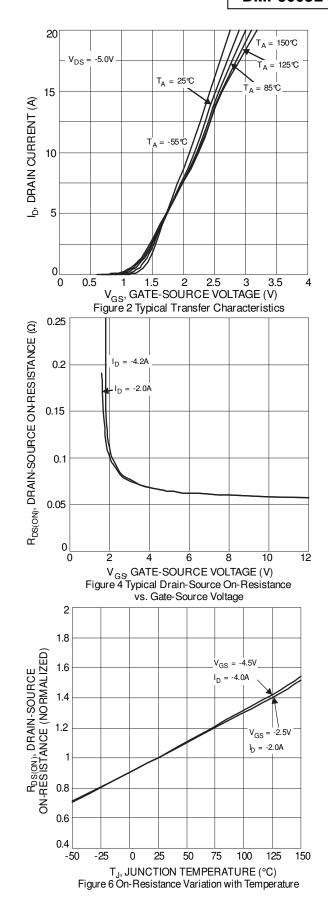
Notes:

- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1in. square copper plate.
 Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.

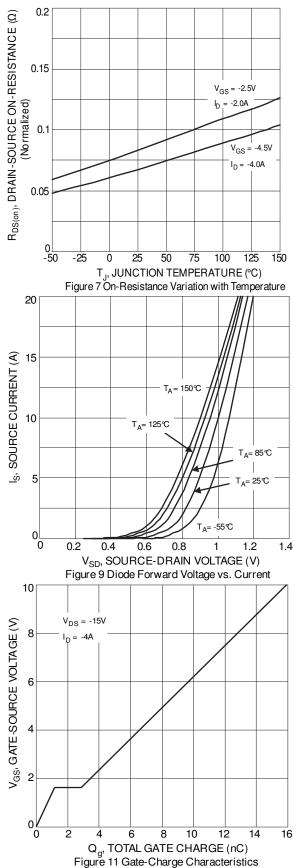
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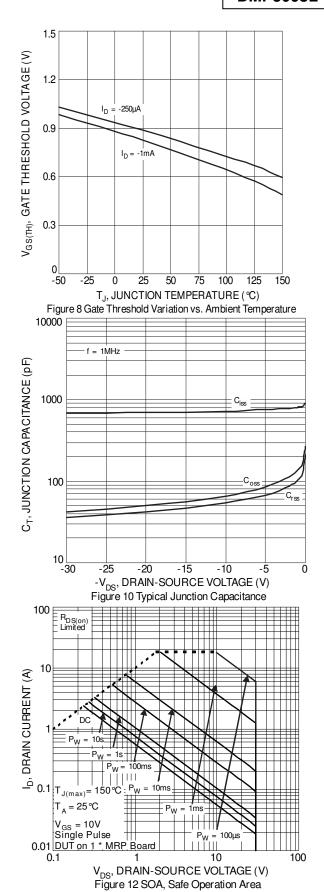














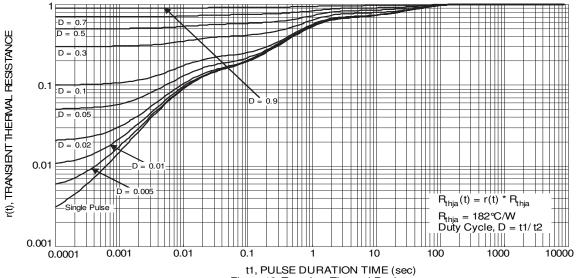
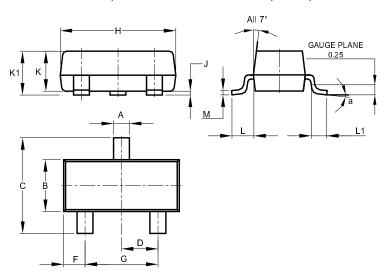


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

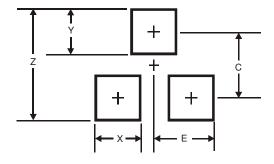
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest the version.



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
C	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	8°						
All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
X	0.8
Υ	0.9
С	2.0
E	1.35



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