



**DMP2110UVT** 

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
	150mΩ @ V <sub>GS</sub> = -4.5V	-1.8A
-20V	200mΩ @ V <sub>GS</sub> = -2.5V	-1.6A
	240mΩ @ V <sub>GS</sub> = -1.8V	-1.4A

## Description

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>), and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

## Applications

- General Purpose Interfacing Switch
- **Power Management Functions**

# Top View Pin-Out



Top View

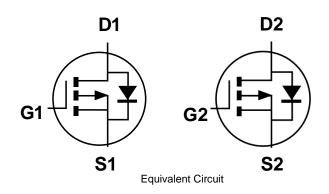


## **Features and Benefits**

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## **Mechanical Data**

- Case: TSOT26 •
- Case Material: Molded Plastic, "Green" Molding Compound. UL . Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (C3)
- Weight: 0.013 grams (Approximate)



## Ordering Information (Note 4)

	Part Number	Case	Packaging			
	DMP2110UVT-7	TSOT26	3,000/Tape & Reel			
DMP2110UVT-13		TSOT26	10,000/Tape & Reel			
Notes:	1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.					

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2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**

AF	ΥM	

AR7 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Ke	ey											
Year	2018	2019	20	020	2021	2022	2	2023	2024	202	25	2026
Code	F	G		H		J		К	L	N	1	Ν
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	-20	V	
Gate-Source Voltage		V <sub>GSS</sub>	±10	V
Drain Current (Note 5) Continuous	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	-1.8 -1.4	A
Pulsed Drain Current (10µs Pulse, Duty Cycle =	I <sub>DM</sub>	-15	A	
Body-Diode Continuous Current (Note 5)		IS	-0.7	A

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.74	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R <sub>0JA</sub>	168	°C/W
Total Power Dissipation (Note 6)		PD	0.74	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	1.01	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

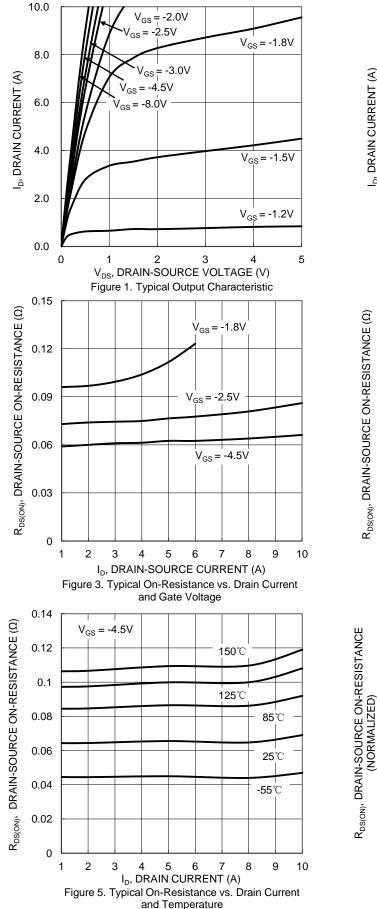
Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	—	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA		
Zero Gate Voltage Drain Current TJ = +25°C	I <sub>DSS</sub>		_	-1.0	μA	$V_{DS} = -16V, V_{GS} = 0V$		
Gate-Source Leakage	IGSS	_	—	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.45	—	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$		
			—	150		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2.8A		
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	—	200	mΩ	V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -2.0A		
	. ,		—	240		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -1.0A		
Diode Forward Voltage	V <sub>SD</sub>	_	—	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$		
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	Ciss		443	_	pF			
Output Capacitance	Coss		59	—	pF	−V <sub>DS</sub> = -6V, V <sub>GS</sub> = 0V −f = 1.0MHz		
Reverse Transfer Capacitance	Crss	_	47	_	pF	1 = 1.000112		
Gate Resistance	R <sub>G</sub>	_	8.5	_	Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$		
Total Gate Charge	Qg	_	6.0		nC			
Gate-Source Charge	Q <sub>gs</sub>	_	0.6	-	nC	$V_{GS} = -4.5V, V_{DS} = -10V, I_D = -3A$		
Gate-Drain Charge	Q <sub>qd</sub>	_	1.8		nC			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	4.0	-	ns			
Turn-On Rise Time	t <sub>R</sub>	_	3.7		ns	$V_{DS} = -10V, V_{GS} = -4.5V,$		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	24.5		ns	$R_L = 10\Omega, R_G = 1.0\Omega, I_D = -1A$		
Turn-Off Fall Time	tF		9.5		ns			
Reverse Recovery Time	t <sub>RR</sub>		8.3		ns	I <sub>F</sub> = -1.0A, di/dt = 100A/µs		
Reverse Recovery Charge	Q <sub>RR</sub>	_	2.0	_	nC	I <sub>F</sub> = -1.0A, di/dt = 100A/µs		

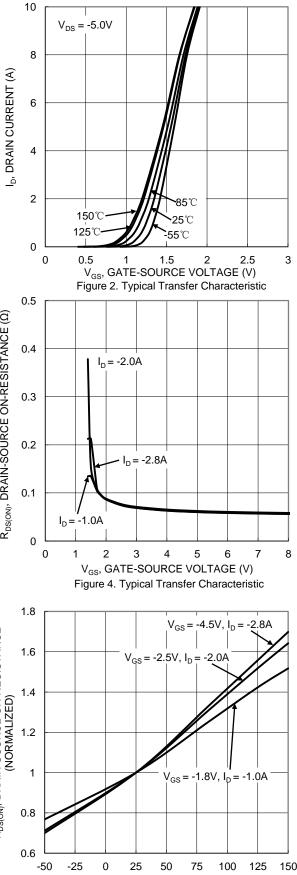
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.



## **DMP2110UVT**





T<sub>J</sub>, JUNCTION TEMPERATURE ( $^{\circ}$ C) Figure 6. On-Resistance Variation with Temperature

DMP2110UVT Document number: DS40891 Rev. 3 - 2



## **DMP2110UVT**

 $I_{D} = -1mA$ 

75

12

100

f = 1MHz

16

= 1ms

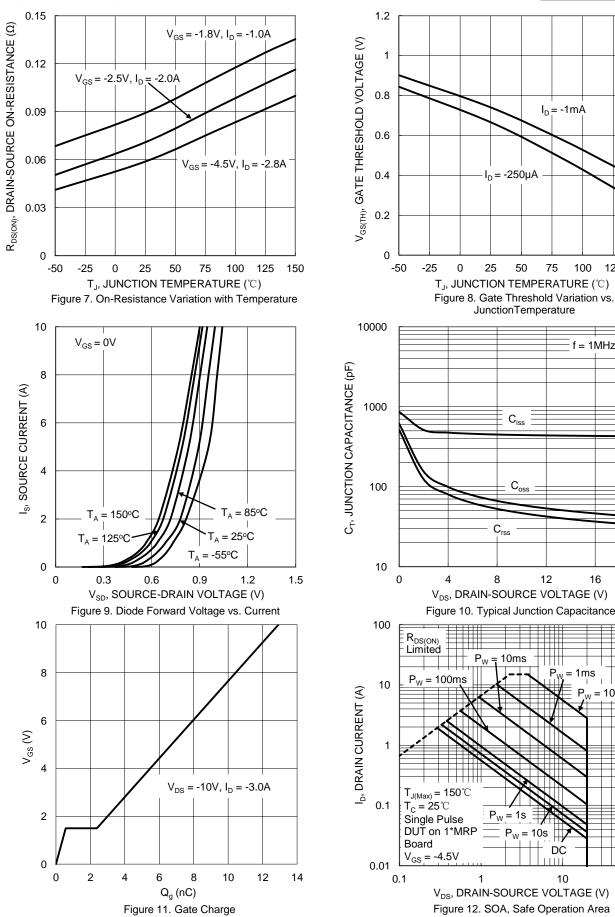
DC

10

20

125

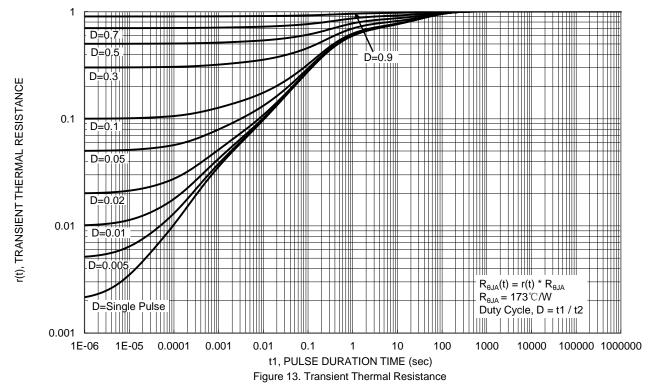
150



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100



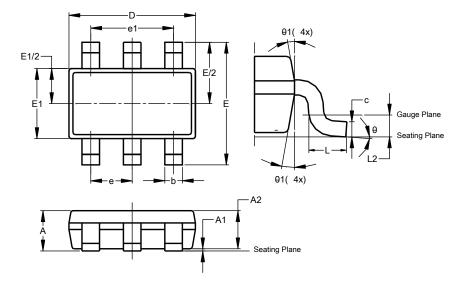




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

TSOT26

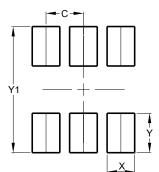


TSOT26						
Dim	Min Max Typ					
Α	-	1.00	-			
A1	0.010	0.100	-			
A2	0.840	0.900	-			
D	2.800	3.000	2.900			
Е	2	2.800 BS	С			
E1	1.500	500 1.700 1.60				
b	0.300	0.450	-			
С	0.120	0.200 –				
е	0.950 BSC					
e1	1	.900 BS	С			
L	0.30	0.30 0.50 -				
L2	0.250 BSC					
θ	0°	8° 4°				
θ1	4°	12°	-			
Α	II Dimen	sions in	mm			

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

TSOT26



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199



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