



Product Summary

BV _{DSS}	RDS(ON) Max	I _{D MAX} Ta = +25°С
001/	$75m\Omega @ V_{GS} = -4.5V$	-3.2A
-20V	110mΩ @ V _{GS} = -2.5V	-2.9A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, which makes it ideal for high-efficiency power management applications.

U-DFN2020-6 (Type B)

Bottom View

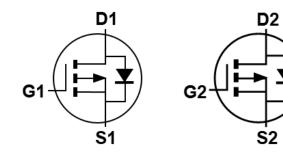
- Load Switch
- Power Management Functions
- Portable Power Adaptors

Features

- PCB Footprint of 4mm²
- Low On-Resistance
- Low Input Capacitance
- Low Profile, 0.6mm Maximum Height
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (Approximate)



Internal Schematic

Ordering Information (Note 4)

Pin1

Part Number	Case	Packaging
DMP2110UFDB-7	U-DFN2020-6 (Type B)	3,000/Tape & Reel
DMP2110UFDB-13	U-DFN2020-6 (Type B)	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

ate Code Key			⊦ YV ●	16 VX		YWX = Y = Yea W = Wa	Date Code ar (ex: 0 = eek (ex: a :		z Repres	ents Week	52 and 53)
Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	9	0	1	2	3	4	5	6	7	8	9	0
Week	1-26			1-26 27-52					53			
Code	A-Z					a	i-Z				z	
Internal Code	Su	ın	Мог	n l	Tue		Wed	Thu	1	Fri		Sat
Code	1	Г	U		V		W	Х		Y		Z



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	Vdss	-20	V
Gate-Source Voltage	Vgss	±12	V
Continuous Drain Current (Note 6) $V_{GS} = -4.5V$	ID	-3.2 -2.6	А
Maximum Continuous Body Diode Forward Current (ls	-1.05	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	Ідм	-15	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	PD	0.82	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Rəja	153	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	PD	1.14	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Rəja	110	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

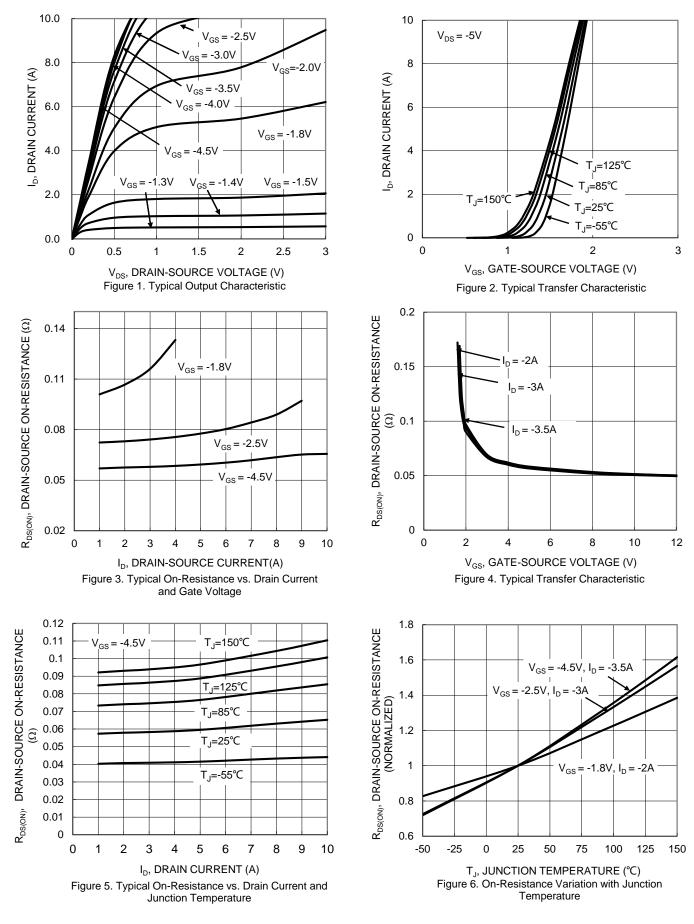
Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	-20	_	—	V	$V_{GS} = 0V, I_{D} = -250 \mu A$
Zero Gate Voltage Drain Current TJ = +25°C	IDSS	—	_	-1.0	μA	Vds = -16V, Vgs = 0V
Gate-Source Leakage	lgss	—	_	±100	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(th)	-0.45	_	-1.0	V	VDS = VGS, ID = -250µA
		—	—	75		$V_{GS} = -4.5V, I_D = -2.8A$
Static Drain-Source On-Resistance	R _{DS(ON)}	—	—	110	mΩ	V_{GS} = -2.5V, I_D = -2.0A
		—	—	168		$V_{GS} = -1.8V, I_D = -1.0A$
Diode Forward Voltage	Vsd	_	_	-1.0	V	Vgs = 0V, Is = -1.0A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	—	443	—	pF	
Output Capacitance	Coss	—	59	—	pF	V _{DS} = -10V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	—	47	—	pF	
Gate Resistance	Rg	—	8.5	—	Ω	VDS = 0V, VGS = 0V, f = 1MHz
Total Gate Charge (V _{GS} = -4.5V)	0	—	6.0	—	nC	
Total Gate Charge (V _{GS} = -8V)	Qg	_	12.7	—	nC	
Gate-Source Charge	Qgs	—	0.6	—	nC	V _{DS} = -4.5V, I _D = -3.0A
Gate-Drain Charge	Qgd	—	1.8	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	4.0	—	ns	
Turn-On Rise Time	t _R	_	3.7	—	ns	V _{DS} = -10V, V _{GS} = -4.5V,
Turn-Off Delay Time	td(OFF)	—	24.5	_	ns	$R_L = 10\Omega, R_g = 6\Omega$
Turn-Off Fall Time	tF	_	9.5	—	ns	
Body Diode Reverse Recovery Time	t _{RR}	—	8.3	—	ns	I _S = -1.0A, dl/dt = 100A/µs
Body Diode Reverse Recovery Charge	Qrr	_	2.0	_	nC	Is = -1.0A, dl/dt = 100A/µs

 Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:



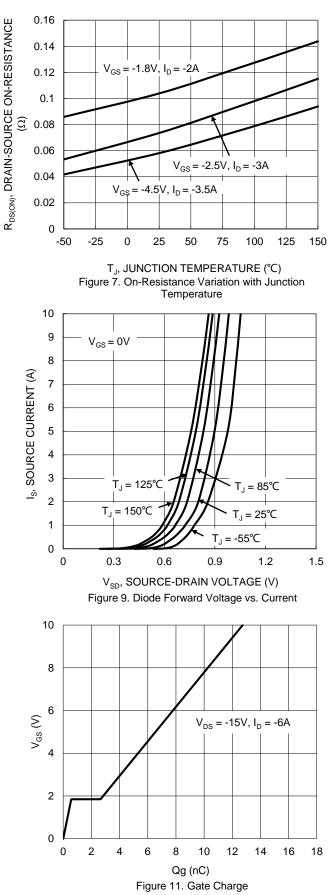
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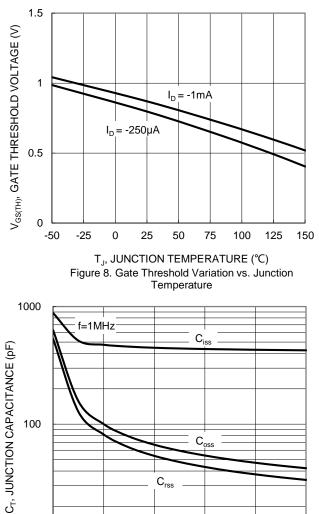


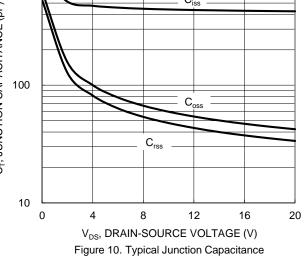
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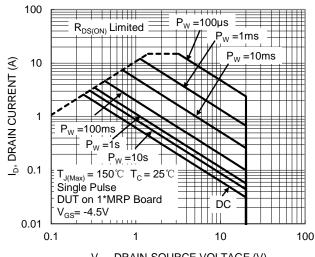


DMP2110UFDB



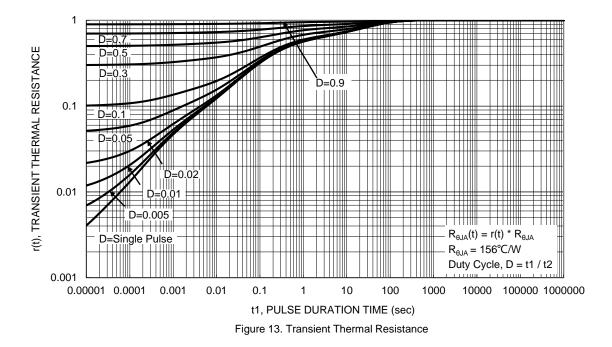






V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area

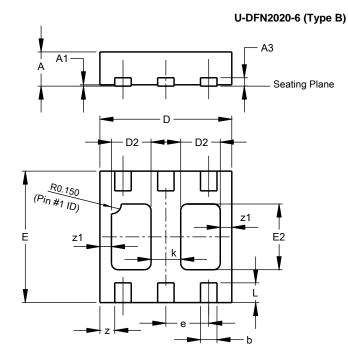






Package Outline Dimensions

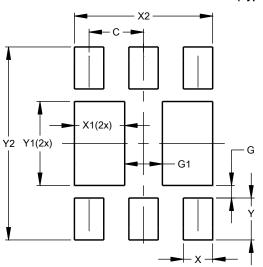
Please see http://www.diodes.com/package-outlines.html for the latest version.



	U-DFN2020-6 Type B							
Dim	Min Max Typ							
Α	0.545	0.605	0.575					
A1	0.00	0.05	0.02					
A3	-	-	0.13					
b	0.20	0.30	0.25					
D	1.95	2.075	2.00					
D2	0.50	0.70	0.60					
е	-	-	0.65					
Е	1.95	2.075	2.00					
E2	0.90	1.10	1.00					
k	-	-	0.45					
L	0.25	0.35	0.30					
z	-	-	0.225					
z1	-	-	0.175					
All	Dimens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



U-DFN2020-6 (Type B)

Dimensions	Value (in mm)		
С	0.650		
G	0.150		
G1	0.450		
Х	0.350		
X1	0.600		
X2	1.650		
Y	0.500		
Y1	1.000		
Y2	2.300		

Document number: DS41738 Rev. 2 - 2



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