

DMP6250SE

60V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _A = +25°C	
-60V	250mΩ @ $V_{GS} = -10V$	-2.1A	
-60 V	300 m Ω @ V _{GS} = -4.5V	-1.9A	

Description

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- DC-DC Converters
- Power Management Functions
- Uninterrupted Power Supply

Features and Benefits

- Low Gate Drive
- Low Input Capacitance
- · Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

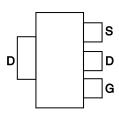
Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Lead Frame.
 Solderable per MIL-STD-202, Method 208 3
- Weight: 0.112 grams (Approximate)

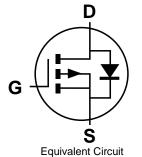
SOT223



Top View



Pin Out - Top View



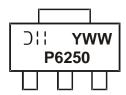
Ordering Information (Note 4)

h-			
Part Number	Qualification	Case	Packaging
DMP6250SE-13	Standard	SOT223	2,500 / Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



Oll = Manufacturer's Marking P6250 = Marking Code YWW = Date Code Marking Y or Y= Year (ex: 7 = 2017) WW = Week (01 to 53)

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Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	-60	V	
Gate-Source Voltage (Note 5)		V_{GS}	±20	V
	$T_A = +25$ °C $T_A = +70$ °C	I _D	-2.1 -1.7	А
Continuous Drain Current (Note 6) V _{GS} = -10V	$T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$	I _D	-6.1 -4.9	А
Maximum Body Diode Continuous Current	Is	-1.8	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	-11	Α	
Single Pulsed Avalanche Current (Note 7) L = 0.1mH	I _{AS}	-12	Α	
Single Pulsed Avalanche Energy (Note 7) L = 0.1mH	E _{AS}	8	mJ	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Dawer Dissination (Note 6)	$T_A = +25^{\circ}C$	6	1.8	W
Total Power Dissipation (Note 6)	T _A = +70°C	P _D	1.1	
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	69	°C/W	
Total Power Dissipation (Note 6) $T_C = +25^{\circ}C$		P _D	14	W
Thermal Resistance, Junction to Case (Note 6)		R _{0JC}	8.7	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

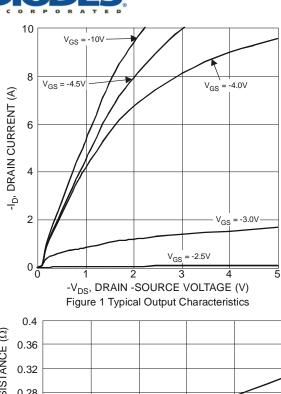
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-60	_	_	V	$I_D = -250 \mu A$, $V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	-1	_	-3	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
			128	250		$V_{GS} = -10V, I_D = -1.0A$	
Static Drain-Source On-Resistance	D		128	250	mΩ	$V_{GS} = -10V$, $I_D = -1.9A$	
Static Diani-Source Off-Resistance	R _{DS(ON)}	_	156	300	11122	$V_{GS} = -4.5V$, $I_D = -0.5A$	
			158	300		$V_{GS} = -4.5V$, $I_D = -1.5A$	
Diode Forward Voltage	V_{SD}	_	_	-1.2	V	$V_{GS} = 0V, I_{S} = -2.0A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	_	551	_	pF	V 20V V 0V	
Output Capacitance	Coss	_	25.7	_	pF	$V_{DS} = -30V, V_{GS} = 0V$ - f= 1MHz	
Reverse Transfer Capacitance	C _{rss}	_	19.1	_	рF	1- 11011 12	
Gate Resistance	R_g	_	12.1	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Q_g	_	4.8	_	nC		
Total Gate Charge (V _{GS} = -10V)	Q_g	_	9.7	_	nC	V 20V I 24	
Gate-Source Charge	Q _{gs}	_	1.5	_	nC	$V_{DS} = -30V, I_{D} = -2A$	
Gate-Drain Charge	Q_{qd}	_	1.6	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	6.3	_	ns		
Turn-On Rise Time	t _R	_	10.3	_	ns	$V_{DS} = -30V, V_{GS} = -10V,$	
Turn-Off Delay Time	t _{D(OFF)}	_	91.4	_	ns	$R_G = 50\Omega, I_D = -1A$	
Turn-Off Fall Time	t _F	_	39.8	_	ns	7	
Reverse Recovery Time	t _{RR}		9.2	_	ns	1. 10.11/1/1/1/1/1/1/1/1/1/1/1/1/1/1/1/1/1/	
Reverse Recovery Charge	Q _{RR}		3.9		nC	-I _S = -1A, di/dt= 100A/μs	

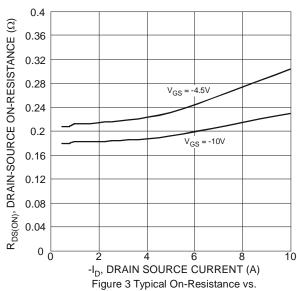
Notes: 5. AEC-Q101 V_{GS} maximum is $\pm 16 \text{V}.$

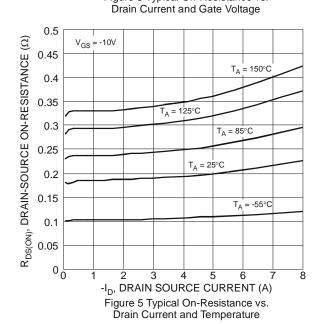
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. For design aid only, not subject to production testing.

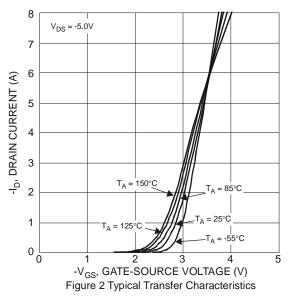
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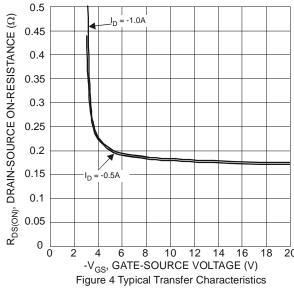


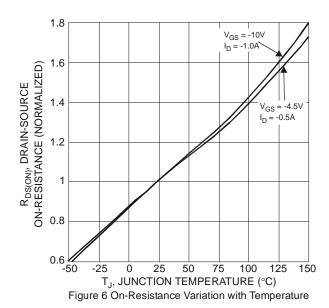














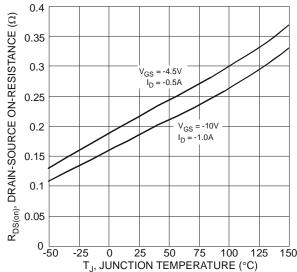
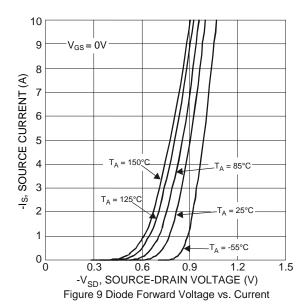
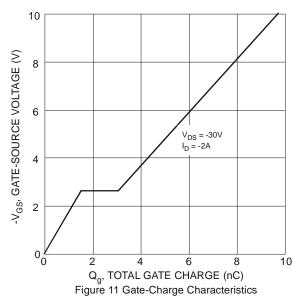


Figure 7 On-Resistance Variation with Temperature





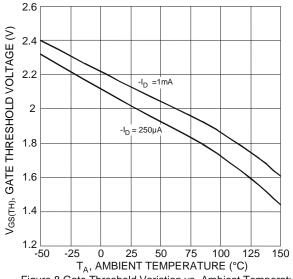
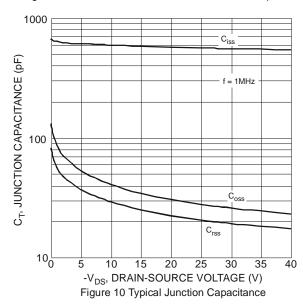
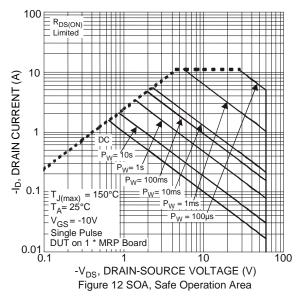
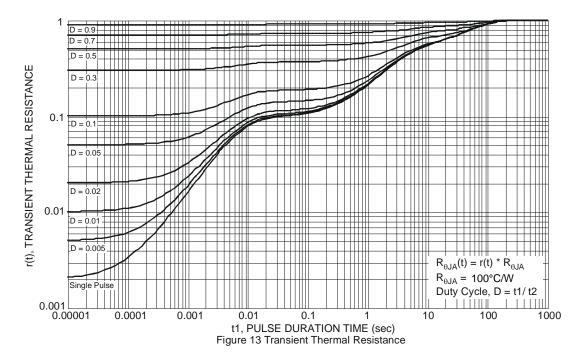


Figure 8 Gate Threshold Variation vs. Ambient Temperature







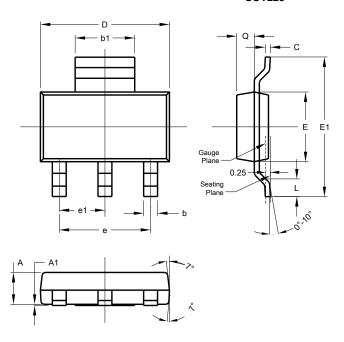




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223

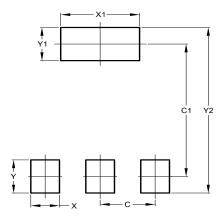


SOT223					
Dim	Min	Max	Тур		
Α	1.55	1.65	1.60		
A1	0.010	0.15	0.05		
b	0.60	0.80	0.70		
b1	2.90	3.10	3.00		
C	0.20	0.30	0.25		
D	6.45	6.55	6.50		
Е	3.45	3.55	3.50		
E1	6.90	7.10	7.00		
е	-	-	4.60		
e1	-	-	2.30		
L	0.85	1.05	0.95		
Q	0.84	0.94	0.89		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Υ	1.60
Y1	1.60
Y2	8.00



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