



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	I _D T _A = +25°C
-30V	80mΩ@ V _{GS} = -10V	-4.0A
-30 V	140mΩ@ V _{GS} =-4.5V	_

Description

This new generation Trench MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance.

Applications

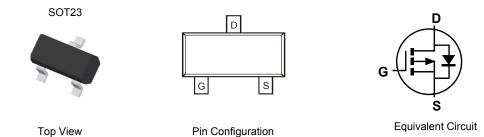
- Power management functions
- Portable Equipment
- Battery Charging

Features and Benefits

- Low On-Resistance
- Fast Switching Speed
- 4.5V Gate Drive Capability
- Thermally Enhanced SOT23 package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approximate)



Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
ZXMP3F30FHTA	Standard	SOT23	3,000/Tape & Reel

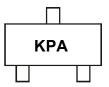
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



KPA = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key

Year	2008		2009	2010		2011	2012		2013	2014		2015
Code	V		W	Х		Y	Z		А	В		С
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	J Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

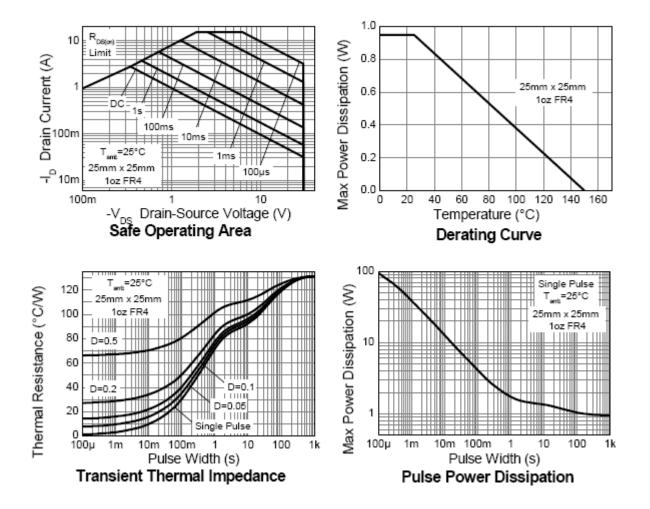


Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Character	ristic	Symbol	Value	Units
Drain-Source Voltage		V _{DSS}	-30	V
Gate-Source Voltage	V _{GSS}	±20	V	
	T _A = +25°C (Note 6)		-3.4	
Drain Current, V _{GS} = -10V	T _A = +70°C (Note 6)	ID	-2.7	Δ.
	T _A = +25°C (Note 5)		-2.8	A
	T _L = +25°C (Note 8)		-4.0	
Pulsed Drain Current (Note 7)		I _{DM}	-15.3	A
Continuous Source Current (Body Di	ode) (Note 6)	Is	-2	A
Pulsed Source Current (Body Diode)	I _{SM}	-15.3	A	

Thermal Characteristics (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Units
	$T_{\rm c} = 125^{\circ}C$ (Note E)		0.95	W
	$T_A = +25^{\circ}C$ (Note 5)		7.6	mW/°C
Total Power Dissipation (Note 5)			1.4	W
Linear Derating Factor	$T_{A} = +25^{\circ}C$ (Note 6)	P _D	11.2	mW/°C
	T _L = +25°C (Note 8)		1.96	W
			15.7	mW/°C
Thermal Desistance Junction to Ambient	(Note 5)	P	131	°C/W
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{ ext{ heta}JA}$	89	C/W
Operating and Storage Temperature Range		T _{J.} T _{STG}	-55 to +150	°C





Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS						•	
Drain-Source Breakdown Voltage	BV _{DSS}	-30		_	V	$V_{GS} = 0V, I_D = -250 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	—	-1	nA	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V _{GS(th)}	-1	—	-3	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
Static Drain-Source On-Resistance (Note 9)				80	mΩ	V _{GS} = -10V, I _D = -2.5A	
Static Drain-Source On-Resistance (Note 9)	R _{DS (ON)}	_		140	11152	V _{GS} = -4.5V, I _D = -1.9A	
Forward Transconductance (Note 9 & 10)	g fs	_	5		S	V _{DS} = -15V, I _D = -3A	
Diode Forward Voltage (Note 9)	V _{SD}	_	-0.8	-1.2	V	V _{GS} = 0V, I _S = -1.7A	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	Ciss	_	370	_	pF		
Output Capacitance	Coss	_	72		pF	V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	38		pF		
GATE CHARACTERISTICS							
Total Gate Charge	Qg		7				
Gate-Source Charge	Q _{gs}		1.2		nC	V _{DS} = -15V, V _{GS} = -10V, I _D = -3A	
Gate-Drain Charge	Q _{gd}	_	1.3	_		$I_D = -3A$	
SWITCHING CHARACTERISTICS (Note 10 & 11	I)						
Turn-On Delay Time	t _{d(on)}	_	1.3				
Rise Time	tr	_	2.6	_	ns	V _{DS} = -15V, V _{GS} = -10V,	
Turn-On Delay Time	t _{d(off)}		49		115	$I_{\rm D}$ = -1A, R _G = 6.0Ω	
Rise Time	t _f	_	22	_			
SOURCE-DRAIN DIODE CHARACTERISTICS (N	Note 11)						
Reverse Recovery Time	trr		14.6		ns	-IS= -1.5A,di/dt=100A/µs	
Reverse Recovery Charge	Q _{rr}		9.5		nC	101.5A,ui/ut-100A/µS	

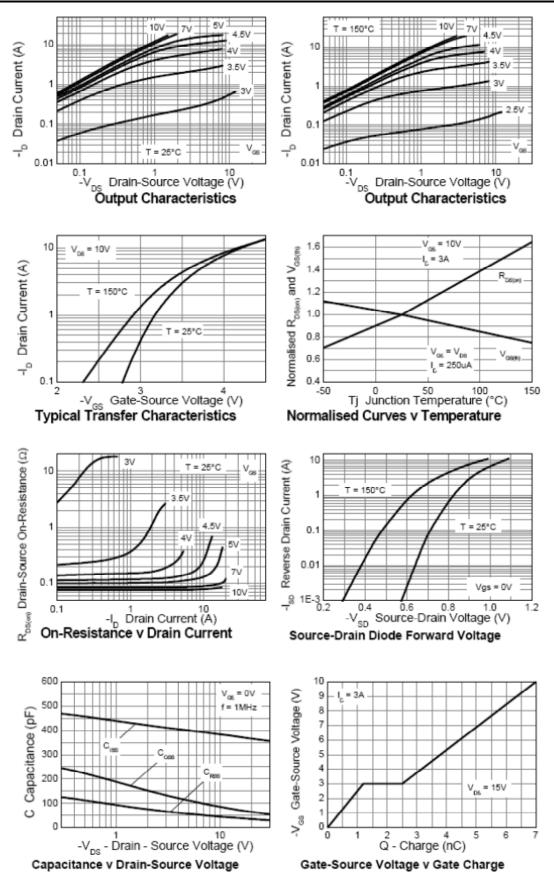
6. Mounted on FR4 PCB measured at t ≤10 sec.
7. Repetitive rating on 25mm x 25mm FR4 PCB, D=0.02, pulse width 300µs – pulse width limited by maximum junction temperature.
8. Thermal resistance from junction to solder-point (at the end of the drain lead).
9. Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%.
10. Output the pulse drain the solution of the solution of the drain lead).

10. Switching characteristics are independent of operating junction temperature.

11. For design aid only, not subject to production testing.

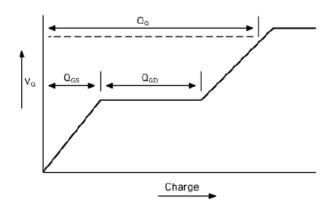


Typical Characteristics

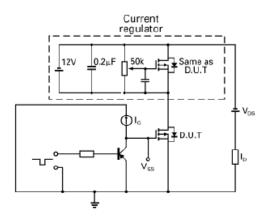




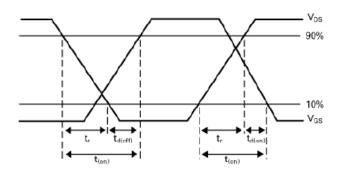
Test Circuits



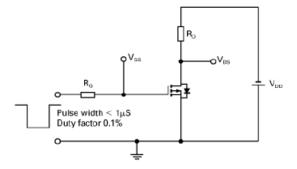
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

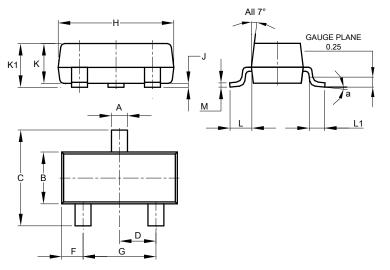


Switching time test circuit



Package Outline Dimensions

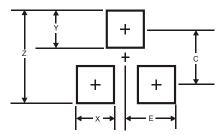
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT23								
Dim	Min	Max	Тур						
Α	0.37	0.51	0.40						
В	1.20	1.40	1.30						
С	2.30	2.50	2.40						
D	0.89	1.03	0.915						
F	0.45	0.60	0.535						
G	1.78	2.05	1.83						
Н	2.80	3.00	2.90						
J	0.013	0.10	0.05						
ĸ	0.890	1.00	0.975						
K1	0.903	1.10	1.025						
L	0.45	0.61	0.55						
L1	0.25	0.55	0.40						
М	0.085	0.150	0.110						
α	8°								
All	Dimens	ions in	mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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