





#### 100V N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub>	I <sub>D</sub> T <sub>A</sub> = 25°C		
100\/	85mΩ @ VGS = 10V	7.7A		
100V	100mΩ @ Vgs = 6V	7.1A		

# **Description and Applications**

This MOSFET features low on-resistance, fast switching and a high avalanche withstand capability, making it ideal for high efficiency power management applications.

- DC-DC Converters
- · Power management functions
- · Disconnect switches
- Motor control
- · Uninterrupted power supply

### **Features and Benefits**

- · Low input capacitance
- Low on-resistance
- Fast switching speed
- "Green" Component and RoHS compliant (Note 1)
- · Qualified to AEC-Q101 Standards for High Reliability

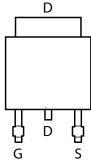
#### **Mechanical Data**

- Case: TO252-3L
- Case Material: Molded Plastic "Green" Molding Compound, UL Flammability Classification Rating 94V-0 (Note 1)
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Matte Tin Finish annealed over Copper leadframe.
   Solderable per MIL-STD-202, Method 208
- Weight: 0.33 grams (approximate)

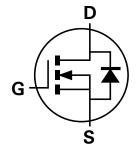




Top View



Pin Out - Top View



Equivalent Circuit

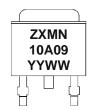
#### Ordering Information (Note 1)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel	
ZXMN10A09KTC	ZXMN10A09	13	16	2,500	

1. Diodes, Inc. defines "Green" products as those which are RoHS compliant and contain no halogens or antimony compounds; further information about Diodes Inc.'s "Green" Policy can be found on our website. For packaging details, go to our website.

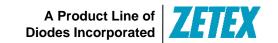
# **Marking Information**

Notes:



ZXMN = Product Type Marking Code, Line 1 10A09 = Product Type Marking Code, Line 2 YYWW = Date Code Marking YY = Year (ex: 09 = 2009) WW = Week (01-52)





#### **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

С	haracteristic		Symbol	Value	Unit
Drain-Source voltage			$V_{DSS}$	100	V
Gate-Source voltage			$V_{GS}$	±20	V
Continuous Drain current V <sub>GS</sub> = 10V		(Note 3) T <sub>A</sub> = 70°C (Note 3) (Note 2)	l <sub>D</sub>	7.7 6.2 5.0	А
Pulsed Drain current V <sub>GS</sub> = 10V		(Note 4)	I <sub>DM</sub>	27	Α
Continuous Source current (Body diode) (Note 3)		I <sub>S</sub>	11	A	
Pulsed Source current (Body diode) (Note 4)		I <sub>SM</sub>	27	A	

#### Thermal Characteristics @TA = 25°C unless otherwise specified

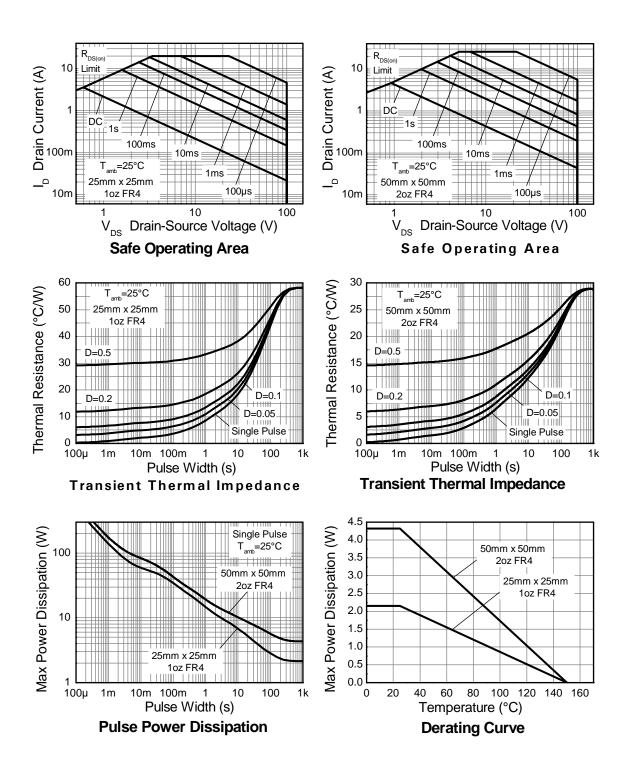
Characteristic	Symbol	Value	Unit		
	(Note 2)		4.31 34.4		
Power dissipation Linear derating factor	(Note 3)	P <sub>D</sub>	10.1 80.8	W mW/°C	
	(Note 6)		2.15 17.2		
	(Note 2)		29		
Thermal Resistance, Junction to Ambient	(Note 3)	$R_{ heta JA}$	12.3	°C/W	
	(Note 6)		58		
Thermal Resistance, Junction to Lead	(Note 5)	$R_{ heta JL}$	1.14	°C/W	
Operating and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C	

#### Notes:

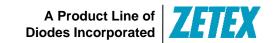
- 2. For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 3. Same as note 2, except the device is measured at  $t \le 10$  sec.
- 4. Same as note 2, except the device is pulsed with D = 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature.
- 5. Thermal resistance from junction to solder-point (at the end of the drain lead).
- 6. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with the high coverage single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.



### **Thermal Characteristics**







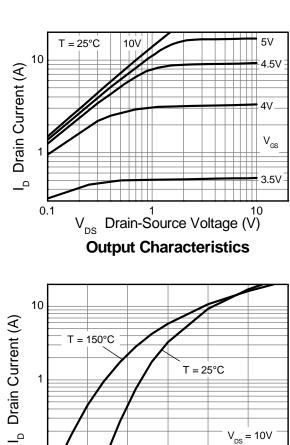
## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test C	ondition
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	100		_	V	$I_D = 250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 100V, V_{G}$	s = 0V
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{D}$	s = 0V
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	2	_	4	V	$I_D = 250 \mu A, V_{DS}$	s = V <sub>GS</sub>
Static Drain-Source On-Resistance (Note 7)	R <sub>DS</sub> (ON)			0.085	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> =	4.6A
Claire Brain Godree On Resistance (Note 1)	KDS (ON)			0.100	22	$V_{GS} = 6V, I_D = 4$	1.2A
Forward Transconductance (Notes 7 & 8)	g <sub>fs</sub>	_	10.7	_	S	$V_{DS} = 15V, I_{D} = 4.6A$	
Diode Forward Voltage (Note 7)	$V_{SD}$	_	0.850	0.950	V	$I_S = 4.7A, V_{GS} = 0V$	
Reverse recovery time (Note 8)	t <sub>rr</sub>	_	40	_	ns	I <sub>S</sub> = 3.0A, di/dt = 100A/μs	
Reverse recovery charge (Note 8)	Q <sub>rr</sub>	_	62	_	nC		
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>	_	1313	_	pF	50)()(	01/
Output Capacitance	Coss	_	83	_	pF	$V_{DS} = 50V, V_{GS} = 0V$ f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	56	_	pF	1 - 1111112	
Total Gate Charge (Note 9)	Qg	_	17.2	_	nC	$V_{GS} = 6V$	
Total Gate Charge (Note 9)	Qg	_	26.0	_	nC		$V_{DS} = 50V$ ,
Gate-Source Charge (Note 9)	Qgs	_	5.6	_	nC	$V_{GS} = 10V$ $I_{D} = 4.6A$	
Gate-Drain Charge (Note 9)	$Q_{gd}$	_	7.6	_	nC		
Turn-On Delay Time (Note 9)	t <sub>D(on)</sub>	_	6.8	_	ns		
Turn-On Rise Time (Note 9)	t <sub>r</sub>	_	5.3		ns	$V_{DD} = 50V, V_{GS} = 10V$	
Turn-Off Delay Time (Note 9)	t <sub>D(off)</sub>	_	27.5		ns	$I_D = 1.0A, R_G \cong 25\Omega$	
Turn-Off Fall Time (Note 9)	t <sub>f</sub>	_	12.3		ns	1	

- 7. Measured under pulsed conditions. Pulse width  $\leq 300 \mu s$ ; duty cycle  $\leq 2\%$
- 8. For design aid only, not subject to production testing.
  9. Switching characteristics are independent of operating junction temperatures.

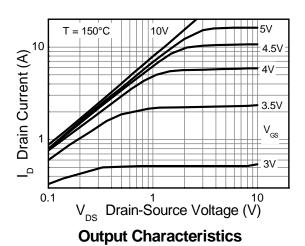


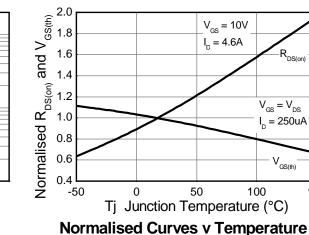
# **Typical Characteristics**

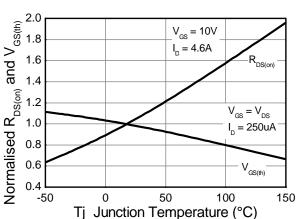


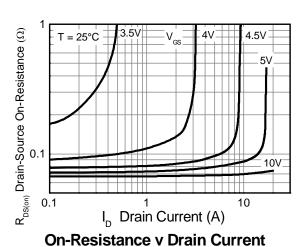
T = 25°C

 $V_{DS} = 10V$ 



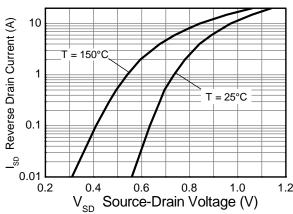






V<sub>GS</sub> Gate-Source Voltage (V)

**Typical Transfer Characteristics** 

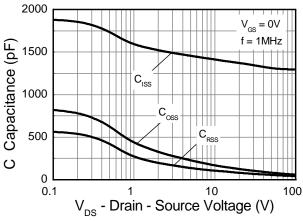


**Source-Drain Diode Forward Voltage** 

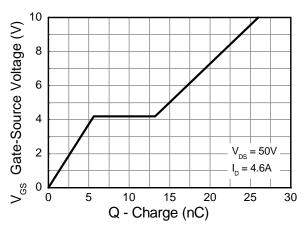
0.1



### **Typical Characteristics - continued**

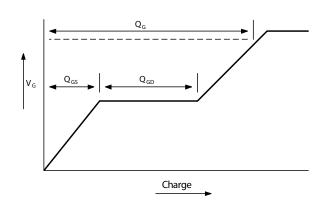


Capacitance v Drain-Source Voltage

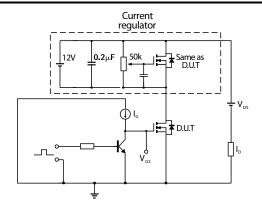


Gate-Source Voltage v Gate Charge

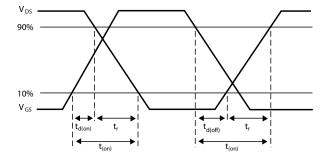
## **Test Circuits**



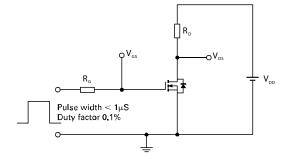
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

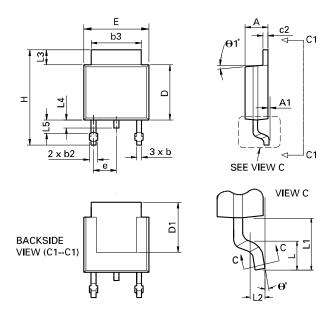


Switching time test circuit



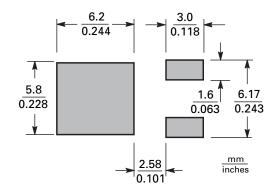


# **Package Outline Dimensions**

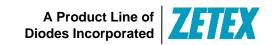


DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min	Max	Min	Max	<u> </u>	Min	Max	Min	Max
Α	0.086	0.094	2.18	2.39	е	0.090 BSC		2.29 BSC	
<b>A</b> 1	-	0.005	-	0.127	Н	0.370	0.410	9.40	10.41
b	0.020	0.035	0.508	0.89	L	0.055	0.070	1.40	1.78
b2	0.030	0.045	0.762	1.14	L1	0.108 REF		2.74 REF	
b3	0.205	0.215	5.21	5.46	L2	0.020 BSC		0.508 BSC	
С	0.018	0.024	0.457	0.61	L3	0.035	0.065	0.89	1.65
c2	0.018	0.023	0.457	0.584	L4	0.025	0.040	0.635	1.016
D	0.213	0.245	5.41	6.22	L5	0.045	0.060	1.14	1.52
D1	0.205	-	5.21	-	θ1°	0°	10°	0°	10°
Е	0.250	0.265	6.35	6.73	θ°	0°	15°	0°	15°
E1	0.170	-	4.32	-	-	=	-	-	-

# **Suggested Pad Layout**







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