



DMTH4007SPS

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C (Note 9)
40V	7.6mΩ @ V _{GS} = 10V	100A

Description

This new generation N-Channel Enhancement Mode MOSFET is designed to minimize $R_{DS(ON)}$, yet maintain superior switching performance. This device is ideal for use in notebook battery power management and loadswitch.

Applications

- Power Management
- DC-DC Converters
- Motor Control

40V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- <1.1mm Package Profile Ideal for Thin Applications</p>
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMTH4007SPSQ)

Mechanical Data

- Case: PowerDl[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)

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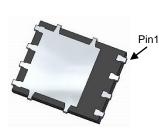
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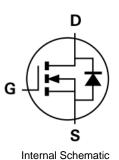
• Weight: 0.097 grams (Approximate)

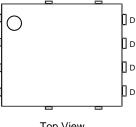


Top View



Bottom View





Top View Pin Configuration

Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH4007SPS-13	PowerDI5060-8	2,500 / Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

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^{2.} See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.



Marking Information



J | = Manufacturer's Marking
H4007SS = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 17 = 2017)
WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	40	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 5)	T _A = +25°C T _A = +70°C	ID	15.7 13.1	А
Continuous Drain Current (Note 6)	$T_{C} = +25^{\circ}C$ (Note 9) $T_{C} = +100^{\circ}C$	ID	100 77	А
Maximum Continuous Body Diode Forward Current (Note 6)		Is	100	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		IDM	200	А
Avalanche Current, L=0.3mH		I _{AS}	20	А
Avalanche Energy, L=0.3mH		E _{AS}	60	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	PD	2.8	W
Thermal Resistance, Junction to Ambient (Note 5)		R _{θJA}	53	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	PD	136	W
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	1.1	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate. 6. Thermal resistance from junction to soldering point (on the exposed drain pad).



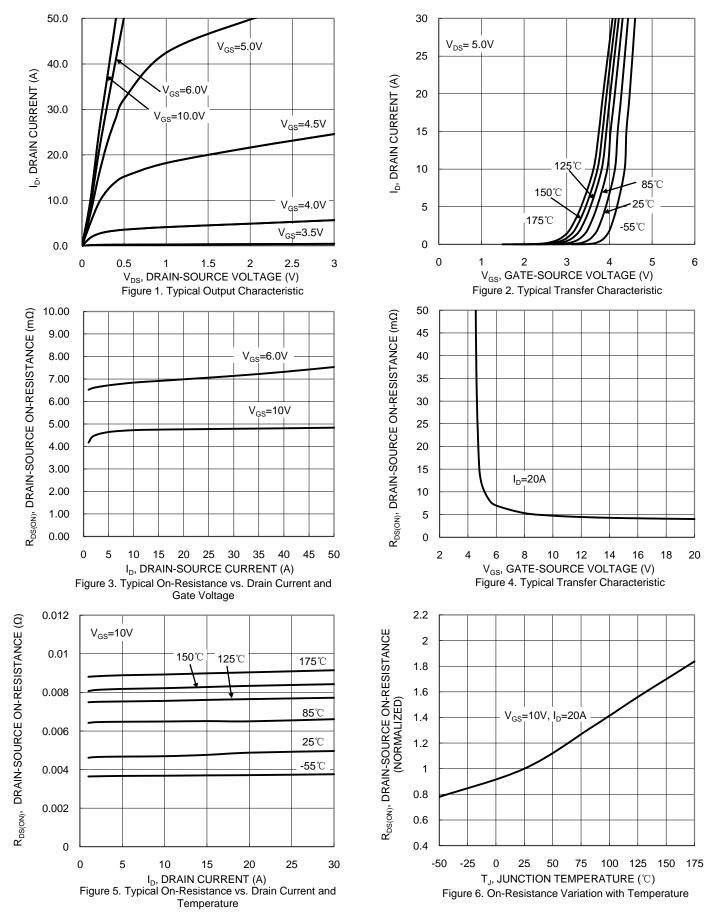
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	—	—	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μA	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)				-		-	
Gate Threshold Voltage	V _{GS(TH)}	2		4	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		4.9	7.6	mΩ	$V_{GS} = 10V, I_D = 20A$	
Diode Forward Voltage	V _{SD}	_	—	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	2,082	—		$V_{DS} = 25V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	Coss	_	790	—	pF		
Reverse Transfer Capacitance	C _{rss}		113	_			
Gate Resistance	Rg		0.46	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg		41.9	—		V _{DS} = 30V, I _D = 20A, V _{GS} = 10V	
Gate-Source Charge	Q _{gs}		10	—	nC		
Gate-Drain Charge	Q _{gd}		11.5	—			
Turn-On Delay Time	t _{D(ON)}	_	7	—		$V_{DD} = 30V, V_{GS} = 10V, \label{eq:VDD} I_D = 20A, R_G = 3\Omega$	
Turn-On Rise Time	t _R	_	11.5	—	ns		
Turn-Off Delay Time	t _{D(OFF)}	_	15.6	—	115		
Turn-Off Fall Time	t _F	_	8.8	_			
Body Diode Reverse Recovery Time	t _{RR}	_	29.9	—	ns		
Body Diode Reverse Recovery Charge	Q _{RR}	_	23	_	nC	I _F = 20A, di/dt = 100A/μs	

Notes: 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing. 9. Package limited.

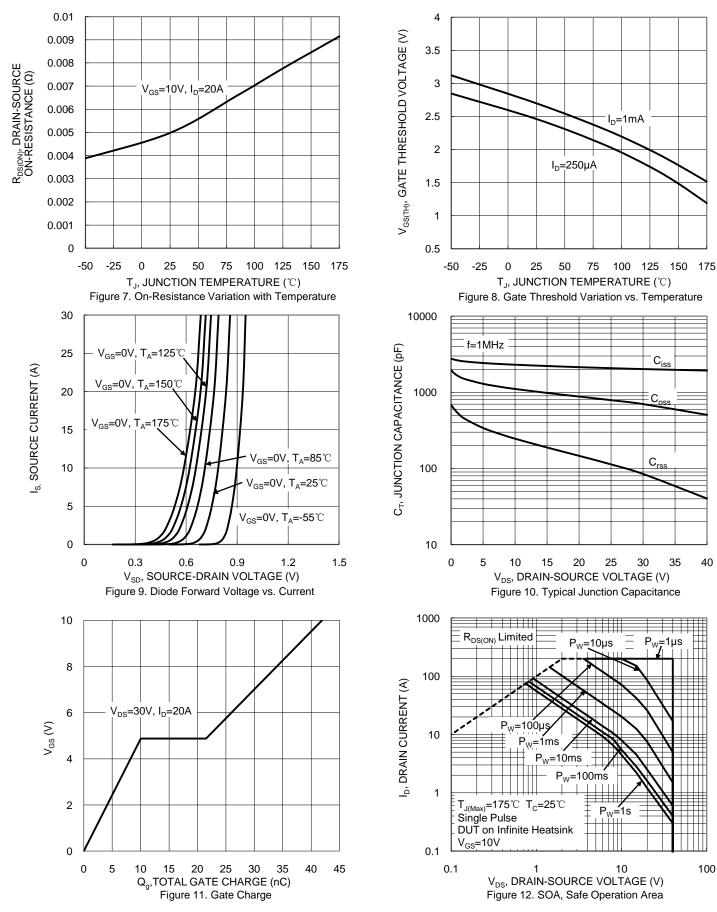


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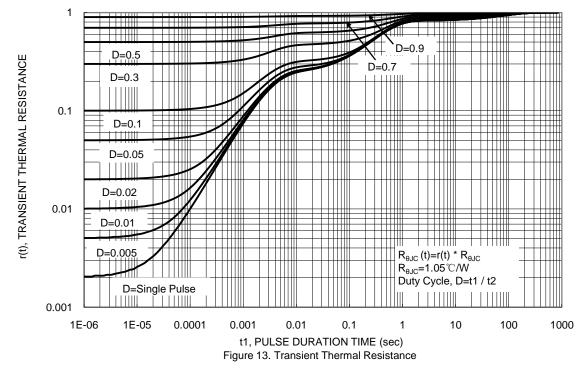






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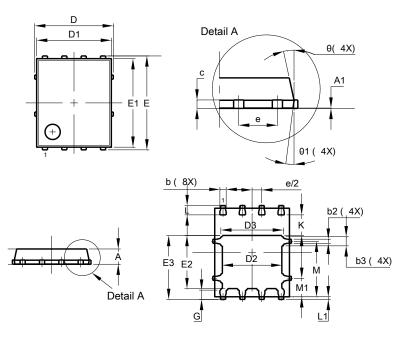




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

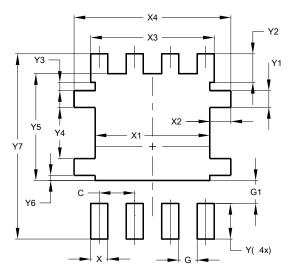


	PowerDI5060-8					
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
A1	0.00	0.05	-			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
С	0.230	0.330	0.277			
D		5.15 BSC				
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
Е	6.15 BSC					
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е		1.27 BSC				
G	0.51	0.71	0.61			
K	0.51	-	-			
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
М	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10°	12°	11°			
Θ1	6°	8°	7°			
Al	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610



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