

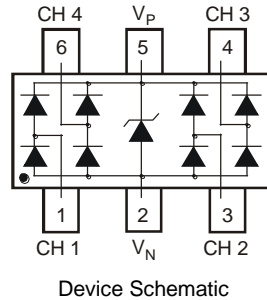
4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Features

- IEC 61000-4-2 (ESD): Air ±15kV, Contact ±8kV
- 4 Channels of ESD Protection
- Low Channel Input Capacitance of 0.85pF Typical
- Typically Used at High Speed Ports such as USB 2.0, IEEE1394, Serial ATA, DVI™, HDMI™, PCI
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q101, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <https://www.diodes.com/products/automotive/automotive-products/>.**
- This part is qualified to JEDEC standards (as references in AEC-Q101) for High Reliability.
- <https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating) Solderable per MIL-STD-202, Method 208③
- Weight: 0.006 grams (Approximate)

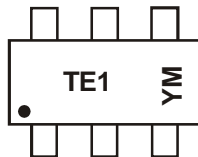


Ordering Information (Note 4)

Part Number	Case	Packaging
D1213A-04S-7	SOT363	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



TE1 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: G = 2019)
 M = Month (ex: 9 = September)

Date Code Key

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019
Code	Y	Z	A	B	C	D	E	F	G

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Operating Supply Voltage	V _P - V _N	6.0	V	—
DC Voltage at Any Channel Input	—	(V _N - 0.5) to (V _P + 0.5)	V	—
Peak Pulse Current	I _{PP}	5.0	A	8/20μs, Per Figure 3
ESD Protection – Contact Discharge	V _{ESD_CONTACT}	±8	kV	IEC 61000-4-2 Standard
ESD Protection – Air Discharge	V _{ESD_AIR}	±15	kV	IEC 61000-4-2 Standard

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	200	mW
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Operating Supply Voltage	V _P	—	3.3	5.5	V	—
Operating Supply Current (Note 6)	I _P	—	—	8.0	μA	(V _P - V _N) = 3.3V
Channel Leakage Current (Note 6)	I _R	—	0.1	1.0	μA	V _P = 5V, V _N = 0V
Reverse Breakdown Voltage	V _{BR}	6.0	—	—	V	I _R = 1mA
Clamping Voltage, Positive Transients	V _{CL1}	—	10.0	—	V	I _{PP} = 1A (Note 7)
Clamping Voltage, Negative Transients	V _{CL2}	—	-1.7	—	V	I _{PP} = -1A (Note 7)
Forward Voltage for Top Diode	V _{FD1}	0.60	0.80	0.95	V	I _F = 8mA, any channel to V _P
Forward Voltage for Bottom Diode	V _{FD2}	0.60	0.80	0.95	V	I _F = 8mA, V _N to any channel
Dynamic Resistance	R _{DYN}	—	0.9	—	Ω	I _{PP} = 1A (Note 7)
Channel Input Capacitance	C _T	—	0.85	1.2	pF	V _{IN} = 1.65V, V _P = 3.3V, V _N = 0V, f = 1MHz

- Notes:
- Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
 - Short duration pulse test used to minimize self-heating effect.
 - Clamping voltage value is based on an 8x20μs peak pulse current (I_{pp}) waveform.
 - Measured from any channel to V_N.
 - Measured from V_P to V_N.
 - For information on the impact of Diodes Incorporated's USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL: http://www.diodes.com/destdtools/appnote_dnote.html.

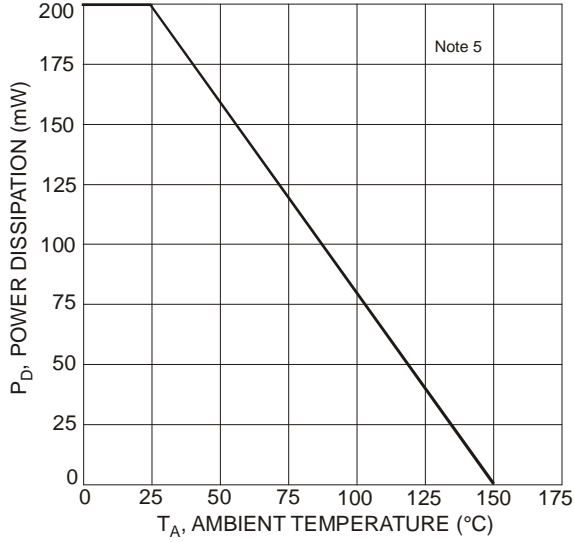


Figure 1 Power Derating Curve

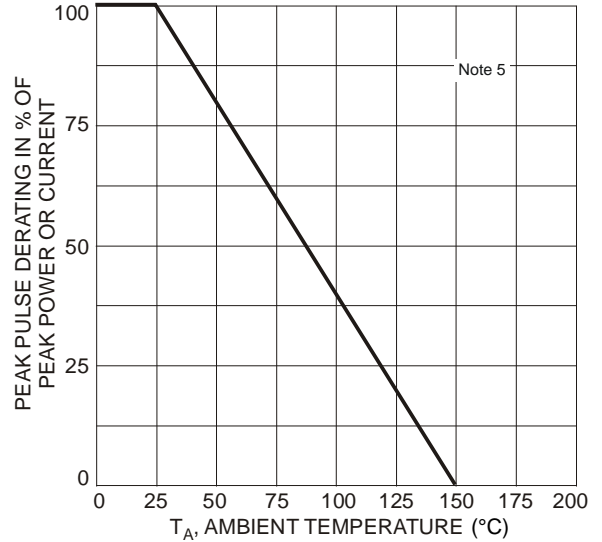


Figure 2 Pulse Derating Curve

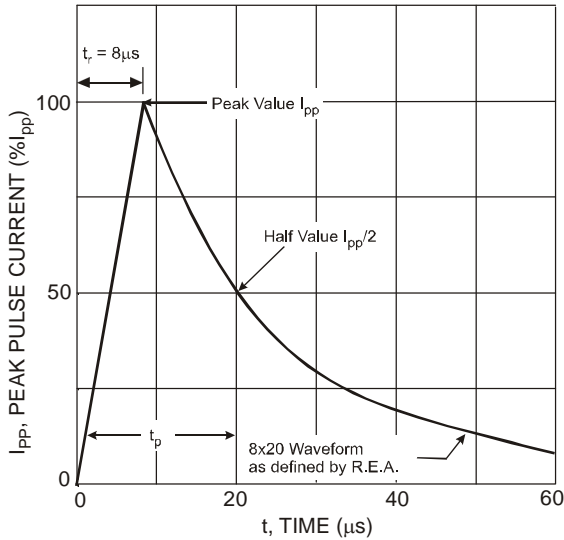


Figure 3 Pulse Waveform

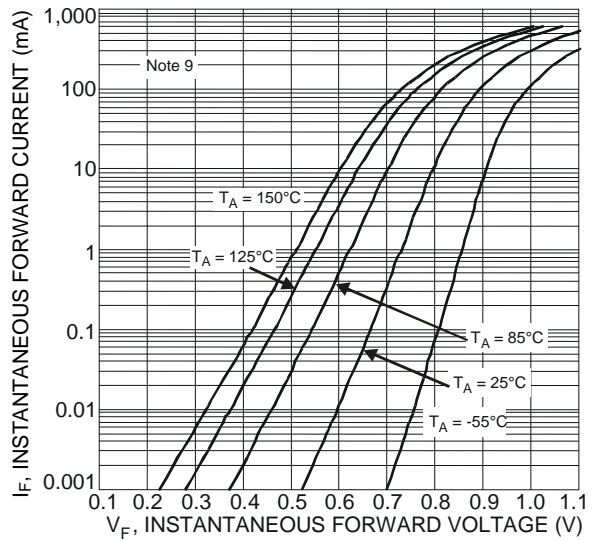


Figure 4 Typical Forward Characteristics

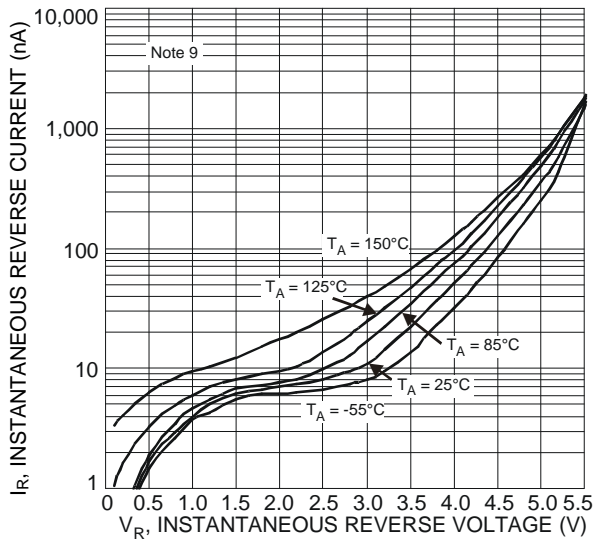


Figure 5 Typical Reverse Characteristics

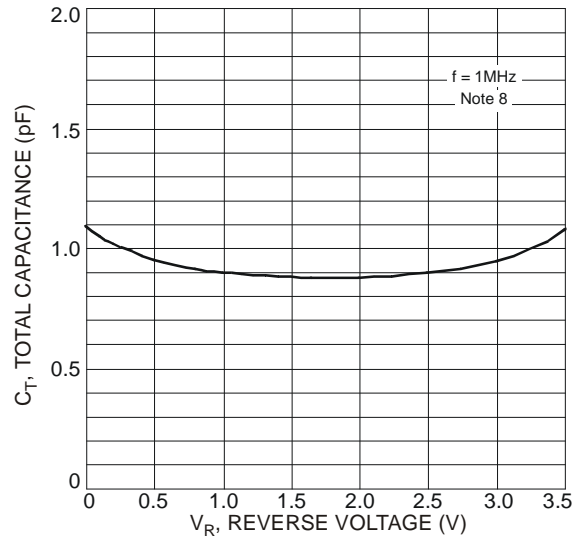
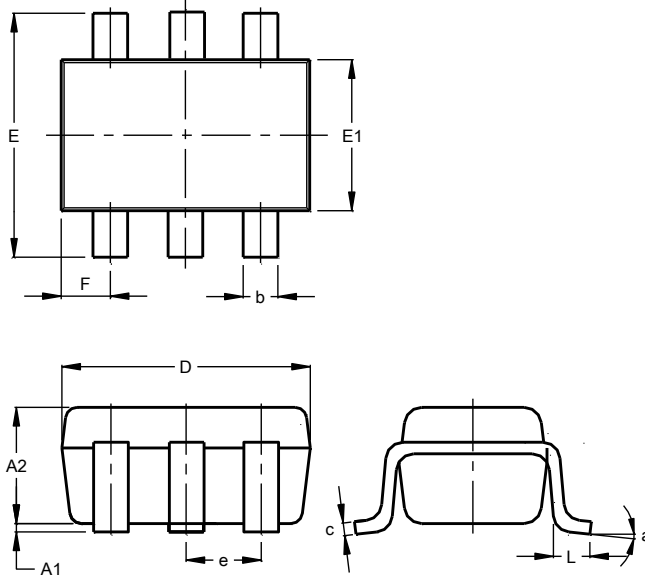


Figure 6 Typical Total Capacitance vs. Reverse Voltage

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT363

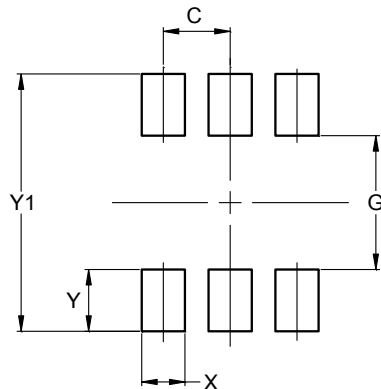


SOT363			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.10	0.30	0.25
c	0.10	0.22	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
F	0.40	0.45	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT363



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.420
Y	0.600
Y1	2.500

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