



DDTC (R1-ONLY SERIES) E

NPN PRE-BIASED TRANSISTOR IN SOT523

Features

- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistor, R1 Only
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DDTC (R1-ONLY SERIES) E is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/guality/product-definitions/

Part Number	R1 (NOM)
DDTC113TE	1kΩ
DDTC123TE	2.2kΩ
DDTC143TE	4.7kΩ
DDTC114TE	10kΩ
DDTC124TE	22kΩ
DDTC144TE	47kΩ
DDTC115TE	100kΩ
DDTC125TE	200kΩ

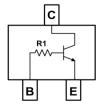
SOT523



Top View

Mechanical Data

- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.002 grams (Approximate)



Device Schematic – Top View

Ordering Information (Note 4)

Part Number	Status	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity Per Reel
DDTC113TE-7-F	Active	Standard	N01	7	8	3,000
DDTC123TE-7-F	Active	Standard	N03	7	8	3,000
DDTC143TE-7-F	Active	Standard	N07	7	8	3,000
DDTC114TE-7-F	Active	Standard	N12	7	8	3,000
DDTC124TE-7-F	Active	Standard	N16	7	8	3,000
DDTC124TEQ-7-F	Active	Automotive	N16	7	8	3,000
DDTC144TE-7-F	Active	Standard	N19	7	8	3,000
DDTC115TE-7-F	Active	Standard	N23	7	8	3,000
DDTC125TE-7-F	Obsolete	Standard	N25	7	8	3,000

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

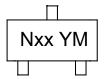
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



Nxx = Product Type Marking Code (See Table in Features) YM = Date Code Marking Y or \underline{Y} = Year (ex: I = 2021)

M or M = Month (ex: 9 = September)

Date Code Key												
Year	2002		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	0			J	K	L	М	N	0	Р	R	S
Mar 4 la	1	E.L.		A		L	l. I	A	0	0.4	Nerr	Dea
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Absolute Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	50	V
Collector-Emitter Voltage	V _{CEO}	50	V
Emitter-Base Voltage	V _{EBO}	5	V
Collector Current	I _{C(MAX)}	100	mA

Thermal Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	150	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	$R_{ heta JA}$	833	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

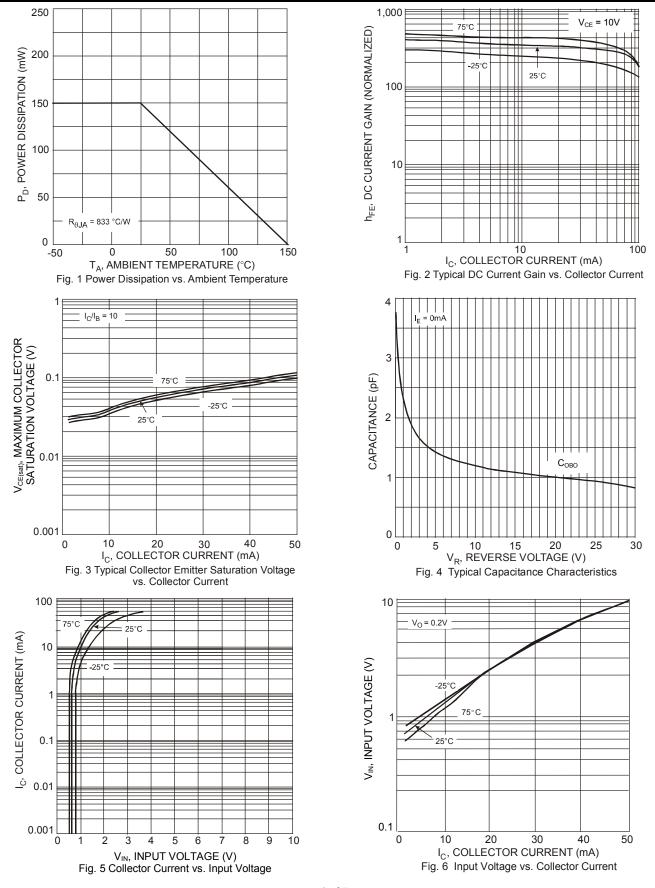
Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	50		_	V	I _C = 50μA
Collector-Emitter Breakdown Voltage	BV _{CEO}	50		_	V	I _C = 1mA
Emitter-Base Breakdown Voltage	BV_{EBO}	5		_	V	I _E = 50μA
Collector Cutoff Current	I _{CBO}	_		0.5	μA	V _{CB} = 50V
Emitter Cutoff Current	I _{EBO}	_		0.5	μA	V _{EB} = 4V
Collector-Emitter Saturation Voltage	V _{CE(sat)}			0.3	V	$\begin{array}{ll} _{C}/l_{B} = 10 \text{mA}/1\text{mA} & \text{DDTC113TE} \\ _{C}/l_{B} = 5 \text{mA}/0.5 \text{mA} & \text{DDTC123TE} \\ _{C}/l_{B} = 2.5 \text{mA}/0.25 \text{mA} & \text{DDTC143TE} \\ _{C}/l_{B} = 1 \text{mA}/0.1 \text{mA} & \text{DDTC114TE} \\ _{C}/l_{B} = 5 \text{mA}/0.5 \text{mA} & \text{DDTC124TE} \\ _{C}/l_{B} = 2.5 \text{mA}/0.25 \text{mA} & \text{DDTC124TE} \\ _{C}/l_{B} = 1 \text{mA}/0.1 \text{mA} & \text{DDTC114TE} \\ _{C}/l_{B} = 1 \text{mA}/0.1 \text{mA} & \text{DDTC115TE} \\ _{C}/l_{B} = 0.5 \text{mA}/0.05 \text{mA} & \text{DDTC125TE} \end{array}$
DC Current Transfer Ratio	h _{FE}	100	250	600	_	I _C = 1mA, V _{CE} = 5V
Input Resistor (R1) Tolerance	ΔR_1	-30		+30	%	—
Gain-Bandwidth Product (Note 6)	f⊤		250	_	MHz	V _{CE} = 10V, I _E = -5mA, f = 100MHz

Notes: 5. Mounted on FR-4 PC Board with minimum recommended pad layout. 6. Transistor only.



Typical Curves – DDTC114TE

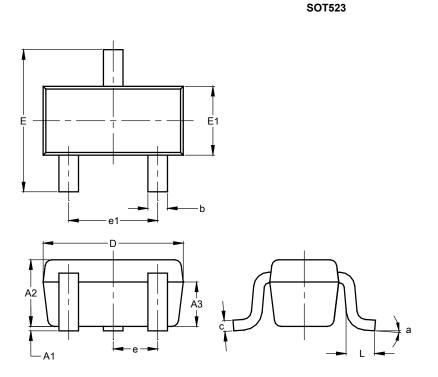


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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

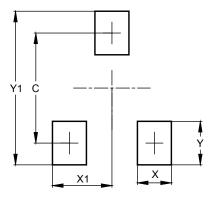


SOT523							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.60	0.80	0.75				
A3	0.45	0.65	0.50				
b	0.15	0.30	0.22				
С	0.10	0.20	0.12				
D	1.50	1.70	1.60				
Е	1.45	1.75	1.60				
E1	0.75	0.85	0.80				
e	0.50 BSC						
e1	0.90	1.10	1.00				
L	0.20	0.40	0.33				
а	0°		8°				
Α	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80



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